Fluidic Packaging of Microengine and Microrocket Devices for High-Pressure and High-Temperature Operation

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Abstract—The fluidic packaging of Power MEMS devices such as the MIT microengine and microrocket requires the fabrication of hermetic seals capable of withstanding temperature in the range 20–600 °C and pressures in the range 100–300 atm. We describe an approach to such packaging by attaching Kovar metal tubes to a silicon device using glass seal technology. Failure due to fracture of the seals is a significant reliability concern in the baseline process: microscopy revealed a large number of voids in the glass, pre-cracks in the glass and silicon, and poor wetting of the glass to silicon. The effects of various processing and materials parameters on these phenomena were examined. A robust procedure, based on the use of metal-coated silicon substrates, was developed to ensure good wetting. The bending strength of single-tube specimens was determined at several temperatures. The dominant failure mode changed from fracture at room temperature to yielding of the glass and Kovar at 600 °C. The strength in tension at room temperature was analyzed using Weibull statistics; these results indicate a probability of survival of 0.99 at an operational pressure of 125 atm at room temperature for single tubes and a corresponding probability of 0.8 for a packaged device with 11 joints. The residual stresses were analyzed using the method of finite elements and recommendations for the improvement of packaging reliability are suggested.

Index Terms—Kovar silicon seal, microengine, microfluidic packaging, Power MEMS.

I. INTRODUCTION

The demand for hermetic seals in many PowerMEMS devices, capable of withstanding high pressures and high temperatures, requires the establishment of novel packaging schemes. Our work is motivated primarily by the needs of the Microengine Project at the Massachusetts Institute of Technology, which is an on-going effort, to build the MEMS gas turbine and rocket engines [1], [2]. These centimeter-sized devices are fabricated using single-crystal silicon by etching wafers and bonding them together to form a three-dimensional structure with internally contained moving parts. The packaging of these devices includes the fluidic, electrical, and sensors connections that interface the engine with the supplies and instruments necessary for operation.

Making fluidic connections is challenging because each joint must be fluid-tight, able to bond to a device made entirely of silicon, and capable of withstanding high temperatures and pressures. In the case of the microengine, all the connections must be capable of holding pressures up to 5 atm; the key requirement here is that these connections must remain gas-tight from room temperature to 600°C. The requirements for the microrocket are more stringent: the pressures experienced are expected to be in the range 125–300 atm in a similar temperature range.

These microscale devices differ significantly in size, design, and materials technology from their macroscale counterparts; hence, traditional packaging techniques cannot be used. At the MEMS scale, there have been many attempts at fluidic packaging by the use of prefabricated tubing and fitting. Gray et al. [3] suggested an approach to integrate microfluidic components into hybrid systems by the successive use of deep reactive-ion etching (DRIE) steps to fabricate accurately sized cross sections for the connecting capillaries. Couplers capable of withstanding pressures up to 34 atm were demonstrated, yet, above 4 atm adhesive-held capillaries were necessary to prevent considerable leakage. Alternate schemes for packaging were proposed by Gonzalez et al. [4] and Meng et al. [5], capable of withstandinpressures of 1.4 and 90 atm, respectively. Such proposed schemes are not compatible with the high pressures and high temperatures required in many PowerMEMS devices, specifically the MIT microrocket device, since they require low-temperature adhesives and perform poorly at the pressures necessary for normal device operation.

Recently, Harrison et al. [6], [7] explored several options for making external fluidic connections to silicon for PowerMEMS devices, including the use of solders, epoxies, brazing, and glass seals. Soldering cannot be used since it cannot withstand the high operating temperatures. Similarly, most epoxies are not able to withstand temperatures of 600°C; even when high-temperature ceramic epoxies were used, they were found to be prone to leakage. A copper-silver braze with an eutectic temperature of 780°C was also considered. This alloy was found to have poor adhesion to silicon and Kovar tubes were brazed to nickel-coated silicon die instead. However, the thermal stresses generated during processing led to fracture of the silicon. By a process of elimination, it was concluded that glass sealing represents the best option for making fluidic connections in the microengine and microrocket devices.

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Glass sealing to Kovar, which is a nickel-cobalt-iron alloy (also known as Nickel Alloy 29–17 [8]) is well understood and has been widely used for decades, and relies on the glass and Kovar having closely matched coefficients of thermal expansion and the formation of a chemical bond. The process of sealing begins by growing a layer of oxide on the metal in a belt furnace using a reducing atmosphere. The growth of the oxide must be slow enough to prevent spalling. The strength and reliability of the joint is enhanced by the incorporation of this oxide into the glass, which forms a true hermetic seal. The primary advantages of this method are that bonds can be formed in many geometric configurations and the seal will remain gas-tight over a range of temperatures. The chief disadvantage is that the materials that can be used are limited by their coefficients of thermal expansion.

The bonding of Kovar to glass for microfluidic applications has also been explored. Sim et al. [9] anodically bonded glass-silicon-glass structure to a Kovar block as part of a bakable pneumatic microvalve development. The Kovar block served as the macro-micro interface, eliminated the use of adhesives, and provided the proper seal to the device. Blom et al. [10] investigated anodic bonding quality of Kovar tubes to Pyrex substrate by varying bond temperature and voltage, and found the method to be adequate at bond temperature as low as 200–300°C. However, glass sealing a Kovar tube to silicon has not previously been thoroughly explored. In this paper, we describe the process for the fabrication of such seals, discuss in detail the factors limiting the quality of hermetic metal-glass-silicon packaging, and make recommendations for improved high-pressure, high-temperature MEMS fluidic packaging.

II. BASELINE PROCESS

The process for making metal-to-glass-to-silicon bonds is derived from the methods used to make matched metal-to-glass bonds. The starting materials are a single-crystal silicon die (with a suitably micromachined orifice), metal (Kovar) tubes, torus (doughnut-shaped) borosilicate glass preforms (which are compacts of powdered glass), and a carbon fixture. The silicon substrate is 1 cm by 1 cm and consists of a 450-μm-thick layer of silicon, with 900-μm diameter hole etched through the center, bonded to a second 450-μm layer of silicon with no hole. The holes were fabricated using DRIE, and consequently have sidewalls with a typically scalloped appearance, with a peak-to-peak roughness of ~0.3 μm. A 10-μm-thick photoresist AZ4620 served as the masking material. DRIE was performed in a Surface Technology Systems (STS) inductively coupled plasma reactor, with alternating cycles of reactive-ion etching in SF₆ plasma and polymer deposition from CF₄ plasma. The silicon die is placed over a carbon flat and the preforms are placed carefully over the orifices. 3813-μm outer diameter tubes having 559-μm inner diameter are then lowered through the holes in the preforms into their respective orifices. Once all the tubes are in place, the fixture is lowered until it rests on the performs, as shown in Fig. 1. The entire assembly is then run through a furnace at elevated temperatures.
(>1000°C) for a few minutes. Specifically, in the baseline process, Corning 7052 glass preforms are used to bond Kovar tubes to bare silicon in a dry nitrogen atmosphere at 1038°C for 15 min. The assembly then passes through cooling chambers for two hours before it is returned to room temperature. The processing temperature exceeds the glass transition temperature and the glass flows to form a seal at the Kovar-silicon junction, as shown in Fig. 2. No evidence of glass flow to the clearance between the orifice and the Kovar was observed following the final stage of the process.

Although used successfully in the packaging of some devices, the seals produced by the baseline process have been observed to be prone to failure due to fracture. Microscopy revealed that the glass contains numerous voids after processing and that pre-cracks, presumably caused by processing-induced residual stresses, occur across the glass-silicon interface (Fig. 3). Moreover, the wetting behavior of the glass on silicon was found to depend sensitively on the details of the processing conditions. All three factors can significantly affect the fracture behavior of the seal. In Sections III–VII, we describe efforts to assess the effects of materials and process parameters on voiding and wetting behavior of the glass, experimental characterization and statistical analysis of the strength of joints in bending and tension, and numerical analysis of residual stresses in glass-silicon composites. Implications for the reliability of packaged microrockets are discussed.

III. VOIDS IN THE GLASS

The single-crystal silicon used in this process is semiconductor-grade and has an extremely low defect density. The glass, however, was found to contain spherical voids with a distribution of sizes, some as large as 20% of the preform thickness, as indicated by scanning electron microscopy of the cross section (Fig. 3). The picture also reveals that most of the voids were concentrated along the bottom of the glass, near the silicon. These voids were found to be closely associated with the fracture surfaces and are presumed to facilitate the nucleation and propagation of cracks. In general, joints that fractured higher in the glass (further from the silicon surface), in the low voids region, were stronger. Joints that failed in the glass near the silicon surface (in the populated voids region) were weaker. This demonstrates the correlation between strength and voids. In order to test the effects of materials and processing conditions on void characteristics, test structures were fabricated in the sessile-drop configuration.

A. Fabrication of Test Specimens

A glass preform, of outer diameter 4 mm, was bonded directly onto the silicon substrate. Five parameters were varied in the experiment: the type of glass used, the surface condition of the silicon, the atmosphere and temperature of the furnace, and the processing time. Three different glasses were used on three different surface conditions of silicon for each batch of test samples. Each batch was run with one variation from the baseline furnace conditions, resulting in a total of nine batches.

The three glasses used in the experiment were Corning 7052, Electroglass 3200 (EN-1), and Fusite K. GBC, Inc., manufactured the Corning and Fusite glass preforms. The surface conditions of the silicon used were the polished side of a wafer, the unpolished side of a wafer, and an oxidized wafer.

Five different atmospheres, three different temperatures, and three different processing times were used in varying the furnace conditions (detailed furnace conditions are described in [6]). In addition to dry and wet nitrogen atmospheres, three other exothermic, propane-based atmospheres were used.

B. Results

The type of glass used was found to affect the size and number of voids within the glass more than any other factor. The Corning 7052 preforms made by GBC showed the least number and smallest voids overall, while the Fusite K preforms, also made by GBC, exhibited the largest number and size of voids. The glass preforms are made from a glass powder, which is packed in a mold and pressed under heat. Examination of the cross-section of several preforms prior to bonding revealed small, evenly distributed, voids in all samples, as shown in Fig. 4. Fig. 5 shows an example of the voids found in each type of glass, with Corning 7052 on the left, EN-1 in the middle, and Fusite K on the right. The EN-1 preforms revealed a microstructure within the glass that is not found in either of the other preforms. It is believed that this is due to the different processing used by Electro-Glass, namely a spray-drying tower that produces a more consistent raw material before the powder is pressed into preforms. These defects, and their coalescence at processing temperatures, are expected to contribute to the voids in the glass seals.

IV. WETTING BEHAVIOR

Wettability is defined as the contact angle between a droplet in thermal equilibrium on a horizontal surface. Depending on the type of surface and droplet medium the droplet may take a variety of shapes. The wetting angle \( \theta \) is given by the angle
between the interface of the droplet and the horizontal surface. The droplet is seemed wetting when \(0 < \theta < 90\) degrees and nonwetting when \(90 < \theta < 180\). \(\theta = 0, 180\) degrees corresponds to perfect wetting and the drop spreads forming a film on the surface. Current measurements of contact angle were collected by SEM images of the preform-silicon-air contact lines.

Although an acute wetting angle is not necessary for two materials to bond, it can enhance the ease of bonding. This was observed to be the case with the glass bonding on Kovar with wetting angles in the range 35–50 degrees. However, the wetting of the glass on all three silicon surfaces was typically very poor, with wetting angles of \(~135\) degrees, leading to reduced contact areas and consequently higher stresses for the same applied load. Better wetting was achieved by the use of a carbon fixture, which presumably promotes better contact between the preforms and the substrate, but the use of this fixture is not always desirable since it inhibits visual and infrared access to the seals, making inspection and analysis difficult. An obtuse wetting angle is undesirable since it can act as a pre-crack and lead to the propagation of circumferential cracks when the seal is loaded during handling or operation [6].

Observation of the behavior of the sessile drop specimens (described in Section III) suggested that the wetting behavior of glass on bare silicon is sensitively dependent on both the glass and the details of the furnace atmosphere; the mechanisms responsible for such behavior are yet to be determined. Other factors such as surface condition of the silicon (i.e., smooth, rough, and oxidized), the furnace temperature, and time in furnace, were not found to have a significant impact on wetting. However, a more robust way to ensure consistently good wetting was developed based on the observation that the glass wets the Kovar tubes well [10]. Thin metal films of Kovar and Ni/Ti bilayers were deposited on silicon substrates and good wetting was consistently obtained as described in Section VI. Both metal films demonstrate very strong adhesion to silicon and no peeling was observed during tests. Ni/Ti is often used in various IC and MEMS process, where the strong adhesion of the Ni to the silicon substrate is insured by the deposition of a 200Å intermediate layer of Ti (e.g., [11]).

V. BENDING TESTS

Bending tests simulate the loads a joint is expected to experience during constrained thermal expansion and contraction of the macro connection assembly, and during handling. For example, the Kovar U-tubes of the \(\mu\)Ockert device proposed by
London et al. [2] (Fig. 6) will thermally expand and contract during the high furnace temperature bond. Knowledge of the maximum attainable bending is required for proper packaging design. Single-tube specimens were fabricated for testing purposes as described as follows.

A. Experimental Details

The baseline silicon substrates and process were used for this test, with the EN-1 glass preform. An MTS mechanical testing machine, with a furnace capable of the temperatures needed (300–1000 K) and position control to an accuracy of 0.1 mm, was used in these experiments. A 1100 N (250 pound) load cell was installed that was capable of measuring forces as small as 0.05 N. The test specimen was clamped to a steel block and held so that the tube protruded horizontally, as shown in Fig. 7. A steel retaining plate with a hole in the center was used to clamp the silicon die to the steel block and to allow the tube of the test specimen to pass through. A loading rod, with a sharp tip, was then lowered into contact with the protruding tube and applied a measured force until the joint failed. The moment was then calculated by multiplying the force applied by the distance along the tube from the joint to the tip of the force rod, which was 2.15 cm.

B. Results

The bending tests revealed a large variation in the response of the joints, as shown in Fig. 8. This plot shows the load applied versus the deflection of the tube for tests at room temperature. While most of the joints failed due to brittle fracture of the glass or silicon, the graph marked with an arrow represents a joint that did not. The failure mode of this sample was yielding of the tube, which eventually prevented any more load from being applied. Fig. 9 is a plot of the failure strength of various joints in the baseline process as a function of the absolute test temperature and indicates the different failure mechanisms observed in these tests. As the temperature is increased, the strength decreases, and the failure mechanism changes from fracture of the glass and silicon to yielding of the Kovar and the glass. At temperatures above 800 K, yielding of the glass became the cause of failure. The mean strength in bending at room temperature was 0.0115 Nm with a standard deviation of 0.0057 Nm, which corresponds to a nominal stress of \( \approx 300 \text{ MPa} \) in the Kovar tube. This value is close to the reported yield strength of Kovar (345 MPa [12]).

VI. TENSION TESTS

Tension tests simulate the loads that a joint is expected to experience from high pressures in the tubes and during handling. To first order, the pressure in the tube \( p \) can be related to the tensile load \( F \) by the expression

\[
F = \pi r^2 p
\]

(1)

where \( r \) is the inner radius of the tube. For the tubes used in this study, (1) implies that a load of 1 N corresponds to a pressure of 15.6 atm.
Fig. 7. Bending test setup.

Fig. 8. Load-displacement response of several specimens in bending at room temperature. The specimens were manufactured using the baseline process.

Fig. 9. Moment applied at failure in bending tests is plotted against the test temperature for specimens prepared using the baseline process.

A. Experimental Details

Single-tube test specimens were prepared for tension tests by the use of EN-1 glass preforms to bond Kovar tubes to metal coated silicon in 800 air/35 propane atmosphere, without the use of carbon fixtures. This manufacturing was performed by Vetro Fuse Inc. Two different metal films were used: 0.1 μm sputtered Kovar and e-beam evaporated bilayers of 0.5 μm Nickel/0.02 μm Ti. The silicon samples were prepared similar to the baseline process with the exception of an additional step that formed the thin metal layer. Following diesaw the samples were placed in a masking chuck, with circular holes, that formed annuli around the orifices, once the deposition was completed. The diameter of the deposited layers varied from 1.5 to 3.45 mm. (All other factors, such as the time of processing and the temperature of the furnace, were identical for all samples.)

The specimens were placed in a fixture consisting of a base and top set screw designed to allow tension to be applied to the joint without the introduction of a moment (Fig. 10). The base of the fixture consisted of a recessed area to receive the silicon base; a drilled top plate is then lowered over the Kovar tube and attaches at four points around the recessed area. The Kovar tubes are then slipped into a corresponding axial hole in the top piece of the fixture, and a setscrew is tightened on the Kovar tube, allowing tension to be applied to the joint. To prevent shear stresses in the specimen the recessed area was designed to allow free movement of the device prior to loading, which began only after the silicon surface reached the top plate. Alignment was checked before each run and adjusted by moving the test sample on the steel block. The fixture was designed to fit on a servohydraulic mechanical testing machine. Testing consisted of subjecting the sample to a constant displacement rate of 10 μm/s at room temperature.

B. Results

Fig. 11 shows the largest load sustained by each joint prior to failure for several sets of specimens. The scatter in the data is indicative of the brittle nature of the material being tested. (For comparison, the mean strength of the specimens prepared by the baseline process is 8.6±5.1 N. For specimens prepared by using EN-1 glass to bond Kovar to bare silicon in [6], the maximum mean strength was 13.1 ± 3 N [6]). It is important to note that the coating material and coating diameter have no significant
effect on the strength; in particular, the use of a coating does not degrade the strength compared to bare silicon. The locus of fracture is similar in all specimens, regardless of the presence or absence of any metal coating, and includes fracture in the glass, in silicon, and along the interfaces. This suggests that the adhesion strengths of silicon/glass, glass/metal, and metal/silicon are comparable. Further, as shown in Fig. 12, no correlation was found between the maximum sustained load and the wetting angle in these samples. However, the wetting angle alone does not determine the actual contact area since numerous voids are present at the glass/metal and glass/silicon interface.

Next, the maximum loads for one particular set (with Kovar coating of diameter 2.0 mm) were fit to a Weibull probability distribution [13]. The probability of failure, $P_f$, is related to the applied load, $F$, by the expression

$$P_f = 1 - \exp\left(-\left(\frac{F}{F_0}\right)^{m}\right)$$

(2)

where $F_0$ is a reference load and $m$ is the Weibull modulus. By fitting the data to (2), the Weibull modulus was determined to be 5 and the reference load 20.1 N, as shown in Fig. 13. The coefficient of regression was 0.98, indicating a good fit.

Fig. 13 has important implications for the design of single seals and for the reliability of packaged devices. Whereas the best performance among the tested specimens was a maximum load of $\sim$27 N, the low value of the Weibull modulus implies that only a significantly lower load can be applied to ensure a low probability of failure. Furthermore, a single packaged device contains $N$ seals, with $N = 11$ for the microrocket device; the failure of even one seal can cause the failure of the entire device. These joints are spaced sufficiently apart that the fracture of any one seal is expected to be independent of the fracture of any other. Therefore, it follows that, if $(1 - P_f)$ is the probability of survival of a single joint, then $(1 - P_f)^N$ is the probability of survival of the package. For operation pressures of 125 atm ($\sim$8 N), Fig. 13 indicates that $P_f = 0.01$ and the probability of survival of a package with eleven joints is 0.90. However, at pressures of 300 atm ($\sim$19 N), $P_f = 0.55$ and the probability of survival of the package is only $\sim 10^{-4}$. Even if $N = 3$, as in an alternate design, the probability of surviving 300 atm is still <0.1.

It is important to note that these conclusions are valid only at room temperature. Similar tests at elevated temperatures, and under different loading conditions (especially bending), are necessary to map the entire operating space for these joints.

C. Fractography

A micrograph of the silicon die after fracture is shown in Fig. 14. The fracture occurs along the glass-silicon interface as well as in the silicon and the glass. The river pattern indicated
by the arrow is usually interpreted to be the result of crack propagation on parallel crystallographic planes [14]. This suggests that the crack propagates radially during the fracture process. Fig. 15 shows the tube and glass bead after fracture; a portion of the silicon substrate is seen on the top surface. A few tests were interrupted immediately following fracture initiation, as indicated by a drop in the load. Fig. 16 shows the top view of one such sample; it is clear that fracture proceeds by the activation of multiple cracks, in the glass, in the silicon, and along the glass-silicon interface, in these specimens. Large voids are clearly visible in the glass.

In one specimen, the glass was dissolved using HF after processing, without subjecting the tube to tensile loads. No pre-cracks were observed in the substrate (Fig. 17).

VII. RESIDUAL STRESSES

The results of Section VI show that, even with the process optimized for small voids, good wetting and no pre-cracking, the
strength is insufficient for the full range of pressures required. To gain insight into the residual stress distribution, and to suggest methods for strength improvement, finite element calculations were performed.

Processing-induced thermal residual stress result from a mismatch in the coefficients of thermal expansion ($\alpha$). The glass and Kovar have closely matched values of $\alpha$ of $\sim 7.0 \times 10^{-6}$ K$^{-1}$ in the temperature range 20–700$^\circ$C [13], while the corresponding value for silicon is $\sim 3.5 \times 10^{-6}$ K$^{-1}$. The softening temperature of the glass is $\sim 700^\circ$C and it is reasonable to expect the glass to flow and accommodate all stresses until it reaches this temperature. Between 700$^\circ$C and room temperature, the differential contraction of the glass and silicon will induce stresses in both materials with a magnitude given, to first order, by

$$\sigma_{\text{Glass}} = \frac{E_{\text{Glass}}(\alpha_{\text{Glass}} - \alpha_{\text{Si}})(T_{\text{Soft}} - T_{\text{RT}})}{1 - \nu_{\text{Glass}}}$$  \hspace{1cm} (3)$$

where $\sigma$ is the stress, $E$ is Young’s modulus, $\nu$ is Poisson’s ratio, $T_{\text{Soft}}$ is the absolute softening temperature (973 K), and $T_{\text{RT}}$ is the absolute room temperature (300 K). For typical values ($E_{\text{Glass}} = 70$ GPa, $\nu_{\text{Glass}} = 0.3$), (3) implies a maximum tensile stress of $\sim 200$ MPa in the glass.

This analysis is valid only in regions away from the edges of the glass bead. The full stress field was determined using the method of finite elements. An axisymmetric model was created using a general-purpose code (ABAQUS/CAE, Hibbitt, Karlsson, and Sorensen, Inc.). Both materials were assumed to be isotropic, homogeneous, elastic solids, with $E_{\text{Si}} = 160$ GPa and $\nu_{\text{Si}} = 0.2$. Any stress relief due to the presence of voids was ignored in the analysis; the predictions of the model are, therefore, worst-case estimates. As shown in Fig. 18, the stress field in the vicinity of the glass-silicon-air interface is complicated. The maximum tensile stresses in the glass of $\sim 200$ MPa occur in the region indicated by the arrow in the figure, consistent with the locations of pre-cracks in Fig. 3.

Even when the stress is insufficient to cause pre-cracks, as in Fig. 17, the residual stresses act as a pre-load, and lower the strength in tension. Equation (3) suggests that the optimal strategy for minimizing residual stresses is to match the coefficients of thermal expansion of the metal and glass to silicon. Pyrex is an attractive candidate, although over a restricted temperature range (20 to 300$^\circ$C). An alternate strategy is to choose glasses such that $\alpha_{\text{Si}} < \alpha_{\text{Glass}} < \alpha_{\text{Kovar}}$, rather than matched to either Kovar or silicon.

**VIII. CONCLUSION**

The baseline process used in the glass sealing of Kovar tubes to silicon for making fluidic interconnects for high-temperature, high-pressure operation was described. The effects of processing parameters on voiding and wetting behavior of the glass were qualitatively discussed, and experimental characterization of the strength of single-tube joints in bending and in tension was described. A numerical analysis of the residual stresses in glass-silicon composites was presented. Our main conclusions are as follows.

1) The volume fraction of voids within the glass is primarily dependent on the quality of the glass preform material.

2) The wetting behavior of glass on bare silicon is sensitively dependent on the details of the furnace atmosphere.
 However, this dependence can be negated by the use of metal-coated silicon substrate. Good wetting was consistently achieved using such samples in a variety of furnace conditions, obviating the need for carbon fixtures during processing.

3) Bending tests indicated that the mechanism of failure is fracture at room temperature and yielding of the Kovar and the glass at elevated temperatures.

4) The mean strength in tension at room temperature was not significantly affected by the presence of metal coatings. In addition, there was no apparent correlation between the strength and the wetting angle in the range of 35°–55°.

5) For one set of 27 nominally identical samples tested in tension at room temperature, the Weibull modulus was found to be 5 and the reference load 20.1 N. This implies that a packaged microrocket device with 11 equally critical connections can survive 125 atm of pressure with a probability of 0.9. At 300 atm, however, the probability of survival of the package is unacceptably low (∼10⁻⁴).

6) Numerical analysis of the residual stresses indicate that the glass in the vicinity of the glass-silicon-air interface experiences the maximum tensile load, consistent with pre-crack patterns observed in several unstressed seal.

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