Multistage Frequency-Hopping Assisted Ultra-Wideband Multiple-Access Communications

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Abstract—Ultra-WideBand (UWB) systems having a bandwidth on the order of a Gigahertz have recently received wide attention both in the USA and in Europe. In this contribution a novel UWB multiple access scheme based on the well-known family of Frequency-Hopping Multiple Access (FHMA) techniques is proposed, where the associated FH is implemented using MultiStage Frequency Hopping Multiple Access, or MS/UWB FHMA. We highlight the principles of the proposed MS/UWB FHMA communication system, investigate the associated spectrum assignment, the FH strategy, the applicable signal detection schemes as well as the achievable Bit Error Rate (BER) performance. Our study shows that MS/UWB FHMA is capable of supporting an extremely high number of users, while employing relatively simple receivers.

I. INTRODUCTION

The last decade has witnessed a paradigm shift from low-bitrate narrowband wireless systems to broadband wireless systems. This trend continued with the recent emergence of the family of Ultra-WideBand (UWB) systems [1] - [5], also referred to as impulse radio or time domain radio systems, which have received considerable attention both in the USA and in Europe. The first UWB propagation results were reported in [1]. It was shown in [1] - [3] that UWB signals do not suffer from severe fading, since they benefit from the presence of a huge number multipath components. Therefore the performance of UWB systems is robust against the hostile channel-induced impairments of conventional narrower bandwidth systems.

In this contribution an alternative multiple access scheme is proposed, which allows each user to access an ultra-wideband. The proposed UWB multiple access scheme is based on multistage Frequency-Hopping (FH) invoked in the context of noncoherent *M*-ary Frequency Shift Keying (MFSK). Hence we refer to the proposed scheme as MultiStage Ultra-WideBand Frequency-Hopping Multiple Access and use the abbreviation of MS/UWB FHMA. In this contribution we highlight the novel concept of MS/UWB FHMA. The spectrum assignment and FH strategy, the system design parameters as well as the Bit Error Rate (BER) performance associated with different detection philosophies are studied. Our investigations will demonstrate that MS/UWB FHMA constitutes a high-efficiency multiple access scheme capable of supporting an extremely high number

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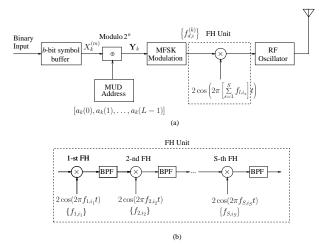


Fig. 1. Transmitter block diagram of the MS/UWB FHMA system using MFSK modulation.

of users with the aid of relatively simple receivers previously proposed in the literature[6] - [8].

II. SYSTEM DESCRIPTION

A. Transmitter

The operation of the transmitter associated with the MS/ UWB FHMA system studied may be understood by referring to Fig.1. During a signalling interval of T_s duration, b message bits of the kth user having a bit rate of R_b are loaded into a b-bit buffer. We denote this b-bit symbol as $X_k^{(m)}$, where $X_k^{(m)} \in [0, M = 2^b)$. Hence, the $M = 2^b$ -ary output symbol rate is $R_s = R_b/b$. Let $R_h = 1/T_h$ be the FH rate, where T_h represents the duration of the FH tones or chips. The ratio of $L = T_s/T_h$ determines, whether slow FH (SFH) - where L < 1- or fast FH (FFH) - where $L \ge 1$ - is invoked. Specifically, if L < 1, several $M = 2^b$ -ary symbols are transmitted in a FH-interval of T_h duration, activating one out of M legitimate frequencies of an MFSK scheme. By contrast, if $L \geq 1$ and integer, an $M=2^b$ -ary symbol activates a particular MFSK tone only for the duration of $T_h = T_s/L$ seconds and then hops to a further (L-1) other legitimate frequencies of the MFSK scheme used.

In our proposed system we assume that $L=T_s/T_h$ is an integer larger than one, i.e. that FFH is employed [6]. In order

to distinguish between different users, each user is assigned a user-signature FH pattern also referred to as an address code [6] - [8]. Let the address code of the kth user, $1 \leq k \leq K$, be expressed as $\mathbf{a}_k = [a_k(0), a_k(1), \ldots, a_k(L-1)]$, where $a_k(l) \in GF(M), \ l = 0, 1, \ldots, L-1$ and GF(M) denotes a Galois field over $M = 2^b$. Then, the transmitted symbol $X_k^{(m)}$ is signalled by invoking the kth user's address code, which can be expressed as

$$\mathbf{Y}_k = [y_k(0), y_k(1), \dots, y_k(L-1)] = X_k^{(m)} \cdot \mathbf{1} \oplus \mathbf{a}_k,$$
 (1)

where 1 is a unit vector of length L, $y_k(l)$ for $1 \leq l < L$ are elements of GF(M), and \oplus denotes modulo-M or modulo- 2^b addition, as seen in Fig.1. Obeying the user signature, the components of \mathbf{Y}_k are passed serially to an MFSK modulator, where they are converted to the MFSK tones $\mathbf{F}_m^{(k)} = [f_{d,0}^{(k)}, f_{d,1}^{(k)}, \dots, f_{d,L-1}^{(k)}]$ referred to as chips of duration T_h . Following MFSK modulation FH is activated obeying the user-specific FH pattern derived using the multistage FH scheme to be detailed in the next Subsection during our forthcoming discourse. Finally, the modulated signal is transmitted using the RF mixing stage. During the time interval $t \in [iT_s, (i+1)T_s]$, the kth user's transmitted signal can be expressed as

$$s_k^{(i)}(t) = \sum_{l=0}^{L-1} \sqrt{\frac{2P}{L}} p_{T_h}(t - iT_s - lT_h) \cdot \cos\left(2\pi \left[\sum_{s=1}^{S} f_{s,i_s,l}^{(k)} + f_{d,l}^{(k)}\right] t + \psi_{i,l}^{(k)}\right), (2)$$

where P is the transmitted power, $p_{T_h}(t)$ is the pulse-shaped waveform associated with a chip defined over the interval $[0,T_h)$, S represents the number of FH stages seen in Fig.1(b), and $f_{s,i_s,l}^{(k)}$ represents the base frequency corresponding to the ith transmitted symbol in the sth FH stage, $s=1,2,\ldots,S$ during the lth chip interval. Finally, $\psi_{i,l}^{(k)}$ represents the phase introduced by the MFSK modulation and FH procedures.

The concept of multistage FH can be augmented with the aid of Fig.1(b) and Fig.2. As an example, we characterize a three-stage FHMA system associated with 16-ary MFSK modulation, which will be further detailed during our forthcoming discourse. In the MS/UWB FHMA system the total bandwidth of W_{ss} is first uniformly divided into m_S number of frequency bands, each having a bandwidth of $W_{FH}^S = W_{ss}/m_S$ - which is referred to as the Stage-S subband - and each subband is correspondingly assigned a frequency from the Stage-S frequency set expressed as $\mathbf{F}_S = [f_{S,0}, f_{S,1}, \dots, f_{S,m_S-1}].$ In the example of Fig.2 this corresponds to Stage-3 at the top. Then, the Stage-S subband is further divided, again, uniformly into m_{S-1} number of frequency bands, where each band has a bandwidth of $W_{FH}^{S-1}=W_{FH}^{S-1}/m_{S-1}$. We refer to W_{FH}^{S-1} as the Stage-(S-1) subband, which is Stage-(S-1) in the stage-(S-1) subband, which is Stage-(S-1) subband, which is Stage-(S-1) in the stage-(S-1) subband, which is Stage-(S-1) subband. 2 in the example of Fig.2. Following the procedure used at Stage-S, each of the Stage-(S-1) subbands is assigned a frequency from the Stage-(S-1) frequency set expressed as

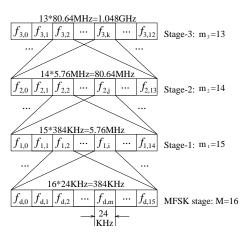


Fig. 2. Example of the spectrum allocation of a 3-stage (L=3) FH SSMA system using 16-ary MFSK modulation, where $m_1=15,\ m_2=14,\ m_3=13$ and $R_b=32Kb/s$.

 $\mathbf{F}_{(S-1)} = [f_{(S-1),0}, f_{(S-1),1}, \ldots, f_{(S-1),m_{(S-1)}-1}]$. According to the above rules, finally, the Stage-2 subband is uniformly divided into m_1 number of frequency bands, each having a bandwidth of $W^1_{FH} = W^2_{FH}/m_1$, which are referred to as the Stage-1 subbands. At the same time, each of the Stage-1 subbands is assigned a frequency from the Stage-1 frequency set of $\mathbf{F}_1 = [f_{1,0}, f_{1,1}, \ldots, f_{1,m_1-1}]$.

Based on the above bandwidth assignment, the total number of FH bands is $M_t = \prod_{s=1}^{S} m_s$. As shown in Fig.2, each FH band consists of M tones associated with MFSK modulation. We referred to this processing stage as the MFSK stage. Since the FH rate is R_h , the frequency spacing between two adjacent MFSK tones should be no less than R_h , which is an often-used system constraint in FFH [6]. Consequently, the total system bandwidth W_{ss} and the FH rate R_h must obey the relationship of $W_{ss} \geq \prod_{s=1}^{S} m_s \times MR_h$. Since we have $R_h = LR_s = LR_b/b$, where R_s and R_b are the symbol rate and bit rate, respectively, the relationship of $W_{ss} \geq \prod_{s=1}^{S} m_s \times MLR_b/b$ holds. We assume that the FH rate at each FH stage is $R_H=1/T_H$, i.e. the frequencies at a given FH stage $s, \ s=1,2,\ldots,S$ hop $\omega=T_s/\hat{T}_H$ times per $M=2^b$ -ary symbol. For example, $\omega=1$ would imply that the FH rate R_H at each FH stage is the same as the symbol rate R_s , i.e. $R_H = R_s$. Furthermore, we assume that the relative FH delay between two adjacent FH stages is T_H/S , i.e. that the FH at the (s+1)th stage happens T_H/S seconds earlier or later than that at the sth stage. Consequently, the actual number of hops (chips) per symbol, L, the number of hops, ω , at each FH stage, as well as the number of FH stages, S, obey the relationship of $L = T_s/(T_H/S) = \omega S$.

Based on the above spectrum assignment strategy and assumptions, multistage FH can be implemented as follows. After the MFSK modulation stage of Fig.1, each MFSK tone is frequency-hopped using S number of possible frequencies from the S frequency sets, one from each set, according to the FH strategy to be outlined in the next subsection. The FH frequency to be activated during each of the L chips is determined by the sum of the frequencies activated at the S number of FH stages,

i.e. by $\sum_{s=1}^{S} f_{s,i_s,l}$, as shown in the Fig.1.

B. Residue Number System Based FH

It is widely recognized that in FHMA communications the FH patterns must be assigned such that: a) there is no ambiguity concerning the identity of the user and the information it transmits; b) the probability of a hit or collision between the frequencies activated by any two FH patterns is as low as possible, which implies that each user must interfere as small a fraction of time as possible with the other users; c) all frequencies in the FH band are activated with the same probability; and d) it is also desirable for the transmitter to hop to a frequency far from the previous one, i.e. the consecutively activated MFSK tones of the FH patterns must be spaced sufficiently far apart, so that the channel's characteristics associated with consecutive hops become independent. In this Subsection a FH scheme based on the Residue Number System (RNS) suitable for the proposed synchronous multistage FHMA system is contrived. Let us first give a brief introduction to the basic philosophy of RNSs.

A RNS [9] is defined by the choice of S positive integers m_1, m_2, \ldots, m_S referred to as moduli. If all the moduli are pairwise relative primes, any integer A_k - which is used to describe the kth user's **FH address**, can be uniquely and unambiguously represented by the residue sequence $A_k \Leftrightarrow$ $(A_{k1}, A_{k2}, \dots, A_{kS})$ in the range of $0 \leq \mathbf{A}_k < M_S$, where $A_{ks} = \mathbf{A}_k \pmod{m_s}$ represents the residue digits of \mathbf{A}_k upon division by m_s , and $M_S = \prod_{s=1}^S m_s$ is the dynamic range of the RNS. By contrast, the notation a_k will be used in the context of multiuser detection to be introduced in Section III, which will be referred to as the kth user's **multiuser detection** (MUD) address. According to the Chinese Remainder Theorem (CRT) [9], for any given S-tuple $(A_{k1}, A_{k2}, \dots, A_{kS})$, where $0 \le A_{ks} < m_s$, there exists one and only one integer \mathbf{A}_k such that $0 \leq \mathbf{A}_k < M_S$ and $A_{ks} = \mathbf{A}_k \pmod{m_s}$, which allows us to uniquely recover the integer value of A_k from the received residue digits. Again, A_k represents the FH address of user k.

Assuming that the integers A_1 and A_2 have RNS representations of $A_1 \Leftrightarrow (A_{11}, A_{12}, \ldots, A_{1S})$ and $A_2 \Leftrightarrow (A_{21}, A_{22}, \ldots, A_{2S})$, respectively, then $A_1 \bullet A_2$, where \bullet denotes addition, subtraction or multiplication, yields another unique residue sequence A_3 . The arithmetic operations over the RNS can be carried out on a residue-by-residue basis, which can be expressed as [9]:

$$\mathbf{A_3} = \mathbf{A_1} \bullet \mathbf{A_2} \pmod{M_S} \Leftrightarrow [(A_{1s} \bullet A_{2s}) \pmod{m_s}]_{s=1}^S,$$
(3)

where on the left of (3) the operation \bullet represents modulo- M_S addition, subtraction or multiplication of two integers, and at the right of (3) \bullet represents the same operations performed on the basis of the appropriate residue digits A_{1s} and A_{2s} , with respect to their corresponding modulus m_s .

Single-User FH: The basic principle of the RNS-based multistage FH scheme is that the frequencies at the S number of stages are activated according to the S number of residue digits of a corresponding residue sequence in the RNS. Specifically, let $\mathbf{A}_k^i \Leftrightarrow (A_{k1}^i, A_{k2}^i, \dots, A_{kS}^i)$ be the residue sequence

representation of integer \mathbf{A}_k^i in terms of a RNS using moduli $m_1,\ m_2,\dots,m_S$, where the superscript i represents the time index. Then the FH frequency at instant i is determined by the sum of $f_{1,A_{k_1}^i}$ from the Stage-1 frequency set $\mathbf{F}_1,\ f_{2,A_{k_2}^i}$ from the Stage-2 frequency set \mathbf{F}_2 , an so on until $f_{S,A_{k_S}^i}$ from the Stage-S frequency set \mathbf{F}_S , i.e. by $\sum_{s=1}^S f_{s,A_{k_S}^i}$. At the time index (i+1), \mathbf{A}_k^i is increased by one and hence it is represented in the RNS domain as $\mathbf{A}_k^{(i+1)} = \mathbf{A}_k^i + 1 \Leftrightarrow (A_{k_1}^{(i+1)},A_{k_2}^{(i+1)},\dots,A_{k_S}^{(i+1)})$. Similarly, the FH frequency at instant (i+1) is determined by the sum of $f_{1,A_{k_1}^{(i+1)}}$ from the Stage-1 frequency set \mathbf{F}_1 , $f_{2,A_{k_2}^{(i+1)}}$ from the Stage-S frequency set \mathbf{F}_S , yielding $\sum_{s=1}^S f_{s,A_{k_S}^{(i+1)}}$. The above procedure is continued until the end of transmission of user k. Since the dynamic range of the above RNS is $[0,M_s=\prod_{s=1}^S m_s)$, the period of the FH patterns is M_S .

Multi-user FHMA: Let us now discuss the RNS-based MS/UWB FHMA scheme. Let m_1, m_2, \ldots, m_S be the pairwise relative primes representing the number of frequencies in the S number of frequency sets, namely in $\mathbf{F}_1, \mathbf{F}_2, \dots, \mathbf{F}_S$, with respect to the S FH stages of Fig.2, respectively. Let $\mathbf{A}_k \Leftrightarrow (A_{k1}, A_{k2}, \dots, A_{kS}), \text{ where } 0 \leq \mathbf{A}_k < M_S, \text{ and }$ $\mathbf{a}_k = [a_k(0), a_k(1), \dots, a_k(L-1)]$ be the FH address and MUD address of user k, respectively. Furthermore, let $C \Leftrightarrow$ (C_1, C_2, \dots, C_S) represent a reference register in the RNS domain, which provides reference to each user, in order to implement synchronous transmission. C is increased by one, whenever S hops happened. According to (3), this can be implemented by increasing each element of (C_1, C_2, \dots, C_S) by one with respect to their corresponding modulus. Note that although the maximum integer represented by the RNS-based registers (C_1, C_2, \dots, C_S) is $M_S - 1 = \prod_{s=1}^S m_s - 1$, this will not prevent us from using it as a synchronous transmission reference. Indeed, this property can substantially simplify the implementation of synchronous transmissions, as we will show below. According to the above assumptions, when the kth user is attempting to access the network and to transmit the information symbol $\boldsymbol{X}_k^{(m)}$, the operation of the multistage FHMA scheme can be described with the aid of the following steps:

- 1) According to (1), $X_k^{(m)}$ is signalled with the aid of the kth user's MUD address with respect to the chips $0,1,\ldots,L-1$, where $L=\omega S$. The M-ary symbols are expressed as $\mathbf{Y}_k=[y_k(0),y_k(1),\ldots,y_k(L-1)]=X_k^{(m)}\cdot\mathbf{1}\bigoplus\mathbf{a}_k$, which are signalled using the L number of chips each hosting one of the M possible signalling tones.
- 2) Based on the kth user's FH address \mathbf{A}_k and on the reference register's instantaneous value of \mathbf{C} , we derive the residue sequence \mathbf{I}_k as follows:

$$\mathbf{I}_k \Leftrightarrow (i_1, i_2, \dots, i_S) = \left[(A_{ks} \oplus C_s) \pmod{m_s} \right]_{s=1}^S,$$
according to (3).

3) For the chip $tS \leq l < (t+1)S$, $t \leq \omega - 1$, the transmitted frequency is constituted by the sum of the frequencies f_{1,i_1+t} from the Stage-1 frequency set \mathbf{F}_1 , f_{2,i_2+t} from the Stage-2 frequency set \mathbf{F}_2 , etc. upto $f_{S-(l-tS)-1,i_{S-(l-tS)-1}+t}$ from the Stage-(S-(l-tS)-1) frequency set $\mathbf{F}_{S-(l-tS)-1}$, and the frequencies $f_{S-(l-tS),i_{S-(l-tS)}+t+1}$ from the Stage-(S-(l-tS)) frequency set $\mathbf{F}_{S-(l-tS)}$ upto f_{S,i_S+t+1} from the Stage-S frequency set \mathbf{F}_{S} , as well as the frequency $f_{d,l}^{(k)}$ corresponding to the MFSK tone activated according to $y_k(l)$. The frequency given by the corresponding sum can be expressed as

$$f_{k,l} = \sum_{j=1}^{S-(l-tS)-1} f_{j,i_j+t} + \sum_{j=S-(l-tS)}^{S} f_{j,i_j+t+1} + f_{d,l}^{(k)}.$$
(5)

 The remaining MFSK symbols are transmitted by repeating the above three steps.

According to the above principles, if the number of users satisfies $K \leq M_s$, K out of the M_s FH addresses, which are expressed in the form of residue sequences derived from the integers $0,1,\ldots,M_s-1$, can be uniquely assigned to the K users. Again, based on this unique assignment and on the RNS-based FHMA scheme described above, it can be readily shown that no collisions occur between any two users, since at any moment, the addition of these FH addresses to the same instantaneous value of the common RNS-based reference register will generate $K \leq M_s$ different integers in the range of $[0,M_s)$. Consequently, the hopping frequencies derived from the resultant K integers represented as K residue sequences are unique. Therefore, again, if the number of users obeys $K \leq M_s$, the probability of hits or collisions is zero.

However, the MS/UWB FHMA system is capable of supporting more than K users, if efficient MUD techniques can be employed. More explicitly, the S-stage MS/UWB FHMA system using the RNS moduli (m_1, m_2, \ldots, m_S) and MFSK modulation can support upto $K = M \times \prod_{s=1}^{S} m_s$ number of users by invoking multiuser detection techniques [7], [8]. If the number of users, K, is higher than the number of unique FH addresses, M_S , i.e., when $K > M_S$, then a given FH address can be assigned to a group of users, and the users in this group are distinguished by their corresponding MUD address, $\mathbf{a}_k = [a_k(0), a_k(1), \dots, a_k(L-1)], k = 1, 2, \dots, K.$ More explicitly, if $U < K/M_s \le U + 1 \le M$, then a given FH address has to be assigned to more than one, namely to U or U+1 users, which form a user group. In order to resolve the associated ambiguity, an additional set of U or (U + 1) MUD addresses are assigned employing a unique mapping to each of the U or the (U+1) users in the same user group, in order to distinguish them from each other. Note that, the same MUD address can be assigned to several users belonging to different groups, since the groups are differentiated with the aid of the unique FH addresses. Based on the above user signature assignment rule, if non-fading channels or frequency non-selective fading channels are encountered by each MFSK tone, then the

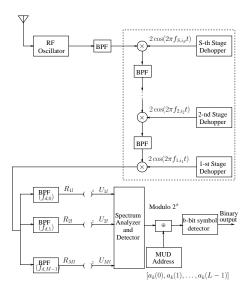


Fig. 3. Receiver block diagram of the MS/UWB FH SSMA system using MFSK modulation.

multiuser detector of the synchronous MS/UWB FHMA system only has to differentiate amongst the users of the same group, i.e. the users that were assigned the same FH address. Consequently, the number of users detected by the multiuser receiver is about K/M_s , which is not higher than M, and significantly lower than K representing the total number of users supported by the MS/UWB FHMA system. Therefore, the complexity of the multiuser receiver employed in the MS/UWB FHMA system can be substantially decreased in comparison to that of the conventional MUD concept, where all system users are detected simultaneously.

Above we have discussed the transmitter, the spectrum assignment, the FH strategy as well as the achievable diversity order of the proposed MS/UWB FHMA system. Let us now consider the detection of the MS/UWB FHMA signals.

III. DETECTION OF MS/UWB FHMA SIGNALS

A. Receiver Description

The receiver schematic of the MS/UWB FHMA system is shown in Fig.3, which essentially follows the inverse structure of the transmitter. At the receiver the received signal is first down-converted to the Intermediate Frequency (IF) corresponding to the FH pattern used. Then, the down-converted signal is frequency dehopped as indicated by the dashed-line box of Fig.3. In contrast to the FH scheme of the transmitter in Fig.1, where FH commences at Stage-1 and continues to the Sth stage, at the receiver of Fig.3 the hopping frequencies are removed in the order commencing with the Sth stage and finishing at the 1st stage, according to the specific FH pattern of a given user. Following the frequency de-hopping stage, the received signal is input to a bank of M energy detectors matched to the M frequency tones of the MFSK stage. The detection interval is T_h seconds, which is synchronized with the chips or FH intervals of the desired user. The M energy detectors provide ML outputs during a symbol period of T_s seconds. As shown in Fig.3,

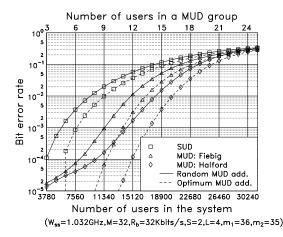


Fig. 4. Bit error rate shown for the conventional single-user detector (SUD), for Fiebig's [7] interference cancellation multiuser detector (MUD: Fiebig), and for the multiuser detector based on multistage interference cancellation [8] (MUD: Halford) in the context of a 2-stage UWB FHMA system associated with parameters $m_1 = 36$, $m_2 = 35$, M = 32, S = 2 and L = 4 for transmission over noiseless interference-contaminated channels ($E_b/N_0=\infty$).

the spectrum of each T_s -duration transmission is analyzed in order to determine, which frequency tone and hence which b-bit symbol $X_{k}^{(m)}$ was transmitted by invoking the MUD address of

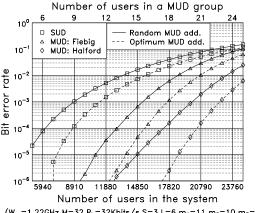
B. Performance Example

The BER performance of the MS/UWB FHMA system is studied by simulation for three detection schemes, namely for the conventional Single-User Detector (SUD), the MUD proposed by Fiebig [7] (MUD: Fiebig) and the MUD proposed by Halford and Brandt-Pearce (MUD: Halford). In our simulations, two types of MUD address are considered, namely a random and the optimum MUD address assignment. In the context of the random MUD address assignment scheme, each user's MUD address is randomly generated, and the kth user's MUD address can be expressed as

$$\mathbf{a}_k = (\gamma_k, \gamma_k \beta, \gamma_k \beta^2, \dots, \gamma_k \beta^{L-1}), \tag{6}$$

where γ_k is an element of GF(M) assigned to user k and β is a fixed primitive element of GF(M). In our simulations GF(32) was considered and the corresponding primitive polynomial was $p(x) = 1 + x^2 + x^5$.

Fig.4 and Fig.5 present the BER comparison of the abovementioned three detection schemes, when there is no additive channel noise $(E_b/N_0 = \infty)$. The parameters used in our simulations were listed in the related figures. As shown in the figures, we assumed that the total system bandwidth was 1.032GHz in Fig.4 and 1.22GHz in Fig.5. The results show that after the 1st stage interference cancellation (IC), the multiuser detectors using both iterative IC [7] and multistage IC [8] outperform the conventional single-user detector by a wide margin. The optimum MUD address assignment significantly outperformed the random MUD address assignment in the context of



 $(W_{ss}=1.22GHz,M=32,R_b=32Kbits/s,S=3,L=6,m_1=11,m_2=10,m_3=9)$

Fig. 5. Bit error rate shown for the conventional single-user detector (SUD), for Fiebig's [7] interference cancellation multiuser detector (MUD: Fiebig), and for the multiuser detector based on multistage interference cancellation [8] (MUD: Halford) in the context of a 3-stage UWB FHMA system associated with parameters $m_1 = 11, m_2 = 10, m_3 = 9, M = 32, S = 3$ and L = 6 for transmission over noiseless interference-contaminated channels $(E_b/N_0=\infty).$

all three detection schemes. Furthermore, for the noiseless but interference-contaminated channel considered, the MUD employing multistage IC [8] outperforms iterative IC [7]. Note that although the total number of users supported by the MS/UWB FHMA system may be as high as tens of thousands, the number of users to be detected by each multiuser detector is extremely low, as shown at the top of Fig.4. Therefore, the MS/UWB FHMA system is capable of supporting a tremendous number of users, while employing low-complexity receivers.

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