Electronic interface design for an electrically floating micro-disc

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Abstract

The design of an electronic interface for an electrically floating micro-disc is presented. The system, based on a second order electromechanical $\Delta - \Sigma$ modulator, can be used as a multi-axis capacitive accelerometer with the proof mass levitated by electrostatic forces. A synchronous detection scheme, used to sense the position of the proof mass, has been designed, simulated in PSpice TM and successfully implemented on a PCB. The interface contains a differential pick-off with no ohmic connection to the proof mass, which distinguishes the circuit from typical sensing circuits for MEMS capacitive accelerometers. A noise analysis of the pick-off circuit was performed, and an electronic equivalent model of the sensing circuit has been developed and used to analyse the linearity of its transfer function. The linearity of the sensing circuit was confirmed experimentally using the PCB implementation.

1. Introduction

In typical micromachined accelerometers the proof mass is attached to the substrate by an anchor [1], which makes their characteristics dependent on the fabrication process tolerances and difficult to tune once the device has been fabricated. Electrostatic levitation is a method to eliminate the need for a mechanical connection and is well suited to microelectromechanical systems (MEMS). On this approach, relatively little research has been done; nevertheless, there are many potential applications including inertial sensors [2], micro-motors [3] and frictionless bearings [4]. An electrostatically levitated micro-motor with the function of a multi-axis accelerometer is reported in [5], and also an electrostatically levitated spherical three-axis accelerometer was developed by Ball Semiconductors [6].

This paper presents an electronic interface for a multiaxis capacitive micromachined accelerometer, for which the proof mass consists of a micro-disc levitated by electrostatic forces. The system is able to detect linear, out-ofplane acceleration and angular acceleration about the two in-plane axes. Compared to conventional force-balanced accelerometers, the effective spring constant depends only on the feedback voltage of the system due to the electrostatic levitation of the proof mass. By changing the feedback voltage, the spring constant can be readily adjusted, thus the sensitivity and the bandwidth of the system can be tuned according to the sensor application. Furthermore, the system offers uniform sensitivity in all degrees of freedom, which is difficult to achieve in conventional multi-axis accelerometers. Nevertheless, this approach imposes more severe requirements on the electronic interface design. To ensure the control of the disc position in three degrees of freedom, the accelerometer is embedded in a second order electromechanical $\Delta - \Sigma$ modulator. This structure offers good linearity, good stability and results in direct digital output of the system. Furthermore, it ensures that high resolution with a simple architecture can be achieved [7].

Capacitive sensor interfaces use different methods to convert the capacitance value into a voltage such as voltage detection with a unity gain buffer [8], or charge integration [9]. In this application, electrostatic levitation implies no ohmic contact to the proof mass, hence the capacitive sensing circuits used in conventional MEMS accelerometers are not suitable [10] and a novel design is required.

The block diagram of the system is presented in figure 1. The top and bottom plates of the sensor are excited with a high frequency signal $V_{\rm IN}$, and four pairs of electrodes (shown in figure 2) are used to sense the position of the disc. The signal from the sensing electrodes is applied to

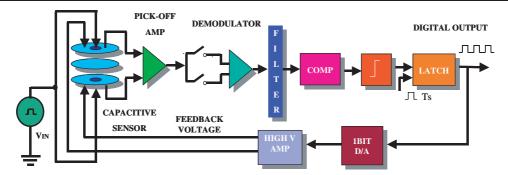


Figure 1. Levitated disc accelerometer system.

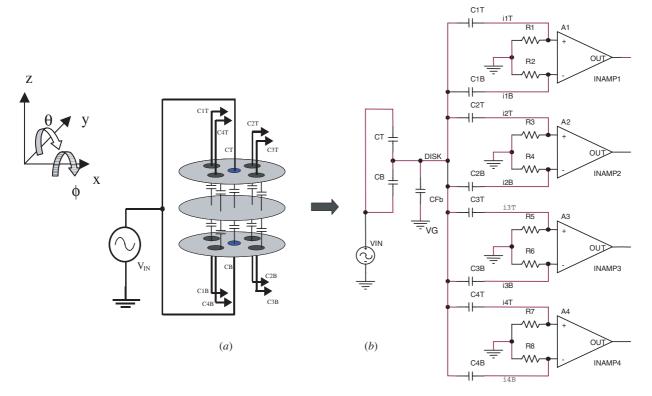


Figure 2. (a) Levitated disc accelerometer and (b) equivalent electronic model of the sensing circuit.

a low noise differential pick-off amplifier, which converts the differential capacitance between the top and the bottom plate into a differential voltage. The output of the amplifier is fed to a synchronous demodulator, followed by a low pass filter.

The mechanical sensing element is equivalent to a double integrator, which introduces a phase lag in the forward path, hence a phase compensator is needed to ensure system stability. After the compensator, the signal is applied to a 1-bit A/D quantizer implemented with a comparator and a latch. The latch is controlled by a clock signal, which determines the sampling frequency, f_s , of the modulator.

The sampling frequency has to be much higher than the bandwidth of the sensor to ensure a high sensor resolution. Considering a sensor bandwidth of 1 kHz, a sampling frequency of 100 kHz was chosen. The latch output is a pulse density modulated bitstream, which contains information

about the displacement of the proof mass. In the feedback path, this signal is applied to a 1-bit D/A converter and further to a high voltage amplifier which provides the voltage used to control the feedback electrodes. This produces an electrostatic force, which acts in opposite direction to the disc displacement, and thus the disc is kept close to the equilibrium position.

2. Disc position measurement circuit

2.1. Sensing circuit

A simplified structure of the levitated disc sensor together with the equivalent electronic sensing circuit is presented in figure 2. The top and bottom plates of the sensor comprise an excitation electrode in the centre and the outside is segmented into four quarters. Each segment consists of

one sensing electrode and two feedback electrodes. Since this section is focused on the sensing part of the electronic interface, for simplicity the feedback electrodes are omitted in figure 2(a). The feedback electrodes of the same quarter are always controlled by voltages of the same magnitude and opposite polarity. From the sensing point of view, this implies no charge injection from the feedback electrodes to the disc, hence the feedback part of the sensor can be represented by an equivalent capacitor C_{Fb} , between the disc and virtual ground (VG in figure 2(b)). Its value is given by the sum of all feedback capacitances.

Figure 2(b) shows the equivalent electronic model for the sensing circuit. A high frequency signal $V_{\rm IN}$ of 1 MHz is applied to the excitation electrodes which, together with the disc, form the capacitances $C_{\rm T}$ and $C_{\rm B}$. Capacitances $C_{k\rm T}$ and $C_{k\rm B}$ ($k=1,\ldots,4$) are formed by the four pairs of sensing electrodes and the disc. These are used for detection of the disc position in three degrees of freedom, and have a nominal value of approximately 1 pF. The pick-off circuit contains four pick-off amplifiers, one for each pair of sensing electrodes.

2.2. Linearity analysis

The linearity of the transfer function of the sensing circuit is an important requirement for the design of the system, since the differential output voltage of the sensing circuit should be proportional to the differential capacitance. Therefore, a linearity analysis of the transfer function, $\Delta V/\Delta C$, of the equivalent sensing circuit presented in figure 2(b) was performed. It can be shown that the differential voltage at the pick-off amplifier input ΔV_k , $k=1,\ldots,4$, can be expressed as a function of capacitances, excitation signal amplitude $V_{\rm IN}$, excitation signal frequency and input resistors at the pick-off amplifier $R=R_k$, $k=1,\ldots,8$, as

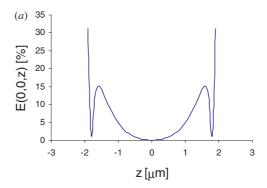
$$\Delta V_{k} = \Delta C_{K} \left| \frac{j\omega R}{1 + j\omega (C_{KT} + C_{KB})R + \omega^{2} C_{KT} C_{KB} R^{2}} V_{IN} \right| \times \frac{C_{T} + C_{B}}{C_{T} + C_{B} + \sum_{k=1}^{4} \left(\frac{C_{kT}}{1 + j\omega C_{KT}R} + \frac{C_{kB}}{1 + j\omega C_{KB}R} \right) + C_{Fb}} \right|,$$

$$K = 1, \dots, 4. \tag{1}$$

The sensing capacitances are a function of the disc movement (vertical displacement z and the tilt angles ' ϕ ' and ' θ ', with respect to the x and y axes [11]). When varying z, ϕ and θ , the expression inside the modulus of equation (1) does not remain constant which means that the dependence between ΔV_k and the differential capacitance $\Delta C_k = C_{kT} - C_{kB}$ (k=1,4) is nonlinear. To see the influence of z, ϕ and θ on the nonlinearity error, and to determine critical situations, when this error is maximum, several different cases were analysed using MathcadTM. The gap between the electrodes was assumed to be $z_0 = 2~\mu\text{m}$, and the radius of the electrodes $R=500~\mu\text{m}$; the maximum tilt angle ϕ or θ can then be expressed as

$$\phi_{\text{max}} = \arctan\left(\frac{z_0}{R}\right) = 0.004 \,\text{rad}.$$
 (2)

Figure 3(a) shows the nonlinearity error, $E(z, \phi, \theta)$, when z varies and the tilt angles ϕ and θ are zero. For the second case, figure 3(b), z is maintained constant and one of the tilt



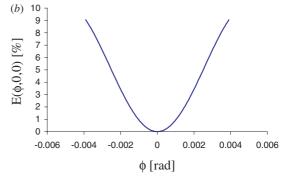


Figure 3. Non-linearity error, E [%] for $(a) \phi = \theta = 0$ and $z \in [-2; 2] \mu m$ and $(b) \phi \in [-0.004, 0.004] \text{ rad}, \theta = 0$ and z = 0.

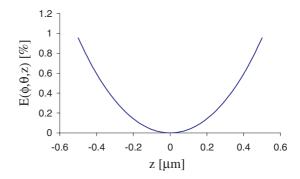


Figure 4. Non-linearity error, E [%] for small displacements of the disc, $z \in [-0.5; 0.5] \, \mu \text{m}$ and $\phi = \theta = 0$.

angles ϕ varies within the maximum values, while the other is zero.

Comparing figure 3(a) with figure 3(b) it can be noticed that the nonlinearity error is influenced more by the vertical displacement than the tilt, especially for large displacements of the disc.

In figure 4, $E(z, \phi, \theta)$ is shown for small vertical displacements, considering the tilt angles are zero. This shows that for a vertical movement of the disc within a quarter of the maximum range, $[-0.5~\mu\text{m},~0.5~\mu\text{m}]$, the maximum nonlinearity error is below 1%. Similar results were achieved by varying the tilt angles within a quarter of the maximum range. Introducing the accelerometer in a digital closed loop and applying a feedback force to bring the disc into the middle (equilibrium) position, the movement of the disc in all directions can be assumed to be maintained within 10% of the maximum value. This implies very small

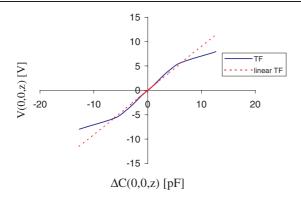


Figure 5. The transfer function of the sensing circuit $\Delta V/\Delta C$, for $z \in [-2; 2]$ and $\phi = \theta = 0$.

values for the nonlinearity error, about 0.15%, which can be neglected.

The transfer function characteristic of the sensing circuit is illustrated in figure 5. This presents the differential voltage ΔV versus the differential capacitance $\Delta C_1 = C_{\rm IT} - C_{\rm IB}$, for a variation of z within the maximum values, and for $\phi = \theta = 0$. It can be noticed that, for small displacements, the characteristic is linear.

2.3. PSpice model and simulations

The capacitive sensing interface is based on a synchronous detection scheme, which consists of a pick-off circuit followed by a demodulator and a low pass filter. Synchronous detection scheme makes use of chopper stabilization technique to cancel the offset and low frequency noise of the pick-off amplifier [12]. The pick-off circuit shown in figure 2 consists of a bridge and an instrumentation amplifier (in-amp). Since there

are four pairs of sensing electrodes, four identical circuits are used. The bridge is formed by a pair of sensing capacitances and two resistors placed between the in-amp inputs and ground. The resistors convert the current from the sensing capacitor (e.g. i_{1T}) into a voltage, and set up the dc level at the amplifier input. Their value was chosen to avoid signal attenuation on one hand, and to minimize thermal noise contribution on the other.

The in-amp, consisting of three low noise precision opamps OPA604, has very high impedance at both of the inputs, a stable amplification of 35 dB for the differential signal and high common mode rejection ratio. The output signal of the amplifier is demodulated using an IC AD734 and then applied to a second order active low pass filter. The cut-off frequency of the filter is chosen above the clock frequency of the comparator, for a correct operation of the Δ - Σ modulator, and below the excitation signal frequency for appropriate filtering after demodulation. To detect and control the position of the disc in three degrees of freedom four Δ - Σ modulators are used, one for each quarter of top and bottom plates of the sensor.

The pick-off circuit has been implemented in Orcad CaptureTM and simulated in PSpiceTM A/D. To simulate the variation of the sensing capacitance in CaptureTM, a variable impedance circuit controlled by a linear voltage was used. Considering a peak-to-peak amplitude of 20 μ V for the control voltage VC_{1T} (figure 6(a)) and a nominal value $C_{1\mathrm{Tref}}$ of 1 pF for the sensing capacitances, a differential capacitive variation of 20 aF ($\Delta C_{1T} = C_{1\mathrm{Tref}}VC_{1T}$) results. This produces a maximum amplitude variation of 10 μ V in the differential input signal of the amplifier (see figure 6(c)), which results in a sensitivity of 0.5 μ V aF $^{-1}$ for the sensing circuit. It can be seen that the output signal of the filter, after the demodulation (figure 6(b)), follows the linear capacitive variation. A PSpiceTM noise analysis was performed, and shows an

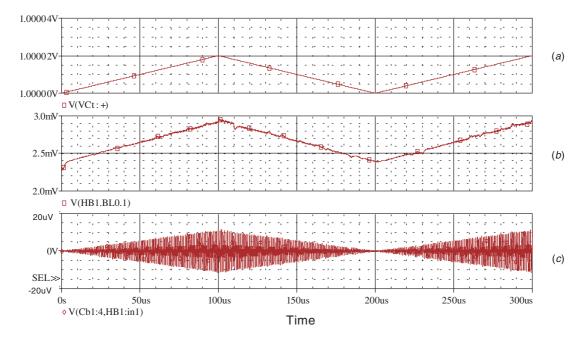


Figure 6. (a) The linear control voltage for the sensing capacitances; (b) the output of the low pass filter after demodulation and (c) the differential input signal of the in-amp.

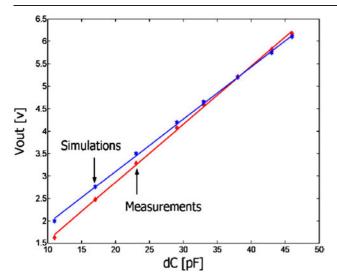


Figure 7. Output voltage of the pick-off circuit versus ΔC , for ΔC variations within 10% of the maximum range.

equivalent input noise floor of 40 nV Hz^{-1/2} for the pick-off amplifier. For a calculated sensitivity of 0.5 μ V aF⁻¹, this limits the capacitive measurement resolution to 1 aF.

3. Experimental results

The linearity of the pick-off circuit has been tested experimentally. Considering larger radius for the electrodes than the one given in the section 2.2, the calculated value for the nominal sensing capacitance is 2.3 pF. For tests, only fixed capacitors were used instead of sensing capacitances and the change in capacitance is implemented using an adjustable capacitor. Assuming that the movement of the disc is maintained within 10% of the maximum range, the maximum ΔC to be measured is 0.46 pF. Measuring capacitive variations in the range from 4.6 pf down to 23 fF (1% of the nominal value) is difficult to realize experimentally. Therefore, the nominal value has been increased to 230 pF and the excitation signal frequency decreased to 10 kHz so that the impedance of the sensing element remains constant. Thus, capacitive variations from 46 pF down to 2.3 pF are achieved, which are easier to realize experimentally. Measurements have been carried out in this range, using two fixed capacitances with the nominal value of 230 pF and one adjustable capacitor, connected in parallel to one of the fixed capacitors. An excitation signal with an amplitude of 0.5 V and a frequency of 10 kHz was applied at the input of the capacitive network and the adjustable capacitor was used to produce the ΔC variations. Figure 7 presents the results of the PSpiceTM simulations and the measurement results for ΔC variations between 10 and 46 pF. The measurement results are represented by stars and the simulation results by diamonds. Both are compared with their best linear approximation (solid line in figure 7), which was calculated in MatlabTM using the polynomial approximation functions called polyfit and polyval. Although there is an offset and a gain discrepancy between the measurements and the simulations, which increases inversely proportional to ΔC ,

the results show a maximum nonlinearity error of 1% of the full-scale range.

This error is much larger than the maximum nonlinearity error calculated in MathcadTM for movement of the disc within 10% of the maximum range. However, a nonlinearity error below 1% is difficult to obtain experimentally, as the accuracy of the measured ΔC depends on the accuracy and the stability of the fixed capacitors.

4. Conclusions

The design of an electronic interface for electrostatically levitated disc accelerometer is presented. The signal detection part of the interface, based on a synchronous detection scheme, has been designed and simulated using PSpiceTM. This part of the interface has been also successfully implemented and tested on a PCB board. A new type of position measurement circuit has been developed as conventional circuits for capacitive accelerometers are unsuitable for this application, since the proof mass is electrostatically floating. A PSpiceTM noise analysis of the pick-off amplifier was performed and showed an equivalent input noise of 40 nV Hz⁻¹. Sensitivity of the pick-off circuit was calculated at 0.5 μ V aF⁻¹, which is equivalent to a capacitive sensing resolution of 1 aF.

An equivalent electronic model for the sensing circuit was developed and used to analyse the linearity of its transfer function $\Delta V/\Delta C$. The linearity analysis shows that for large displacements of the disc, the nonlinearity error depends more on the vertical displacement than on the tilt, and for small displacements this error is very small. When electrostatic forces are applied to keep the displacement of the disc within 10% of the maximum range, the analysis shows a nonlinearity error of about 0.15%, which can be neglected. PSpiceTM simulation results and measurements on a PCB board show a nonlinearity error of 1% for a movement of the disc within 10% of the maximum range.

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