

Electrodeposition of Ni-Si Schottky Barriers

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Electrodeposition is being used to fabricate magnetic microstructures directly on patterned n-type Si wafers of various substrate resistivities. The Ni-Si Schottky barrier is characterized and found to be of high quality for relatively low Si resistivities (1–2 $\Omega\cdot\text{cm}$), with extremely low reverse leakage. It is shown that a direct correlation exists among the electrodeposition potential, the roughness, and the coercivity of the films. A conductive seed layer or a back contact is not compulsory for electrodeposition on Si with resistivities up to 15 $\Omega\cdot\text{cm}$. This shows that electrodeposition of magnetic materials on Si might be a viable fabrication technique for magnetoresistance and spintronics applications.

Index Terms—Electrodeposition, Schottky barriers, silicon, spintronics.

I. INTRODUCTION

THE future of spintronics and magnetoresistance applications depends heavily on the integration of magnetic materials with silicon. Schottky barriers (SBs) between magnetic metals and semiconductors are particularly useful for suppressing substrate leakage currents in magnetoresistance measurements. Furthermore, hot electron spin valve transistors require high quality SBs for their operation [1], [2]. The development of fabrication methods for high quality magnetic metal-Si SB junctions is therefore crucial for the implementation of integrated spintronic devices.

Electrodeposition is low-temperature, cost effective, and particularly attractive for its vital role in the fabrication of self-assembled magnetic nanostructures where line-of-sight techniques do not work [3]. Electrodeposition has been used in the past for the formation of magnetic layers on Si [4] but the electrical behavior of the resulting structures has not yet been investigated.

In this paper, electrodeposition as a metallization technique for spintronic applications is proposed. Ni microstructures were grown directly on lithographically patterned Si substrates. The characterization of electrodeposited Ni-Si contacts reveals that the resulting SB is superior to those fabricated by other metallization techniques [5]. The film's roughness is controlled by the electrodeposition conditions and a correlation between roughness and coercivity is found. It is also shown that electrodeposition on resistive Si is possible without the need of back contacts for resistivities up to 15 $\Omega\cdot\text{cm}$.

II. EXPERIMENTAL PROCEDURE

Electrodeposition of Ni on n-type, $\langle 100 \rangle$ Si with resistivities of 0.01–0.02 $\Omega\cdot\text{cm}$, 1–2 $\Omega\cdot\text{cm}$, and 10–20 $\Omega\cdot\text{cm}$ was performed using a Ni sulphate bath [3]. An Autolab AUT72032 potentiostat three electrode system with a Pt counter electrode and a saturated calomel reference electrode (SCE) were used.

For the definition of microstructures a 20-nm thermal SiO₂ was grown on the Si wafers and patterns were defined by photolithography. Subsequently, the wafers were immersed in a 20:1 buffered HF solution which etched the oxide and exposed Si areas of different shapes and sizes ranging from 2.5 μm to 1.5 mm. On some Si wafers ohmic Al back contacts were defined to allow for electrical measurements. This was done by Al evaporation followed by an anneal for 30 min at 450 °C in H₂/N₂. In all experiments the wafers were sawed in pieces, RCA cleaned, and dipped in 20:1 buffered HF prior to electrodeposition. The thickness of the Ni films was controlled by measuring the charge accumulated at the cathode electrode (Si substrate) during electrodeposition taking into account the hydrogen evolution effects.

The films were characterized using an optical microscope and a Topometrix 100 μ , 10-Z linearized Accurex atomic force microscopy (AFM) Explorer. Electrical measurements of the Ni-Si junctions were taken using a Hewlett-Packard 4155 A semiconductor parameter analyzer. Magnetic measurements were performed at room temperature using an Oxford Instruments 3001 vibrating sample magnetometer.

III. RESULTS AND DISCUSSION

A. Schottky Barrier Characterization

Ni pads were electrodeposited on Si and the Ni-Si electrical contact was measured for two different Si resistivities. For Si resistivities of 0.01–0.02 $\Omega\cdot\text{cm}$, the SB is very weak. In contrast, for Si with 1–2 $\Omega\cdot\text{cm}$ excellent SBs are achieved with very low reverse bias current, 6–7 orders of magnitude smaller than the forward bias current. Fig. 1 illustrates typical J–V curves of Ni-Si junctions for both cases.

For the characterization of the high resistivity Ni-Si SB, the saturation current density J_S was derived by extrapolation of the linear forward part to the y-axis and it was found to be 1 $\mu\text{A}\cdot\text{cm}^{-2}$, in agreement with the reverse bias current. The SB height is 0.71 V and the ideality factor (η) is around 1.1 at low voltages.

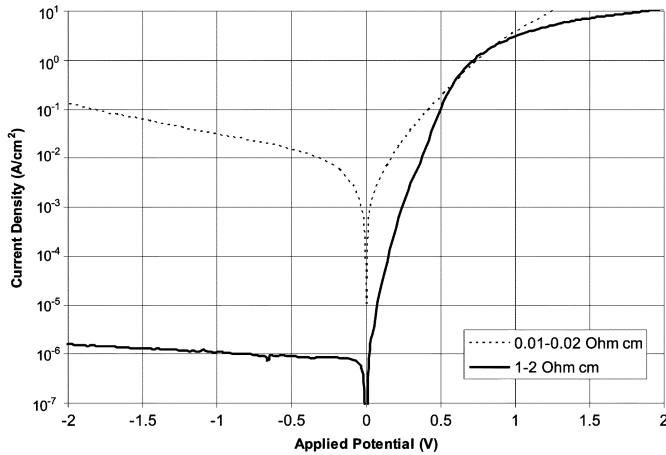


Fig. 1. J–V curves of electrodeposited Ni–Si contacts showing excellent SB for Si resistivity 1–2 $\Omega\cdot\text{cm}$. The contacts are circular with diameter 0.04 cm.

Some of the junctions exhibited a deviation from linearity appearing in the vicinity of 0.3 V at forward bias. This deviation can be explained phenomenologically by considering an inhomogeneous junction. Such junctions consist of several SBs in parallel. These SBs can be taken into account by assuming that the total current is the resultant of two currents running in parallel

$$I_{\text{total}} = I_1 + I_2. \quad (1)$$

The first current, I_1 , corresponds to an ideal SB current and can be expressed as

$$I_1 = I_{S1} \cdot e^{(V - I_1 \cdot R_{S1}) \cdot \frac{q}{k \cdot T}}. \quad (2)$$

The second current, I_2 , contains nonideal effects of a practical Schottky diode

$$I_2 = I_{S2} \cdot e^{(V - I_2 \cdot R_{S2}) \cdot \frac{q}{\eta \cdot k \cdot T}}. \quad (3)$$

In the above equations, R_{S1} and R_{S2} are the series resistances, I_{S1} and I_{S2} the saturation currents, q the elementary charge, k the Boltzmann constant, and T the absolute temperature. The variation of the ideality factor η in (3) gives I – V curves with different forward bias deviations. In such a manner, the I – V characteristics of nominal identical Schottky contacts prepared on a single substrate can be fitted as shown in Fig. 2.

The sum of the I_{S1} and I_{S2} values used in the model corresponds to a current of 4 nA, in good agreement with the reverse leakage current of the experimental curves. A more detailed analysis of the forward bias nonlinearities would require measurements as function of temperature for an independent derivation of the barrier height and would introduce parameters representing the internal physical characteristics of the junctions as suggested by Tung [6].

B. Magnetic Properties

Control of the roughness of the metallic films was achieved by varying the electrodeposition potential. Nonpatterned Si samples were used for the electrodeposition of Ni films using constant electrochemical potentials from -1.1 to -1.8 V, which

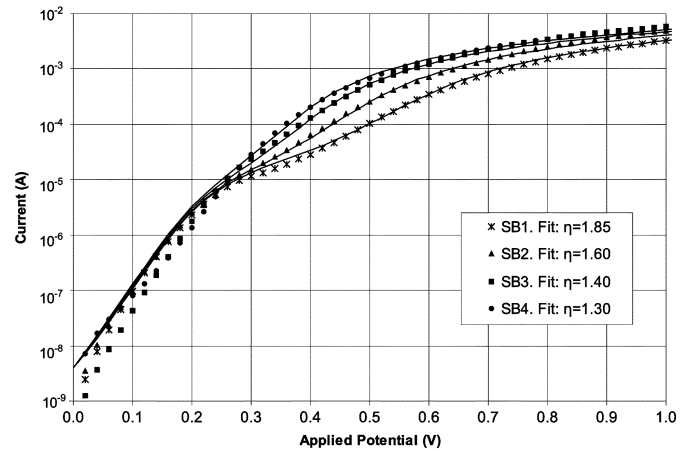


Fig. 2. Fitting of four nominal identical Ni–Si (1–2 $\Omega\cdot\text{cm}$) SBs prepared on a single substrate with the phenomenological model for inhomogeneous SBs as discussed in the text. The contacts are square with side 0.07 cm. $R_{S1} = 6$ k Ω , $R_{S2} = 100$ Ω , $I_{S1} = 1.9$ nA, $I_{S2} = 2$ nA.

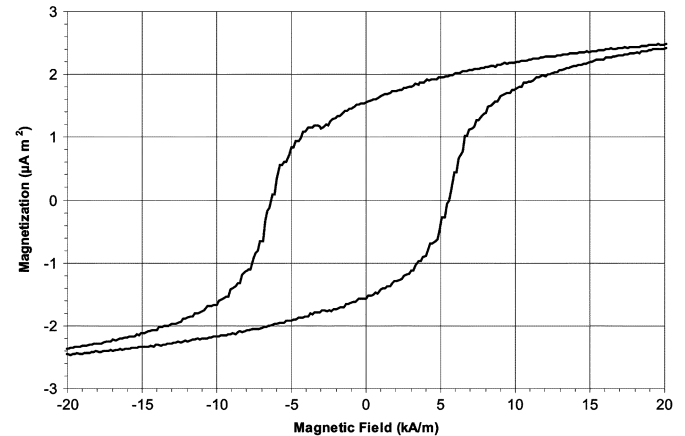


Fig. 3. Hysteresis loop of a 300-nm-thick Ni film electrodeposited on Si.

lead to different electrodeposition rates. The surface of the resulting films was characterized using AFM.

The smoothest surfaces and smallest grains were achieved for the potential of -1.3 V. Lower absolute potential values could not provide a uniform electrochemical nucleation, resulting in increased roughness. Higher absolute potential values lead to very fast film growth giving even rougher and intensively dendroid surfaces with high spikes.

Magnetic measurements were performed using a VSM to investigate the impact of the surface morphology on the magnetic properties of the Ni films. A typical hysteresis loop from a 300-nm-thick Ni film electrodeposited on Si is illustrated in Fig. 3. Table I shows correlation among the electrodeposition potential, the root-mean-square (RMS) roughness, and the coercivity values.

The coercivity increases with the surface roughness, in good agreement with other reports on similar magnetic films [4]. This effect can be attributed to roughness induced domain wall pinning contributions to coercivity [7]. From magnetization measurements the film thickness was calculated to be 300 nm. The charge corresponding to this Ni mass revealed that 10% of the charge reaching the Si substrate during electrodeposition was

TABLE I
SURFACE AND MAGNETIC PROPERTIES CORRELATION

Electrodeposition Potential (V)	RMS Roughness (nm)	Coercivity (Oe)
-1.1	17.9	74.9
-1.3	15.2	70.8
-1.4	19.1	76.9
-1.6	237	80.1
-1.8	608	87.2

Different electrodeposition potentials result in films with different roughnesses. The RMS roughness is correlated with the magnetic coercivity.

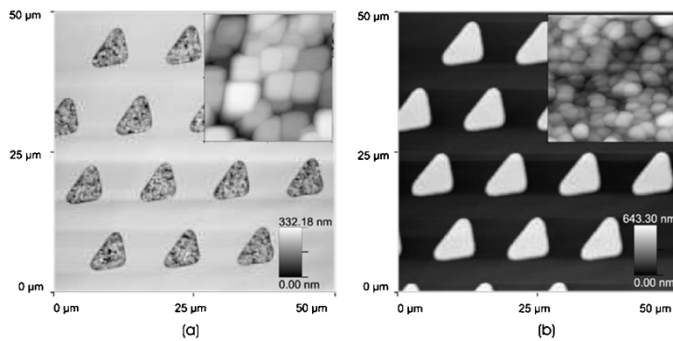


Fig. 4. AFM images of Ni electrodeposited on patterned Si showing roughness reduction by using electrochemical pulse pretreatment: (a) 1.5 V for 0.2 s, (b) 1.7 V for 0.4 s. In the insets, AFM scans of $2 \mu\text{m} \times 2 \mu\text{m}$ are presented.

due to hydrogen evolution. These considerations allow the control of the Ni film's thicknesses.

Further reduction of the surface roughness is achieved by applying pulsed electrochemical pretreatment of the Si surface before the film deposition stage. As the electrochemical nucleation rate is proportional to the applied current density, a high, short duration pulse just before electrodeposition leads to smoother Ni layers. AFM images of two Ni films electrodeposited on patterned Si using pulsed pretreatment and cyclic voltammetry are shown in Fig. 4. A pulse of -1.5 V for 0.2 s gave films with 37.1 nm rms roughness whereas a pulse of -1.7 V for 0.4 s gave a much smoother film of 7.1 nm rms roughness. Further reduction of the film roughness is necessary for spintronic applications.

C. Electrodeposition on Resistive Si

A back-contact is not always practical in integrated circuits, and hence we also investigated the direct electrodeposition of Ni on Si without metallic contacts. Cyclic voltammetry measurements show that it is possible to do electrodeposition even on Si of high resistivities. The only visible effect of high resistivity is the ohmic voltage drop in the substrate which can be modeled accurately using Poisson's equation as shown in Fig. 5.

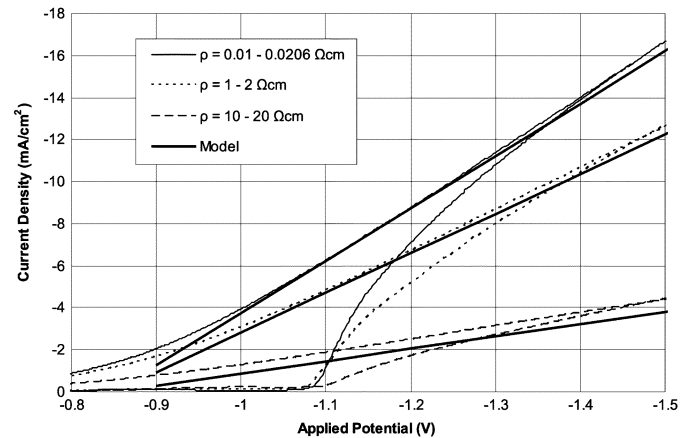


Fig. 5. Cyclic voltammetry curves of Ni electrodeposition on Si with different resistivities. The thick solid lines correspond to a Poisson model with the Si resistivity as the only varying parameter.

IV. CONCLUSION

The Schottky barrier of Ni-Si junctions fabricated by electrodeposition was characterized. It was shown that high quality barriers are obtained using Si with resistivity $1\text{--}2 \Omega\cdot\text{cm}$, with very low reversed leakage. The roughness of the magnetic films is correlated to their coercivity. This study opens up the way for the investigation of spintronic effects on Si and consequently for the exploitation of all the benefits of Si technology.

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