

## Observation and Analysis of Tunneling Properties of Single Spherical Nanocrystalline Silicon Quantum Dot

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(Received September 15, 2005; revised January 13, 2006; accepted January 20, 2006; published online April 25, 2006)

Electron tunneling is investigated experimentally and theoretically for a single nanocrystalline silicon quantum dot with a diameter of less than 10 nm. Nanocrystalline silicon quantum dots are deposited by very high frequency (VHF) plasma chemical vapor deposition (CVD) sparsely over the oxide-covered silicon substrate. The dots are then naturally oxidized at room temperature by exposing it to ambient air, resulting in an ultrathin oxide tunnel barrier. A current peak is clearly observed at room temperature with the highest peak-to-valley current ratio of about 17 by contact-mode atomic force microscopy (AFM). We also perform three-dimensional numerical simulations of the transmission spectrum and tunneling current based on the scattering matrix theory. In the current–voltage ( $I$ – $V$ ) characteristics calculated at room temperature we demonstrate the resonant tunneling current peaks associated with the quasi-bound state formed in a spherical silicon quantum dot.

[DOI: 10.1143/JJAP.45.3638]

KEYWORDS: resonant tunneling, room-temperature operation, nanocrystalline silicon quantum dot, atomic force microscopy, three-dimensional simulation, scattering matrix

### 1. Introduction

Resonant tunneling (RT) was first observed by Tsu and Esaki in 1974 for compound semiconductor superlattice structures.<sup>1)</sup> RT in silicon has also been a main topic of research in the past few years mainly because of the compatibility with current IC fabrication technology. Silicon-based resonant tunneling devices (RTDs) would have a significant impact on complementary metal–oxide–semiconductor (CMOS) technology. RTDs could be integrated together with a field-effect transistor to fabricate functional devices such as low-power-tunneling-based static random-access memory (TSRAM),<sup>2,3)</sup> and multivalued circuits.<sup>4)</sup>

Silicon on insulator (SOI)<sup>5)</sup> was first used for studying RT in Si, and negative differential conductance (NDC) was observed at low temperature. RT through a two-dimensionally confined structure has also been reported recently using a nanocrystalline silicon quantum disc,<sup>6)</sup> and NDC has been observed at room temperature. Quantum-dot-based IC technology is predicted to become the most basic component of ultra-scaled semiconductor devices.<sup>7)</sup> Nanocrystalline-based RTDs can be used for local refresh in dynamic random-access memory (DRAM) with extremely low power dissipation.<sup>8)</sup>

In this work we used a spherical nanocrystalline silicon quantum dot (nc-Si QD) to fabricate an ideal zero-dimensional RT structure. The nc-Si QD is basically a zero-dimensional system with a diameter of less than 10 nm, and quantum confinement occurs spherically. By using nanocrystalline silicons we expected room-temperature negative differential conductance (NDC) characteristics with a large peak-to-valley current ratio. In addition, we performed a three-dimensional (3D) scattering matrix simulation for a zero-dimensional RT structure<sup>9)</sup> to analyze the transmission properties of a nanoscale Si QDs.

### 2. Experimental

For the experiment we prepared an N-type  $\langle 100 \rangle$  silicon substrate with a resistance of  $1 \times 10^{-3} - 2 \times 10^{-2} \Omega \text{ cm}$ . A 1.5-nm-thick oxide layer was first grown on the substrate using  $\text{H}_2\text{SO}_4 : \text{H}_2\text{O}_2$  (30 : 70) solution at  $130^\circ\text{C}$  for 10 min. Nanocrystalline Si QDs were then deposited using an ultrahigh-vacuum plasma-enhanced chemical vapor deposition (UHV-PEVCD) system. The sample was then exposed to ambient air for 1–2 h at room temperature to oxidize the nc-Si dots naturally, forming oxide of around 0.5–1.5 nm thickness. Nanocrystalline Si QDs were observed by using scanning electron microscopy (SEM), and the average diameter of the nc-Si dots was found to be about 8–10 nm with a density of  $1 \times 10^{14} \text{ cm}^{-2}$ . SEM image of the nc-Si dots is shown in Fig. 1. The sample was then prepared for contact-mode AFM measurement by attaching the sample to a metal disc using silver paste. For electrical characterizations we used a Au-coated contact-mode AFM tip. The

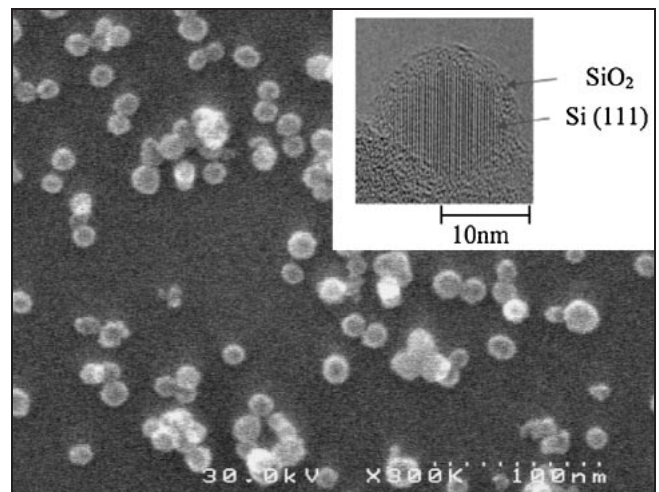


Fig. 1. SEM image of nc-Si dot deposited on chemically grown  $\text{SiO}_2$  film. The inset shows the HRTEM image of a single nc-Si QD.

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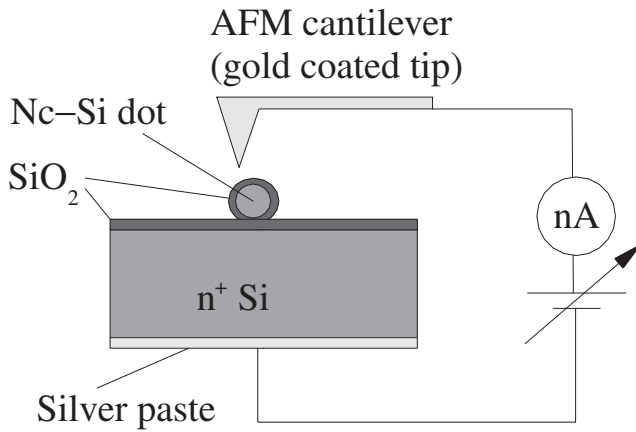


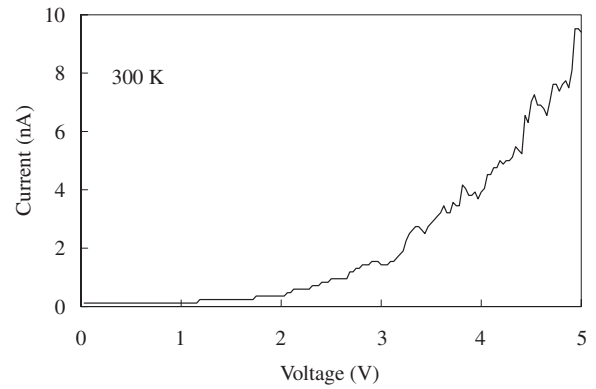
Fig. 2. Measurement setup for nc-Si QD using contact-mode AFM.

measurement setup is shown in Fig. 2. An area of  $1 \times 1 \mu\text{m}^2$  on the sample was first scanned to produce the topographical image: we then made the cantilever tip approach a dot isolated from others with an interdot distance of around 20 nm. Topographical scanning must be repeated prior to current measurement due to drift in the AFM image.

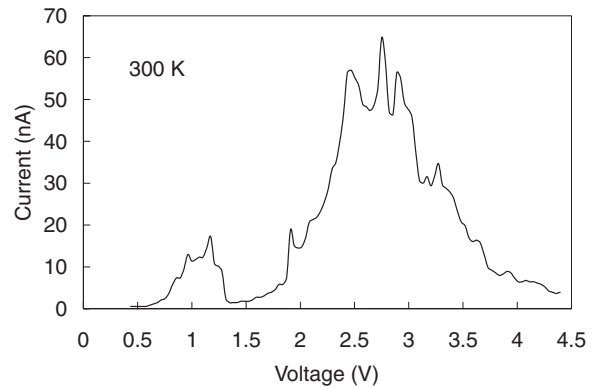
### 3. Results and Discussion

Current–voltage ( $I$ – $V$ ) characteristics are shown in Fig. 3, obtained when the tip was positively biased with respect to the substrate at room temperature. Figure 3(a) shows the  $I$ – $V$  characteristic of a sample without the nc-Si QD as a reference. It shows a monotonous increase in the background current. Figures 3(b) and 3(c) show the  $I$ – $V$  characteristics observed for two different nc-Si QDs. Current peaks were clearly observed at room temperature. In Fig. 3(b), the peak-to-valley (P/V) current ratio is around 17 for the first peak. The physical origin of the current peaks will be discussed later. The characteristics were reproduced several times before thermal and current noise became dominant due to the surface contamination of the nc-Si by the AFM tip. The characteristic in Fig. 3(c) also shows the current peaks, but their peak voltages are larger than those in Fig. 3(b) and the peak currents are smaller. Both peak voltages and peak currents were found to vary to some extent depending on the nc-Si dots chosen for measurement. One of the possible mechanisms that contribute to this fluctuation is the perturbation induced by the AFM tip to the nc-Si QD system.<sup>10)</sup> The AFM tip is probably not always positioned right on top of the nc-Si QD, and the tip-to-dot contact resistance may vary depending on the tip position relative to the dot, affecting the peak voltage. Another possible mechanism is the different charge storage in the nc-Si QD, which increases the energy level in the QD for every electron trapped in the dot. In fact, we have observed the current hysteresis for the forward and reverse bias sweeps, which are associated with charge storage in the QD.

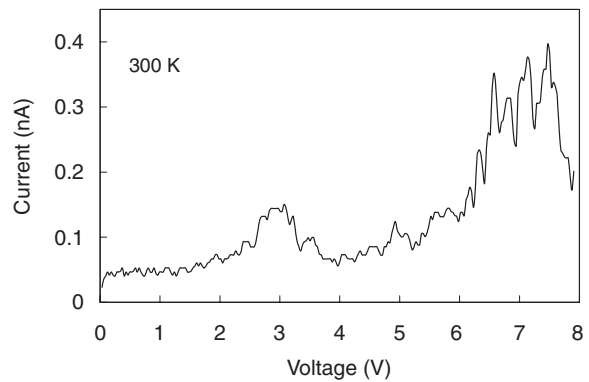
For studying the tunneling process via a single nc-Si QD theoretically, we also performed 3D scattering matrix simulation.<sup>9)</sup> The simulation is based on the 3D Schrödinger equations with scattering boundary conditions, which are numerically solved to calculate a multimode resonant tunneling characteristic. The 3D device structure used for



(a)



(b)



(c)

Fig. 3. Room-temperature  $I$ – $V$  characteristics. (a)  $I$ – $V$  characteristic from sample without nc-Si QD. (b)  $I$ – $V$  characteristics observed for sample with nc-Si QD. (c)  $I$ – $V$  characteristic observed for different nc-Si dot.

simulation is shown in Fig. 4. An undoped Si QD is embedded in the  $\text{SiO}_2$  matrix, which is sandwiched between heavily doped Si emitter and collector contact regions. We assumed for the numerical simulation that the oxide barrier thickness is 0.5 nm and the diameter of the sphere is 7 nm, which are consistent with the size of the 8 nm nc-Si QD after natural room-temperature oxidation. The transmission spectrum calculated for the device is shown in Fig. 5 with six transmission peaks indicated with roman numerals (I–VI). Two transmission subpeaks (IV and VI) were observed between the main peaks. These subpeaks are a unique feature of the spherical QD that causes a lateral-mode mixing of electron waves during the tunneling through the QD. The 3D probability density of electrons for the first

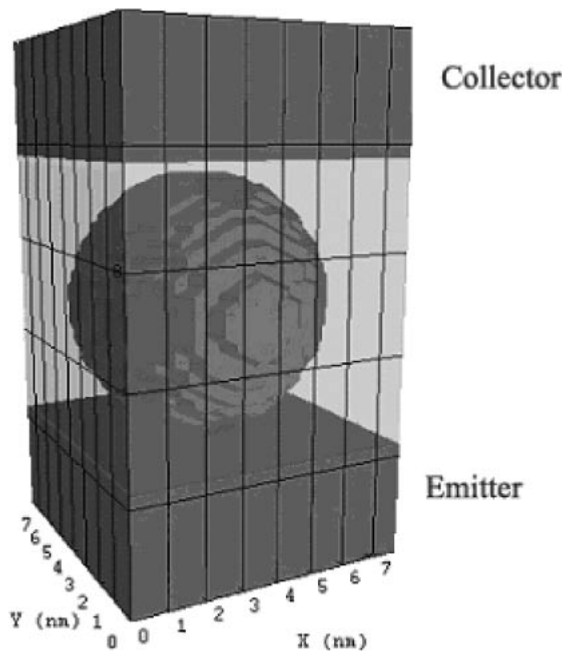


Fig. 4. 3D Structure of nc-Si QD sandwiched between two oxide barriers used for simulation.

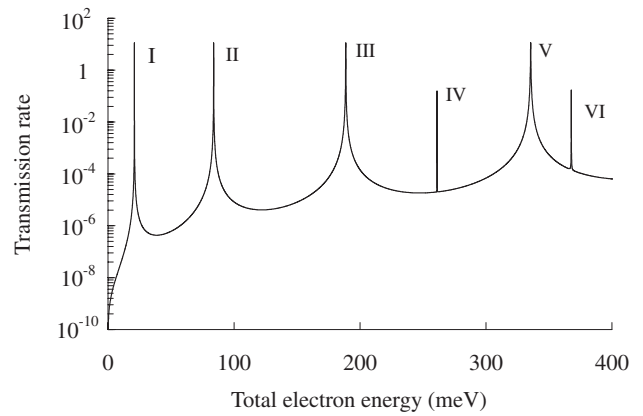


Fig. 5. Calculated transmission rates for single nc-Si QD RTD with 7 nm dot diameter. The first six energy peaks are identified.

incident mode taken at the first energy peak is shown in Fig. 6. The orthoslices are taken at the emitter, poles of the sphere, and the center of the sphere, all of which show the spherical s-orbital-like nature of the first quasi-bound state of the nc-Si dot. A p-orbital-like nature is seen for the quasi-bound state in the nc-Si dot for the second incident mode taken at the second incident peak shown in Fig. 7. The 3D

probability density of electrons this time change from the first incident mode in the sphere pole to the second incident mode at the center of the sphere. Strong wave function localization occurs in the dot with the electron density occupying the volume of the QD.

We also studied the total tunnel current using the calculated transmission spectrum. We adopted a fine energy mesh of 0.02 meV for the transmission calculations in order to obtain the total tunneling current precisely. The calculated  $I-V$  characteristic is shown in Fig. 8. The bias voltage step here was chosen to be 0.5 mV.

In Fig. 8(a), we show the  $I-V$  characteristics simulated at temperatures of 17 K, 77 K, and room temperature. It was found that the  $I-V$  curve at 17 K reflects the transmission

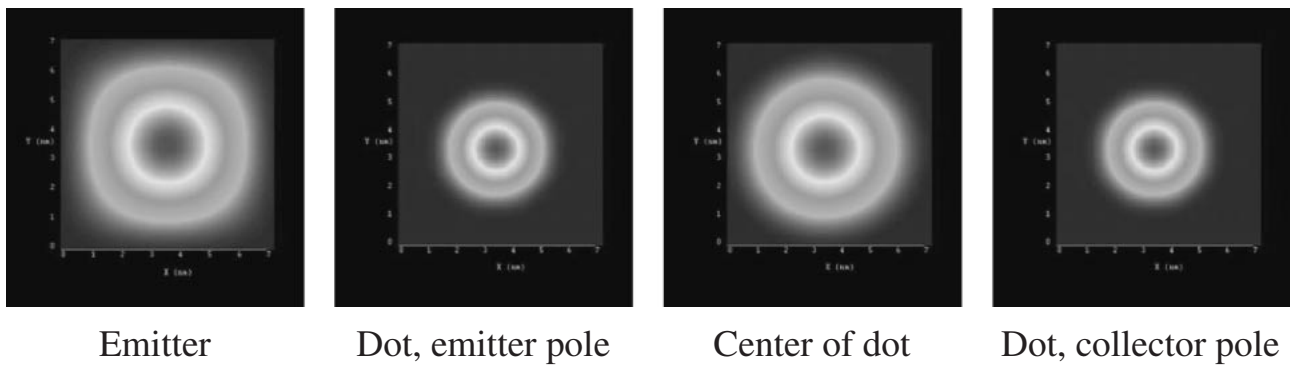


Fig. 6. 3D probability density of electrons calculated for first-mode incident wave at first energy peak.

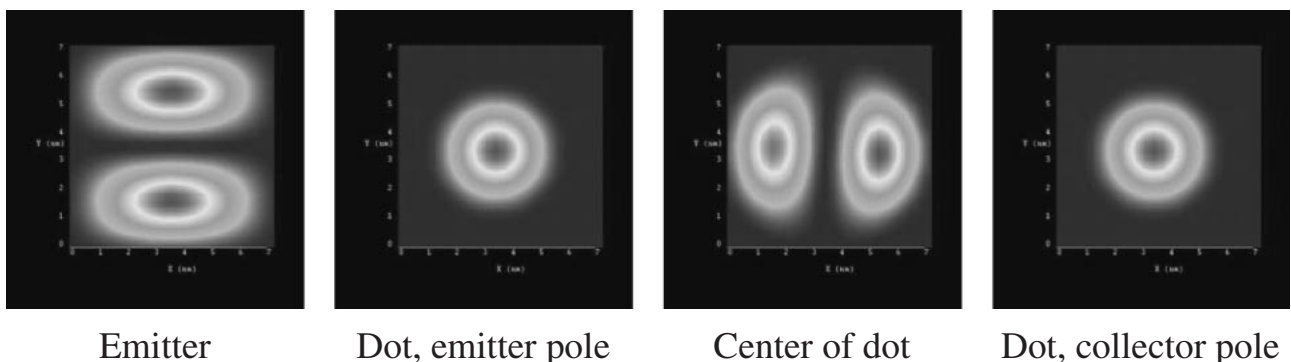


Fig. 7. 3D probability density of electrons calculated for second-mode incident wave at second energy peak.

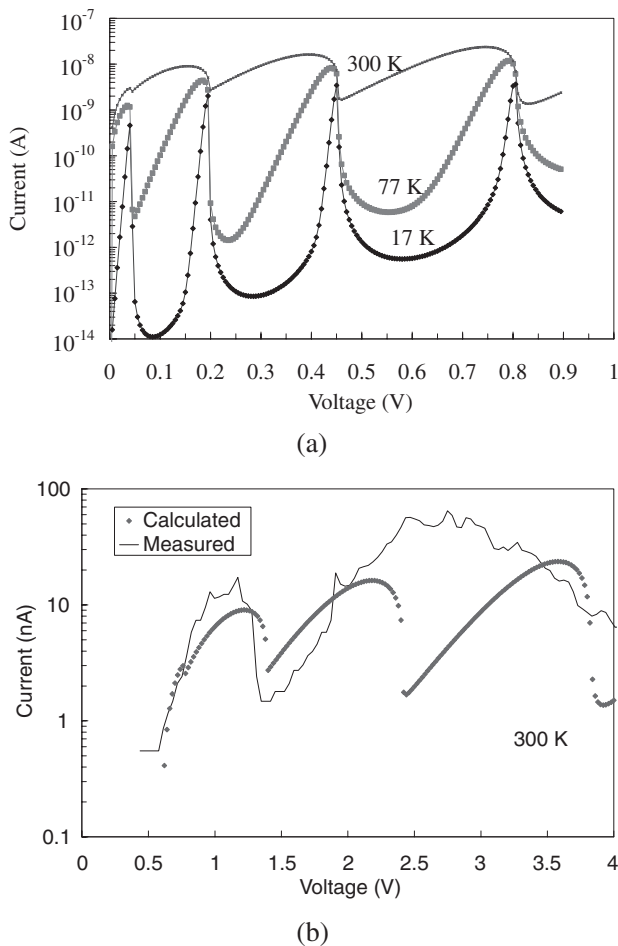


Fig. 8. Calculated  $I$ - $V$  characteristics (a) Original data from simulation at 17, 77, and 300 K. (b) Comparison between experimental results and calibrated simulation results.

spectrum in Fig. 5 straightforwardly. Four current peaks result from the transmission peaks indicated by I, II, II and V in Fig. 5, respectively. Note that a sharp and low subpeak indicated by IV does not give a visible contribution to the  $I$ - $V$  curve. At higher temperatures, the width of the individual current peaks increases markedly due to thermal broadening, resulting in a large increase in the valley currents. It should also be noted that the first current peak at room temperature is seen as simply a small shoulder on the second current peak. This is because the contributions from the first two transmission peaks (indicated by I and II in Fig. 5) overlap each other, and the two peaks merge virtually into one current peak.

By comparing these results with the experimental curve in Figs. 3(b) and 3(c), it is apparent that the observed peak voltages are much larger than simulated ones. One of the causes is the Schottky barrier formed between the gold AFM tip and Si with a height of about 0.7 eV. In addition, we suppose that the voltage drop across the nc-Si dot is only a fraction of the voltage applied between the AFM tip and the substrate. In Fig. 8(b), we tried to reproduce the experimental result by assuming that 25% of the total bias is applied to the nc-Si QD and the rest of the applied bias drops externally due to various factors such as contamination in the tip-dot interface. This ratio was determined so that the calculated peak voltage of the second current peak (first peak

is merged into the second one as explained above) fits approximately to the observed first peak voltage in Fig. 3(b).

However, this result exhibits a large discrepancy regarding the observed large second current peak. The calculated  $I$ - $V$  curve does not give a high and broad peak as observed in the experiments. The widths of the current peaks at resonance are primarily determined by the intrinsic broadening of the individual transmission peaks, and therefore there is no way to calibrate the width of the second peak while that of the first peak is maintained. These results indicate that there is a likelihood that the first current peak is caused by resonant tunneling, whereas the second current peak is certainly not caused by it. The increase in the current after the first current peak is attributable to the background current which is commonly observed in conventional RTDs and is not included in our present simulation. We also learned from our past observations that the nc-Si QDs may move during the measurement with the high bias voltage applied to the tip. This causes a misalignment of the nc-Si dot and tip and eventually their separation. It is therefore supposed that the current drop after 2.5 V in Fig. 3(b) is due to such displacement of the nc-Si QD, and this scenario is also consistent with the large current fluctuation observed over the second current peak. Further careful investigations are obviously needed for the large-bias regime to probe this hypothesis.

#### 4. Conclusions

We have observed the tunneling properties of a single spherical nc-Si quantum dot at room temperature for the first time by contact-mode AFM. A current peak has been observed with the highest peak-to-valley current ratio of about 17, which may be associated with resonant tunneling via the single nc-Si quantum dot. We have also calculated the transmission spectra and associated tunneling current on the basis of the 3D scattering matrix theory. By using the simulations we have demonstrated resonant tunneling through the quasi-bound states formed in a single spherical silicon quantum dot.

#### Acknowledgement

The authors would like thank K. Usami for the nc-Si dot deposition, and Y. Tsuchiya and S. Huang for discussions.

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