Two-Dimensional Numerical Simulation of Fermi-Level Pinning Phenomena Due to DX Centers in AlGaAs/GaAs HEMT's

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Abstract—Fermi-level pinning phenomena due to DX centers in AlGaAs/GaAs HEMT's are analyzed using two-dimensional numerical simulation based on a drift-diffusion model. A DX center model is introduced assuming Fermi-Dirac statistics for ionized donor density with the aluminum mole fraction dependence of the deep-donor energy level. The calculated results reveal that the decrease in transconductance of AlGaAs/GaAs HEMT's in a high gate-bias region is caused by the existence of DX centers. This is because the Fermi level is pinned at deep donor levels in the n-AlGaAs layer. Furthermore, the superiority of AlGaAs/InGaAs pseudomorphic HEMT's is discussed in terms of the Fermi-level pinning.

I. Introduction

THE existence of DX centers [1], [2] in Si-doped $Al_xGa_{1-x}As$ layers grown by MBE has been one of the most serious problems in developing heterostructured high-speed devices utilizing GaAs/AlGaAs systems. For example, persistent photoconductivity [1] in high electron mobility transistors (HEMT's) [3], [4], saturable pulsation of laser diodes [5], and persistent photocapacitance in p-n GaAs / AlGaAs heterojunction diodes [6] are attributed to DX centers. In AlGaAs/GaAs HEMT's, two-dimensional electron gas (2DEG) formed at the Al-GaAs/GaAs heterointerface is used as a channel. DX centers have a large influence on the electron transfer between AlGaAs and GaAs layers [7]-[9], resulting in the saturation in sheet carrier density of 2DEG [10]. Therefore, it is important to suppress the influence of DX centers in order to obtain further improvement in Al-GaAs/GaAs HEMT performance.

Up to now, two-dimensional device simulators based on a classical drift-diffusion model have been used as powerful design tools. For the past few years, two-dimensional numerical models for HEMT's [11]-[13] have been developed based on the drift-diffusion approach. In addition, the velocity overshoot effect [12] and the surface Fermi-level pinning effect [13] have been investigated in AlGaAs/GaAs HEMT's. Recently, several authors [14]-[16] have reported one-dimensional analysis of charge control in HEMT's, taking DX centers into ac-

Manuscript received December 6, 1988; revised March 20, 1989. The authors are with the Central Research Laboratory, Hitachi, Ltd., Kokubunji, Tokyo 185, Japan. IEEE Log Number 8929683. count. Park et al. [15] have shown good agreement between the calculated and experimental gate capacitance-voltage characteristics of AlGaAs/GaAs HEMT's. However, these calculations have been performed under a zero source-drain bias condition, and two-dimensional distributions of electrostatic potential and electron current in submicrometer-gate HEMT's have not been taken into consideration.

The present paper reports a full two-dimensional analysis of Fermi-level pinning phenomena due to DX centers in AlGaAs/GaAs HEMT's. We introduce a DX center model where the energy of deep donor levels varies with aluminum mole fraction. DX centers are shown to pin the Fermi level, thus greatly decreasing the transconductance of AlGaAs/GaAs HEMT's. Furthermore, the superior performance of InGaAs/AlGaAs pseudomorphic HEMT's as compared to that of conventional Al-GaAs/GaAs HEMT's is discussed in terms of the disappearance of the Fermi-level pinning phenomena.

II. DEVICE MODELING

The motion of carriers in HEMT's is analyzed by a classical drift-diffusion approach. The dependent variables are the electron density n, the hole density p, and the electrostatic potential ψ . Basic equations are described as follows:

1) Poisson equation

$$\nabla \cdot (\epsilon \nabla \psi) = -q(N_D^+ - N_A^- + p - n) \tag{1}$$

where q is the magnitude of an electron charge, ϵ is the dielectric constant, and N_D^+ and N_A^- are the concentrations of ionized donors and acceptors. It is assumed that donors and acceptors in GaAs layers are fully ionized, except that the ionized donor density in n-AlGaAs layers depends on the aluminum mole fraction as explained later.

2) Current continuity equation

$$\nabla \cdot J_n = q \cdot R \tag{2}$$

$$\nabla \cdot J_p = -q \cdot R \tag{3}$$

where J_n and J_p are the electron current density vector and the hole current density vector, and R is the Shockley-Read-Hall recombination rate with an assumed carrier lifetime of 1.0×10^{-9} s.

3) Current density equation

$$J_n = -q \mu_n n \nabla \phi_n \tag{4}$$

$$J_n = -q \mu_n p \nabla \phi_n \tag{5}$$

where μ_n and μ_p are the carrier mobilities for electrons and holes, and ϕ_n and ϕ_p are the electron and hole quasi-Fermi potentials, respectively. These equations are discretized by the finite-difference method and solved self-consistently using the Gummel scheme [17].

The electron and hole densities are given by the following expressions:

$$n = n_i \cdot \exp\left[\left\{\left(\psi + \theta - \gamma_n\right) - \phi_n\right\} / V_T\right] \qquad (6)$$

$$p = n_i \cdot \exp\left[\left\{\phi_p - (\psi + \theta - \gamma_p)\right\}/V_T\right] \quad (7)$$

where n_i is the intrinsic carrier density, V_T is the thermal voltage k_BT/q , and γ_n and γ_p are degeneracy parameters to take Fermi-Dirac statistics into account [17]. The band parameter θ is defined as the energy difference between the vacuum level and the intrinsic Fermi level

$$\theta = -\psi_0 + \chi + \frac{E_g}{2} - \frac{V_T}{2} \cdot \ln \frac{N_v}{N}$$
 (8)

where ψ_0 is the potential reference, χ is the electron affinity, E_g is the bandgap, and N_c and N_v are the effective densities of states in the conduction band and valence band, respectively.

The DX center model is introduced assuming Fermi-Dirac statistics for an ionized deep donor density in $n-Al_xGa_{1-x}$ As layers. The ionized donor density in n-AlGaAs layers is given by

 $N_D^+(x)$

$$= N_D \frac{1}{1 + g \cdot \exp\left[-\left(Ec - \Delta E_D(x) - \phi_n\right)/k_B T\right]}$$
(9)

where the degeneracy factor of the Si donor levels g, which equals 2, and the energy ΔE_D are dependent on the Al mole fraction x. The functional formula $\Delta E_D(x)$ is adjusted to fit the experimental data by Ishikawa $et\ al.$ [2]. For example, ΔE_D measured from the conduction band edge is assumed to be 60 meV at x=0.3. To contrast the Fermi-level pinning phenomena caused by DX centers with Fermi-level behaviors in non-DX-center structures, a perfect ionization model is introduced as a reference; 100 percent ionization of donors in n-AlGaAs layers is assumed as for those in n-GaAs layers.

$$N_D^+(x) = N_D. (10)$$

Hereafter, these two models (i.e., (9) and (10)) are used to analyze the Fermi-level pinning due to DX centers in HEMT's.

The Al mole fraction dependence and the doping concentration dependence of carrier mobilities are considered according to the experimental data of Neumann and Flohrer [18]. The conduction band and valence band discon-

tinuities ΔE_c and ΔE_v , respectively, in the GaAs/AlGaAs heterostructure are assumed to be 60 and 40 percent of the Γ -band energy gap difference, respectively.

The electron velocity overshoot effect is approximately introduced using the following piecewise linear velocity-field relation [12]:

$$v_n(E) = \mu_n \cdot E, \qquad (E \le v_s/\mu_n)$$

$$v_s, \qquad (E > v_s/\mu_n) \tag{11}$$

where the saturation velocity v_s is estimated through quantitative comparison with experimental data as discussed later.

III. CALCULATED RESULTS

A. Influence of DX Centers on AlGaAs / GaAs HEMT Performance

Fig. 1 shows a schematic cross section of the analyzed 0.5- μ m-gate HEMT. It has a 0.015- μ m-thick undoped AlGaAs surface layer [19], a 0.025- μ m-thick highly doped n-AlGaAs layer, and a 0.002- μ m-thick undoped AlGaAs buffer layer. The doping concentration in each layer used for the calculation is listed in Table I. The source and drain alloy regions are modeled by heavily doped n⁺-GaAs, and the semi-insulating substrate by a lightly doped p⁻-GaAs layer. The electron mobility in the slightly doped p⁻-GaAs channel is assumed to be 7200 cm²/(V · s), and that in the n-AlGaAs layer 1500 cm²/(V · s) according to the mobility model described in Section II. The Schottky-barrier height of the gate electrode is assumed to be 0.8 V.

Fig. 2 shows the two-dimensional electrostatic potential distribution and electron current distribution near the un-AlGaAs/p⁻-GaAs heterojunction calculated using the DX center model (see (9)). The bias conditions are source-drain voltage $V_{ds} = 1.0 \text{ V}$ and source-gate voltage $V_{gs} = 0 \text{ V}$. The length of each arrow shown in the figure is in proportion to the magnitude of the current density at that point. The electron current flows along the un-AlGaAs/p⁻-GaAs heterointerface where the two-dimensional electron gas is formed. It should be noted that the current component due to low-mobility electrons in the n-AlGaAs layer, i.e., so-called parallel conduction, is negligible. The thickness and doping concentration of this layer are designed so that a charge-neutral region does not exist in this region.

The full transistor characteristics of the HEMT with a gate width of 200 μ m calculated at 300 K are shown in Fig. 3; (a) shows results using the DX center model, and (b) using the perfect ionization model. In these calculations, the saturation velocity of electrons v_s is assumed to be 2.0×10^7 cm/s. The validity of this estimation is checked in the next section. It is found that the saturation current calculated using the DX center model is much smaller than that using the perfect ionization model.

In Figs. 4(a) and (b), drain current and transconductance calculated at drain bias $V_{ds} = 1.2$ V are shown as functions of gate bias. Calculated results using the DX

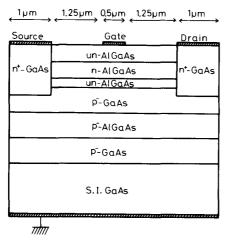


Fig. 1. A schematic cross-sectional view of the analyzed 0.5- μ m-gate AlGaAs/GaAs HEMT.

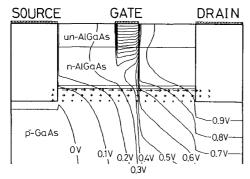


Fig. 2. Two-dimensional electrostatic potential distribution and electron current distribution near the AlGaAs/GaAs heterojunction for $V_{ds}=1.0$ V and $V_{gs}=0$ V. Electrostatic potentials are shown by contour lines with an interval of 0.1 V.

Layer	Doping concentration(c m ⁻³)
un(p) - AlGaAs	1 × 1 0 13
n * - A 1 G a A s	2 × 1 0 18
un(p) - A 1 G a A s	1 × 1 0 13
p G a A s	1 × 1 0 14
p A 1 G a A s	1 × 1 0 14
p G a A s	1 × 1 0 14
S. I G a A s	1 × 1 0 *
Source, Drain n + - G a A s	2 × 1 0 1 6

center model (solid lines) are compared to those using the perfect ionization model (dashed lines). As shown in Fig. 4(a), the drain currents calculated using these two models are nearly equal at the threshold voltage, but the difference between them increases when the gate bias approaches zero. The drain current using the DX center

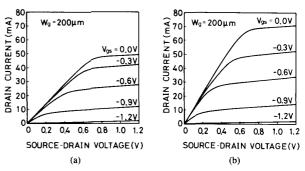
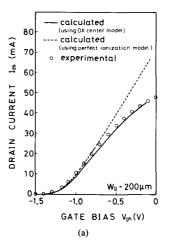


Fig. 3. The full transistor characteristics of the HEMT with a gate width of 200 μ m calculated at 300 K. (a) Using the DX center model. (b) Using the perfect ionization model.



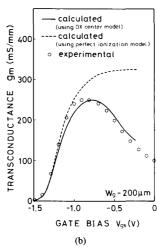


Fig. 4. Gate bias dependence of calculated and experimental (a) drain current I_{ds} and (b) transconductance g_{m} . Solid lines represent the calculated results using the DX center model, and broken lines using the perfect ionization model. The experimental data are shown by open circles.

model tends to saturate, and the transconductance falls off rapidly near $V_{gs} = 0$ V as shown in Fig. 4(b). These results show excellent agreement, quantitatively and quali-

tatively, with the experimental data (open circles), which are obtained from fabricated 0.5-μm-gate HEMT's with the same structural parameters as those used in the calculations.

As shown above, DX centers in the n-AlGaAs layer largely decrease the transconductance of AlGaAs/GaAs HEMT's. Physically speaking, such a decrease in transconductance can be explained by Fermi-level pinning due to DX centers. This phenomenon is clarified in Section IV-A. Furthermore, the superior features of Al-GaAs/InGaAs pseudomorphic HEMT's are discussed from the viewpoint of Fermi-level pinning in Section IV-

B. Velocity Overshoot Effect in AlGaAs / GaAs HEMT's

For the two-dimensional electron gas at the Al-GaAs/GaAs heterointerface, several Monte Carlo simulations have been carried out to obtain the electron velocity-electric field relation. For example, a calculated drift velocity of 1.5×10^7 cm/s at an electric field of 5 kV/cm has been reported for a uniform two-dimensional electron gas at 300 K [20]. In the present calculation, the piecewise velocity-field relation is employed. In this section, the saturation velocity v_s is estimated from the calculated drain current and transconductance values.

Fig. 5(a) shows the calculated drain current as a function of gate bias with various values of v_s (solid lines) and experimental values (open circles). In the same way, Fig. 5(b) shows the calculated transconductances and the experimental values. From these figures, it is inferred that $v_s = 2.0 \times 10^7$ cm/s, which is consistent with the values reported so far [16], [19], [21], [22]. This value seems to be slightly larger than that estimated from the Monte Carlo simulations. In the HEMT structure, the electrostatic potential steeply drops near the drain side edge of the gate electrode, as shown in Fig. 2. A high electric field appears over a very short length (about $0.05 \mu m$) of the channel, and the maximum electric field in this region is higher than 20 kV/cm. The electron velocity in this small region is inferred to exceed the drift velocity in a uniform channel obtained from Monte Carlo simulations. The saturation velocity in (11) can be interpreted as an average over the whole channel. Considering the local velocity overshoot in the channel, the v_s value of 2.0×10^7 cm/s estimated in this section is considered to be adequate.

IV. DISCUSSION

A. Fermi-Level Pinning Due to DX Centers

In this section, Fermi-level pinning due to DX centers is discussed. The calculated sheet electron concentration of the two-dimensional electron gas $n_s^{2\text{DEG}}$ is shown in Fig. 6 as a function of gate bias. The increase in $n_s^{\rm 2DEG}$ calculated using the DX center model (solid line) is suppressed near zero bias, i.e., the change in n_s^{2DEG} by gate bias becomes small. This suppression of maximum n_s^{2DEG} brings about the decrease in transconductance described in Section III-A. In the perfect ionization model (broken line),

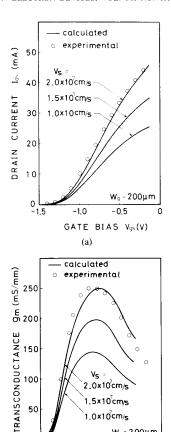


Fig. 5. Calculated (a) drain current and (b) transconductance as functions of gate bias with various values of v_s . Experimental results are plotted by open circles.

(b)

1.0

1.0x10⁷cm/s

GATE BIAS Vgs (V)

Wg = 200 µm

-0.5

50

0

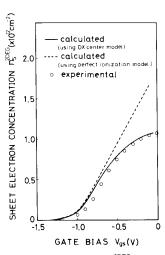


Fig. 6. Calculated electron concentration $n_s^{\rm 2DEG}$ accumulating at the heterointerface as a function of gate bias. Solid lines represent the calculated results using the DX center model, and broken lines using the perfect ionization model. Experimental results are plotted by open circles.

 $n_s^{\rm 2DEG}$ depends almost linearly on gate bias, and the maximum $n_s^{\rm 2DEG}$ is about twice that using the DX center model. Experimental values are shown with open circles, and these agree well with the calculated values using the DX center model.

Conduction band-edge profiles as a function of depth are shown in Fig. 7. The calculated results using the DX center model and the perfect ionization model at thermal equilibrium are shown in Fig. 7(a) and (b), respectively. The deep donors in the n-AlGaAs layer, namely DX centers, are indicated by the broken line in Fig. 7(a). The Fermi level (thin solid line) shown in Fig. 7(a) is located near the DX centers in the n-AlGaAs layer. In contrast, that shown in Fig. 7(b) is located near the conduction band edge. It should be noted that DX centers act as effective conduction band edge.

The calculated electron quasi-Fermi potential ϕ_n measured from the conduction-band minimum is shown in Fig. 8 as a function of gate bias. For gate bias $V_{gs} < -1.0$ V, the electron quasi-Fermi potential ϕ_n calculated using the DX center model (solid line) is almost the same as that calculated using the perfect ionization model (broken line). They begin to separate at about $V_{gs} = -0.8$ V. Such ϕ_n differences give rise to the difference between the n_s^{2DEG} values obtained from the two model calculations (see Fig. 6). At near zero bias, ϕ_n calculated using the DX center model approaches the deep-donor energy level ΔE_D and not the conduction band edge. This is the Fermilevel pinning due to DX centers, which suppresses the maximum n_s^{2DEG} and g_m .

Electron distributions are shown in Fig. 9 for the same bias conditions used in Fig. 7. It can be seen that the electron concentration of the 2DEG calculated using the DX center model (solid line) becomes smaller than that using the perfect ionization model (broken line) because of this Fermi-level pinning phenomenon.

As shown above, DX centers in the n-AlGaAs layer cause Fermi-level pinning resulting in a large decrease in transconductance of AlGaAs/GaAs HEMT's. In Section IV-B, an AlGaAs/InGaAs pseudomorphic HEMT structure is analyzed in terms of the Fermi-level pinning phenomenon.

B. Discussion on Pseudomorphic AlGaAs / InGaAs HEMT's

Recently, much attention has been paid to Al-GaAs/InGaAs pseudomorphic HEMT's [23], [24]. In this structure, a large conduction-band discontinuity can be obtained by using a small Al mole fraction (x < 0.2) where DX centers are not created in n-AlGaAs layers. Thus, the AlGaAs/InGaAs pseudomorphic HEMT structure is attractive for eliminating device performance degradation caused by DX centers. In this section, we discuss the performance of AlGaAs/InGaAs pseudomorphic HEMT's compared with that of conventional Al-GaAs/GaAs HEMT's.

The AlGaAs/InGaAs pseudomorphic HEMT analyzed here has the same structure as the AlGaAs/GaAs HEMT

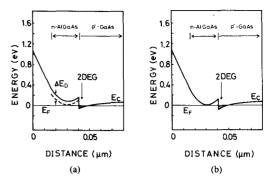


Fig. 7. Calculated energy band diagram as a function of depth in thermal equilibrium. (a) Using the DX center model. (b) Using the perfect ionization model.

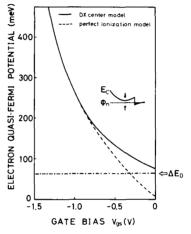


Fig. 8. Calculated electron quasi-Fermi potential measured from conduction-band minimum as a function of gate bias. Solid lines represent the calculated results using the DX center model, and broken lines using the perfect ionization model.

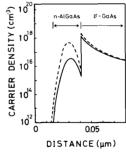


Fig. 9. Calculated electron distribution as a function of depth in thermal equilibrium.

investigated above except for the following two points. The Al mole fractions of the Al_{0.3} Ga_{0.7} As layers are all changed with 0.15, and p⁻-GaAs layer where the two-dimensional electron gas is formed is replaced by a p⁻-InGaAs layer; the In mole fraction is determined to be 0.25 so that the pinchoff voltage of the analyzed pseudomorphic HEMT is nearly equal to that of the Al-

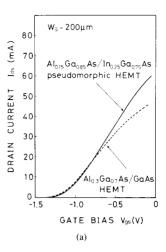
GaAs/GaAs HEMT. The energy bandgap shifts in the InGaAs layer due to elastic strain produced by the lattice mismatch is introduced according to [25] and [26]; the conduction band discontinuity in the Al_{0.15}Ga_{0.85}As/ In_{0.25}Ga_{0.75}As heterostructure is estimated to be 231 meV. In the p⁻-In_{0.25}Ga_{0.75}As layer, the electron effective mass is assumed to be $0.056 m_0$ and the electron mobility is $7200 \text{ cm}^2/(\text{V} \cdot \text{s})$, the same as for the p⁻-GaAs layer. In this calculation, we assume (9) for the ionized donor density in the n⁺-Al_{0.15}Ga_{0.85}As layer. The energy level of the Si donor, ΔE_D , in the n⁺-Al_{0.15}Ga_{0.85}As layer is 4 meV [2]. For electron saturation velocity in InGaAs layers, some experimental values have been reported [23], [27]. The purpose of this analysis is to investigate the Fermi-level pinning phenomena. Therefore, the same saturation velocity of 2.0×10^7 cm/s is assumed so that we can directly compare device performance with and without the DX centers' effect; this assumption is considered to be valid, according to [28].

Figs. 10(a) and (b) shows drain current and transconductance as functions of gate bias. The solid lines represent the calculated results for the AlGaAs/InGaAs pseudomorphic HEMT, and the broken lines for the AlGaAs/GaAs HEMT. It should be noted that improvements in the drain current and transconductance of the AlGaAs/InGaAs pseudomorphic HEMT can be seen near $V_{gs} = 0$ V. The reason for these improvements is that the Fermi level is not pinned at such deep levels as DX centers in the n-Al_{0.3}Ga_{0.7}As ($\Delta E_D = 60$ meV) and can rise to near the conduction band edge. Owing to the disappearance of Fermi-level pinning, the maximum transconductance can be enhanced to 325 mS/mm. This is almost the same value as obtained using the perfect ionization model in Section III (see broken line in Fig. 4(b)).

As discussed above, the Fermi-level pinning phenomena disappear in AlGaAs/InGaAs pseudomorphic HEMT's. It should be noted that the transconductance is enhanced even though the same electron mobility and saturation velocity are assumed in the InGaAs layer. This improvement results only from the disappearance of the Fermi-level pinning phenomena due to DX centers.

V. Conclusion

Fermi-level pinning phenomena due to DX centers in AlGaAs/GaAs HEMT's have been theoretically analyzed using two-dimensional numerical calculations based on the drift-diffusion model. The DX center model has been introduced assuming Fermi-Dirac statistics for ionized donor density with the Al mole fraction dependence of donor levels in n-AlGaAs layers. The calculated results using the DX center model are in excellent agreement with the experimental data. It has been revealed that the Fermi level is pinned by DX centers and that maximum transconductance of AlGaAs/GaAs HEMT's is very much decreased. The superiority of AlGaAs/InGaAs pseudomorphic HEMT's has been demonstrated. It has been shown that Fermi-level pinning phenomena disappear in this structure. The maximum transconductance of Al-



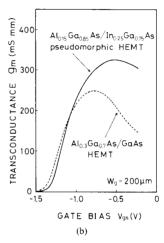


Fig. 10. Gate bias dependence of (a) drain current and (b) transconductance g_m . Solid lines represent the calculated results for the Al-GaAs/InGaAs HEMT, and broken lines for the AlGaAs/GaAs HEMT.

GaAs/InGaAs pseudomorphic HEMT's is enhanced even though the same values of mobility and saturation velocity as used in the conventional AlGaAs/GaAs HEMT's are assumed.

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REFERENCES

- [1] D. V. Lang, R. A. Logan, and M. Jaros, "Trapping characteristics and a donor-complex (DX) model for the persistent-photoconductivity trapping center in Te-doped Al_xGa_{1-x}As," *Phys. Rev. B*, vol. 19, pp. 1015–1030, 1979.
- [2] T. Ishikawa, J. Saito, S. Sasa, and S. Hiyamizu, "Electrical properties of Si-doped Al_xGa_{1-x}As layers grown by MBE," *Japan. J. Appl. Phys.*, vol. 21, no. 11, pp. L675-L676, 1982.
- [3] T. Mimura, S. Hiyamizu, T. Fujii, and K. Nanbu, "A new field effect transistor with selectively doped GaAs/n-Al_xGa_{1-x}As heterojunctions," *Japan. J. Appl. Phys.*, vol. 19, pp. L225-L227, 1980.

- [4] S. Hiyamizu, T. Mimura, T. Fujii, and K. Nanbu, "High mobility of two-dimensional electrons at the GaAs/n-AlGaAs heterojunction interface," Appl. Phys. Lett., vol. 37, pp. 805-807, 1980.
- [5] J. L. Merz, J. P. van der Ziel, and R. A. Logan, "Saturable optical absorption of the deep Te-complex center in Al_{0.4} Ga_{0.6} As," Phys. Rev. B, vol. 20, pp. 654-663, 1979.
- [6] M. I. Nathan, S. Tiwari, P. M. Mooney, and S. L. Wright, "DX centers in AlGaAs p-n heterojunctions and heterojunction bipolar transistors," J. Appl. Phys., vol. 62, pp. 3234-3236, 1987.
- [7] P. Godts, E. Constant, J. Zimmermann, and D. Depreeuw, "Investigation of influence of DX centres on HEMT operation at room temperature," *Electron. Lett.*, vol. 24, pp. 937-938, 1988.
- [8] M. I. Nathan, P. M. Mooney, P. M. Solomon, and S. L. Wright, "Room-temperature electron trapping in Al_{0.35} Ga_{0.65} As/GaAs modulation-doped field-effect transistors," *Appl. Phys. Lett.*, vol. 47, pp. 628-630, 1985.
- [9] A. Kastalsky and R. A. Kiehl, "On the low-temperature degradation of (AlGa) As/GaAs modulation-doped field-effect transistors," IEEE Trans. Electron Devices, vol. ED-33, no. 3, pp. 414-423, 1986.
 [10] K. Hirakawa, H. Sakaki, and J. Yoshino, "Concentration of electrons of the content of the content
- [10] K. Hirakawa, H. Sakaki, and J. Yoshino, "Concentration of electrons in selectively doped GaAlAs/GaAs heterojunction and its dependence on spacer-layer thickness and gate electric field," *Appl. Phys. Lett.*, vol. 45, pp. 253-255, 1984.
 [11] D. Loret, "Two-dimensional numerical model for the high electron
- [11] D. Loret, "Two-dimensional numerical model for the high electron mobility transistor," Solid-State Electron., vol. 30, no. 11, pp. 1197– 1203, 1987.
- [12] J. Yoshida and M. Kurata, "Analysis of high electron mobility transistors based on a two-dimensional numerical model," *IEEE Electron Device Lett.*, vol. EDL-5, no. 12, pp. 508-510, 1984.
- [13] J. Y.-F. Tang, "Two-dimensional simulation of MODFET and GaAs gate heterojunction FET's," *IEEE Trans. Electron Devices*, vol. ED-32, no. 9, pp. 1817-1823, 1985.
- [14] M. J. Moloney, F. Ponse, and H. Morkoç, "Gate capacitance-voltage characteristics of MODFET's: Its effect on transconductance," *IEEE Trans. Electron Devices*, vol. ED-32, no. 9, pp. 1675-1684, 1985
- [15] K. Park, H. B. Kim, and K. D. Kwack, "A model for the capacitance-voltage characteristics of MODFET's," *IEEE Trans. Electron Devices*, vol. ED-34, no. 12, pp. 2422-2427, 1987.
 [16] Y. Ando and T. Itoh, "Analysis of charge control in pseudomorphic
- [16] Y. Ando and T. Itoh, "Analysis of charge control in pseudomorphic two-dimensional electron gas field-effect transistors," *IEEE Trans. Electron Devices*, vol. 35, no. 12, pp. 2295-2301, 1988.
 [17] T. Ohtoshi et al., "A two-dimensional device simulator of semicon-
- [17] T. Ohtoshi et al., "A two-dimensional device simulator of semiconductor lasers," Solid-State Electron., vol. 30, no. 6, pp. 627-638, 1987.
- [18] H. Neumann and U. Flohrer, "Electron mobility in Al_xGa_{1-x}As," Phys. Status Solidi, vol. 25a, pp. K145-K147, 1974.
- [19] H. Hida, K. Ohata, Y. Suzuki, and H. Toyoshima, "A new low-noise AlGaAs/GaAs 2DEG FET with a surface undoped layer," *IEEE Trans. Electron Devices*, vol. ED-33, no. 5, pp. 601-607, 1986.
- [20] K. Yokoyama and K. Hess, "Calculation of warm electron transport in AlGaAs/GaAs single heterostructures using a Monte Carlo method." J. Appl. Phys., vol. 59, no. 11, pp. 3798-3802, 1986.
- method," J. Appl. Phys., vol. 59, no. 11, pp. 3798-3802, 1986.

 [21] Y. Takanashi and N. Kobayashi, "AlGaAs/GaAs 2-DEGFET's fabricated from MO-CVD wafers," IEEE Electron Device Lett., vol. EDL-6, no. 3, pp. 154-156, 1985.
- [22] W. P. Hong and P. K. Bhattacharya, "High-field transport in In-GaAs/InAlAs modulation-doped heterostructures," *IEEE Trans. Electron Devices*, vol. ED-34, no. 7, pp. 1491-1495, 1987.
- [23] A. Ketterson et al., "High transconductance InGaAs/AlGaAs pseudomorphic modulation-doped field-effect transistors," IEEE Electron Device Lett., vol. EDL-6, no. 12, pp. 628-630, 1985.
- [24] T. Henderson et al., "Microwave performance of a quarter-micrometer gate low-noise pseudomorphic InGaAs/AlGaAs modulationdoped field effect transistor," IEEE Electron Device Lett., vol. EDI -7, no. 12, pp. 649-651, 1986.
- EDL-7, no. 12, pp. 649-651, 1986.

 [25] H. Asai and K. Oe, "Energy band-gap shift with elastic strain in Ga_xIn_{1-x}P epitaxial layers on (001) GaAs substrates," J. Appl. Phys., vol. 54, no. 4, pp. 2052-2056, 1983.
- [26] K. Nishi, K. Hirose, and T. Mizutani, "Optical characterization of InGaAs-InAlAs strained-layer superlattices grown by molecular beam epitaxy," Appl. Phys. Lett., vol. 49, pp. 794-795, 1986.
- [27] T. S. Henderson, W. T. Masselink, W. Kopp, and H. Morkoç, "Determination of carrier saturation velocity in high-performance In_yGa_{1-y}As/Al_xGa_{1-x}As modulation-doped field-effect transistors (0 ≤ y ≤ 0.2)," *IEEE Electron Device Lett.*, vol. EDL-7, no. 5, pp. 228-290, 1986.

[28] L. W. Nguyen, D. C. Radulescu, P. W. Tasker, W. J. Schaff, and L. F. Eastman, "0.2-μm gate-length atomic-planar doped pseudomorphic Al_{0.3} Ga_{0.7} As/In_{0.25} Ga_{0.75} As MODFET's with f_T over 120 GHz," *IEEE Electron Device Lett.*, vol. 9, no. 8, pp. 374-376, 1988.



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