

Iterative Detection Aided H.264 Wireless Video Telephony Using Irregular Convolutional Codes

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Abstract—In this paper we evaluate the performance of a Data-Partitioned (DP) H.264 coded video transmission system using Unequal Error Protection (UEP) Irregular Convolutional Codes (IRCC). Using UEP, perceptually more important bits are provided with more strong protection relative to less important bits. An iterative detection aided combination of IRCC and a rate-1 precoder was used to improve the overall BER performance and to enhance the objective video quality expressed in terms of Peak Signal-to-noise Ratio (PSNR). The effect of different error protection schemes on the attainable system performance is demonstrated, while keeping the overall bit-rate budget constant for the transmission of DP H.264 source coded video over correlated narrowband Rayleigh fading channels. In this paper we exploited the high design flexibility of IRCCs, which constitutes a family of different rate subcodes, while maintaining an excellent iterative decoding convergence performance. Additionally, due to the use of different-rate subcodes, IRCCs have the capability of providing UEP for the H.264 coded video stream. An EXIT chart matching procedure was used for the design of our specific IRCC. Additionally, EXIT charts were used for analysing the attainable system performance of various error protection schemes employed. Explicitly, our experimental results show that the proposed UEP scheme using IRCC outperforms its Equal Error Protection (EEP) counterpart employing regular convolutional codes by about 0.5 dB E_b/N_0 at the PSNR degradation point of 1 dB.

I. MOTIVATION AND BACKGROUND

Multimedia source coded information, such as speech, audio and video typically exhibit unequal sensitivity to channel errors [1, 2]. Therefore, Unequal Error Protection (UEP) is used by exploiting the innate UEP capability of Irregular Convolutional Codes (IRCCs) due to the employment of various constituent codes having different coding rates to ensure that the perceptually more important bits suffer from a lower number of channel errors. The construction of IRCCs was studied by Tuchler and Hagenauer [3], where a family of different convolutional codes having various code-rates were designed with the aid of EXIT charts for achieving an improved iterative decoding behaviour for a serially concatenated system. For wireless multimedia applications maintaining both a high compression efficiency as well as a high integrity, while communicating over heterogeneous communication networks is of primary importance. Therefore, to evaluate the performance of our proposed error protection schemes, we used the high-compression efficient and network-friendly H.264/AVC source codec [4], which employs various variable length coding and predictive coding techniques to achieve compression efficiency. However, these coding techniques make the compressed bit-stream more vulnerable to the effects of channel errors. Due to variable length coding a single bit error in the video stream may render the correct decoding of future code words impossible. Moreover, due to predictive coding the effects of channel errors are likely to be propagated to the neighbouring video blocks. Therefore, various error resilient techniques, such as Data Partitioning DP have been incorporated into the H.264/AVC codec in order to mitigate this error sensitivity problem. Additionally, various error resilient schemes have been proposed in [5], but the price paid is the reduced compression efficiency and increased computational

complexity. A serial concatenated code based iterative joint source-channel decoding procedure was presented in [6]. Moreover, an irregular variable length coding (IrVLC) scheme designed for near-capacity joint source and channel coding was presented in [7]. Likewise, [8] advocates the employment of state-of-the-art system design principles and the performance of burst-by-burst adaptive transceivers designed for interactive cellular and cordless video telephony. Instead of the traditional serial concatenation of the classic Variable Length Codes (VLC) with a channel code, a parallel concatenated coding scheme was presented in [9] where the VLCs were combined with a turbo code. A joint video and channel coding system employing an iteratively decoded serial concatenation of a Vector Quantisation (VQ) based video codec and a Trellis-Coded Modulation (TCM) scheme was proposed in [7]. The attainable performance improvements of Iterative Source Channel Decoding (ISCD) Scheme employing a specific bit-to-symbol mapping scheme were analysed in [10], in the context of H.264 encoded video signals transmission using UEP and Sphere Packing (SP) modulation aided Differential Space Time Spreading (DSTS) in a non-coherently detected, diversity-aided multiuser scenario. Furthermore, the performance analysis of diverse low-complexity Short Block Codes (SBCs) for the transmission of H.264 coded video stream over correlated narrowband Rayleigh fading channels was presented in [11].

Against this background, the novel contribution of our work is the UEP of DP-aided and H.264/AVC coded stream using EXIT chart-optimised IRCCs. More explicitly, we proposed a serially concatenated turbo transiver, consisting of an iterative combination of an outer IRCC and an inner rate-1 precoder.

The rest of the paper is organised as follows. An overview of H.264 Data Partitioning is provided in Section II. In Section III we portray our system model. The IRCC design strategy is outlined in Section IV, followed by an IRCC design example applied to our proposed system in Section V. Section VI provides the Extrinsic Information Transfer Chart (EXIT) analysis of the proposed error protection schemes. The performance of the proposed system is characterised with the aid of our simulation results in Section VII. Finally, we offer our conclusions in Section VIII.

II. H.264 DATA PARTITIONING

The H.264 DP scheme is designed to achieve an enhanced error resilience. Instead of the classic approach of coding all video parameters of a Macro-Block (MB) into a single bit-string representing a video-slice, the H.264 DP may generate three bit-strings per slice referred to as partitions, and hosting different-sensitivity parameters. This provides us with the ability to protect the different sensitivity classes based on their relative importance. The three different partitions generated by the DP mode of H.264 are,

- Type A: It contains the slice header information, motion vectors, quantisation parameters and MB types. When a type A partition is corrupted, the bits of the entire slice are dropped. In this

case the slice is marked as corrupted and the decoder will apply error concealment techniques using the previous decoded video segments.

- Type B: It carries both the intra-frame coded MB coefficients and the intra-MB coded Block Pattern (CBP) bits, representing blocks within a MB containing non-zero transform coded coefficients.
- Type C: It typically contains inter-frame Motion Compensated Error Residual (MCER) bits and inter-frame CBP bits for MBs encoded using inter-frame compensated prediction, as well as intra-frame CBP and intra-frame MCER bits for the MBs coded using the H.264 specific intra-frame prediction mode.

Therefore, in the H.264 video stream, partition A contains very important information. Similarly, the information contained in partition B and C has its own significance in terms of effect on video quality. If partition B of a slice is present along with A, the intra-frame MB update is added to the reconstructed frame, while if partition C is present, the MCER of a slice is reconstructed and added to the motion compensated slice. Recovery from inter-frame error propagation can only be achieved by encoding certain image regions in intra-frame mode by switching off inter-frame prediction for certain MBs, when no feedback channel is available. Typically a limited fraction of the MBs is encoded in the intra-frame coding mode, so that partition B typically hosts the lowest number of bits in an encoded slice.

III. SYSTEM OVERVIEW

The schematic of the proposed videophone arrangement is shown in Figure 1. At the transmitter side, the video sequence is compressed using the H.264/AVC video codec. Then, the output bit-stream representing a video frame consisting of K source coded bits x_k , $k = 1, 2, \dots, K$, is de-multiplexed into three different bit-streams, namely Stream A, Stream B and Stream C, containing the sequentially concatenated partitions of type A, B and C of all the slices per frame, respectively. The de-multiplexers binary output sequences x_a , x_b , and x_c , where we have $a = 1, 2, \dots, A$, $b = 1, 2, \dots, B$ and $c = 1, 2, \dots, C$, and $K = A+B+C$, are then concatenated according to a specific order, depending on the choice of the error protection scheme applied to the bit-string x_i . Subsequently, the concatenated bit-string is coded using an IRCC having a specific code rate into the coded string x'_i . The coded string x'_i is then interleaved using the bit-interleaver Π of Figure 1, into the interleaved sequence \bar{x}_i , which is then encoded by the rate-1 precoder, characterised by the generator polynomial of $1/(1 + D)$ [12], before transmission. In the iterative decoder of Figure 1, the extent of the statistical independence of the extrinsic information provided by an interleaver is always related to its length [13]. Therefore instead of independently performing the iterative decoding operation on the various frame slices, we integrated all the bits generated by the MBs of the slices of each partition within a given video frame. The integrated bit-strings of each partition are then subsequently concatenated into a single string, which results in a longer interleaver and hence improves the attainable iterative decoding performance without extending the video delay. The precoded bit-stream y_i is QPSK modulated and transmitted over a temporally correlated narrowband Rayleigh fading channel, associated with the normalised Doppler frequency of $f_d = f_D T_s = 0.01$, where f_D is the Doppler frequency and T_s is the symbol duration. At the receiver the soft-information obtained after QPSK demodulation is forwarded to the inner decoder. The extracted extrinsic information is then exchanged between the inner and outer decoder, in order to attain the lowest possible BER [14]. At the receiver the soft-information obtained after QPSK demodulation in the form of its Log-Likelihood

Ratio (LLR) representation $D_{M,a}$ is forwarded to the rate-1 precoder. The rate-1 precoder's decoder processes this input information and the fed back *a priori* information $L_{M,a}$ from the outer decoder in order to generate the *a posteriori* LLR values $L_{M,p}$. The rate-1 precoder's *a priori* LLR values $L_{M,a}$ are subtracted from its *a posteriori* LLR values $L_{M,p}$ to generate the extrinsic LLR values $L_{M,e}$, which are subsequently deinterleaved by the soft-bit interleaver of Figure 1, yielding $L_{D,a}$. Then, the soft bits $L_{D,a}$ are passed to the convolutional decoder, which uses the Log MAP algorithm of [15] to compute the *a posteriori* LLR values $L_{D,p}$. Observe in Figure 1 that $L_{M,a}$ is the interleaved version of $L_{D,e}$, which in turn is generated by subtracting the *a priori* information $L_{D,a}$ from the *a posteriori* information $L_{D,p}$. During iterative decoding the precoder's decoder exploits the *a priori* information for the sake of providing improved *a posteriori* LLR values for the outer channel decoder, which in turn exploits the input LLR values for the sake of providing improved *a priori* information for the precoder in the subsequent iteration. Further details about iterative decoding are provided in [16].

IV. IRREGULAR CONVOLUTIONAL CODE DESIGN

The details of IRCC design can be found in [3]. More explicitly an IRCC consisting of I'_n constituent subcodes is constructed by first selecting a convolutional mother code C_1 with rate- r_1 . The remaining $[I_n - 1]$ subcodes C_i of rate- $r_i > r_1$, where we have $i = 2 \dots I_n$, are obtained by puncturing, while subcodes C_i with rate- $r_i < r_1$ are created by adding more generators and by puncturing. In IRCCs K input bits are encoded into N coded bits, where each rate- r_i subcode encodes only a fraction ($\alpha_i r_i N$) of information bits and generates ($\alpha_i N$) encoded bits, with α_i representing the relative size of a fraction. The weighting coefficients α_i , $i = 1 \dots I_n$ and the target code rate of $R \in [0 \ 1]$ should satisfy,

$$\sum_{i=1}^{I_n} \alpha_i = 1, \text{ and } \sum_{i=1}^{I_n} \alpha_i r_i = R, \text{ where } \alpha_i \in [0 \ 1].$$

V. DESIGN EXAMPLE

In our design example we used a fixed overall coding rate of $R = 0.7$, which was assigned to the outer code, because we used a unit rate code as our inner code. As a benchmarker we used a half-rate memory-4 Recursive Systematic Convolutional Code (RSC) as our outer code defined by the generator polynomial $(1, g_1/g_0)$, where $g_0 = 1 + D + D^4$ and $g_1 = 1 + D^2 + D^3 + D^4$ are the feedback and feedforward polynomials, respectively. Then the overall RSC code rate of 0.7 was obtained by puncturing. Alternatively, for the IRCC design, we used the proposed $I_n = 17$ constituent subcodes of [3], having code rates $r_i = [0.10 \ 0.15 \ 0.20 \ 0.25 \ 0.30 \ 0.35 \ 0.40 \ 0.45 \ 0.50 \ 0.55 \ 0.60 \ 0.65 \ 0.70 \ 0.75 \ 0.80 \ 0.85 \ 0.90]$, respectively. We employed the EXIT chart matching algorithm of [17] and selected the target inner code transfer function recorded at $E_b/N_0 = 3 \text{ dB}$, which resulted in the weighting vector of $\alpha_i = [0.003112 \ 0.029389 \ 0 \ 0 \ 0 \ 0 \ 0 \ 0 \ 0 \ 0.291014 \ 0.657845 \ 0 \ 0 \ 0.0477998 \ 0]$ for the outer IRCC code. By concatenating the three resultant partitions per video frame into a specific order depending on the error protection scheme applied, we can control their relative error protection with the aid of IRCC coding, which provides a stronger error protection for the concatenated bit-stream portion coded by lower-rate subcodes relative to the bit-stream portion coded by the higher-rate subcodes.

VI. EXIT CHART ANALYSIS

The EXIT [18] function of both the IRCC and benchmarker RSC are shown in Figure 2. In addition to the beneficial UEP capability of the IRCC, it can be observed from the EXIT curves

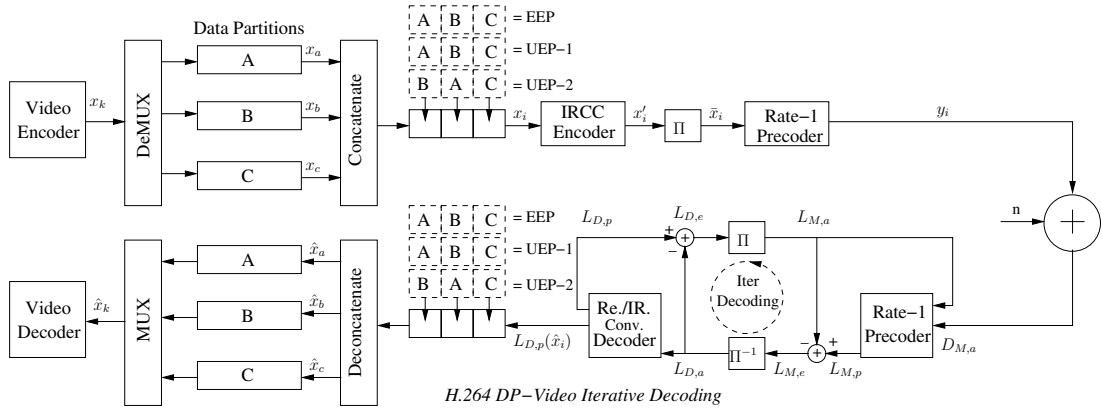


Fig. 1. The proposed system model.

TABLE I
CODE RATES FOR DIFFERENT ERROR PROTECTION SCHEMES

Protection Scheme	Concatenation order	Code Rate		
		Outer Code	Inner Code	Overall
EEP	[A-B-C]	RSC	Rate-1 Precoder	0.7
UEP-1	[A-B-C]	IRCC	Rate-1 Precoder	0.7
UEP-2	[B-A-C]	IRCC	Rate-1 Precoder	0.7

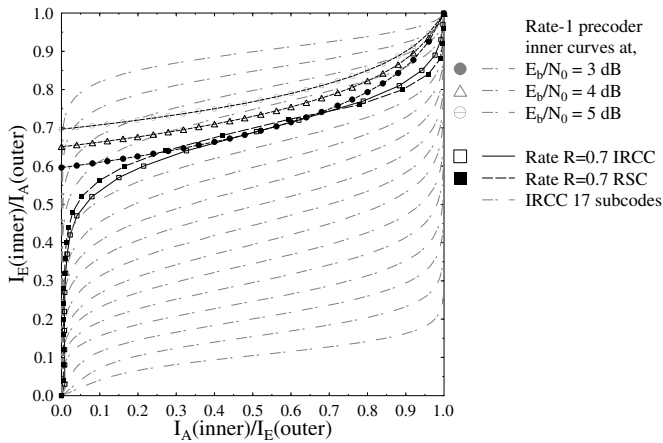


Fig. 2. The EXIT functions of 17 subcodes used in [17] along with rate-1 inner precoder curves and the employed regular/irregular outer code curves.

that there is a wider EXIT chart tunnel between the rate-1 inner precoder's curve and the outer irregular channel code's curve relative to the benchmarker scheme dispensing with the regular outer IRCC channel code. Therefore, given a limited number of affordable iterations, a relatively short interleaver and a wider EXIT tunnel, our designed 0.7-rate IRCC may be expected to perform better than the benchmarker scheme using the identical-rate RSC code. The actual decoding trajectories of the various error protection schemes employing the regular/irregular outer channel codes as well as using the rate-1 inner precoder detailed in Table I was recorded at $E_b/N_0 = 3 \text{ dB}$, 4 dB and 5 dB , as portrayed in Figures 3, 4 and 5, respectively, where I_A is the mutual information between the coded bits and the corresponding *a priori* LLR values input to the decoder, while I_E is the mutual information between the coded bits and the corresponding *a posteriori* LLR values output from the decoder. These trajectories were plotted by acquiring the mutual information between the hypothesized and soft-bit values at the input and output of both the inner and outer decoder during the bit-by-bit Monte-Carlo simulation of the iterative decoding algorithm. It may be inferred from the EXIT trajectories of Figures 3, 4 and 5 that as

expected, the convergence behaviour of the iterative decoding scheme was improved by the irregular outer channel codes, which provide a wider EXIT tunnel relative to the regular outer channel code, although they have the same code rates.

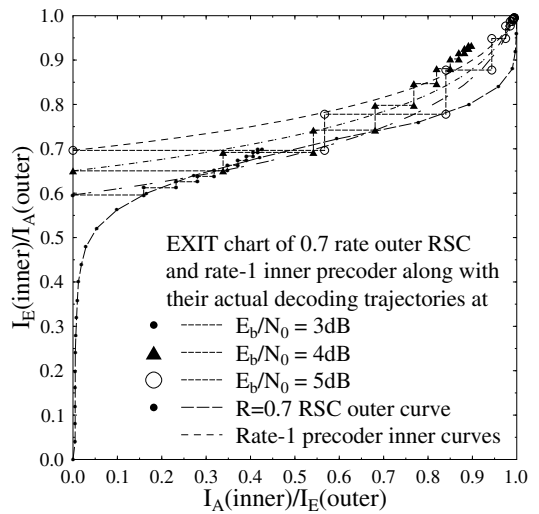


Fig. 3. The EXIT chart and decoding trajectories of the EEP scheme.

VII. SYSTEM PERFORMANCE RESULTS

The performance results of our proposed system are presented in this section. We used a 45 frame "Akiyo" video sequence [5] in (176x144)-pixel Quarter Common Intermediate Format (QCIF) as our test sequence. This test sequence was encoded using the H.264/AVC JM 13.2 reference video codec at 15 frames-per-second (*fps*) operated at the target bitrate of 64 kbps . Each QCIF frame was partitioned into 9 slices and each slice was composed of 11 MBs. The resultant video encoded clip consists of an intra-coded 'I' frame followed by 44 predicted 'P' frames, corresponding to a 3 seconds time-lag between two consecutive 'I' frames at 15 fps , in order to reduce inter-frame error propagation. Additionally, we incorporated intra-frame coded MB updates of three randomly dispersed MBs per frame in order to mitigate the effects of error propagation. The insertion of 'B' pictures results in an unacceptable loss of lip-synchronisation as a result of the corresponding delay incurred due to the bi-directionally predicted video coding operations [4] and hence was avoided. The remaining system parameters of our experimental setup are listed in Table II. With an interactive videophone scenario in mind, the error resilient encoding techniques, such as Flexible Macro-block Ordering (FMO) [4] and the employment of multiple reference frames for inter-frame motion compensation were turned off, because despite their

TABLE II
SYSTEMS PARAMETERS

System Parameters	Value
Channel Coding	Re./IR. conv. code
Over-all Code Rate	0.7
Modulation Scheme	QPSK
Number of Transmitters, N_t	1
Number of Receivers, N_r	1
Channel	Correlated Rayleigh Fading
Normalised Doppler Frequency	0.01
Interleaver Length	$\approx (64000/15)$
Source Coding	H.264/AVC
Bit Rate (Kbps)	64
Frame Rate (fps)	15
No of Slices/frame	9
No of MB's/Slice	11
Intra-frame MB update/frame	3

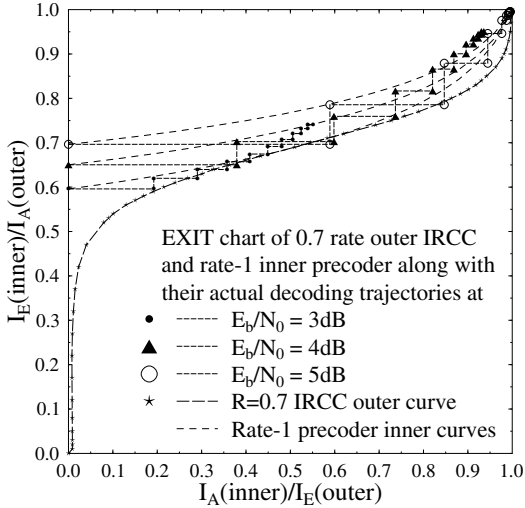


Fig. 4. The EXIT chart and decoding trajectories of the UEP-1 scheme.

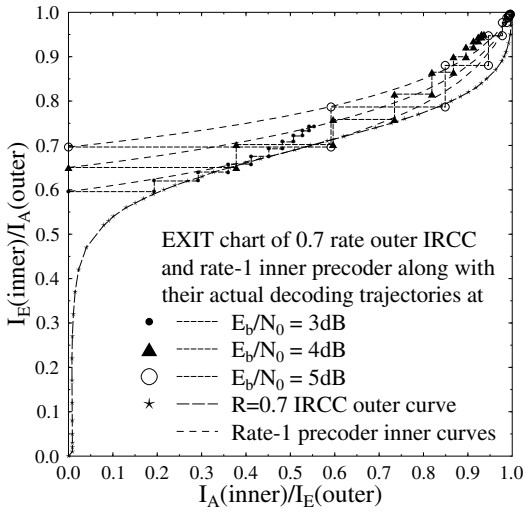


Fig. 5. The EXIT chart and decoding trajectories of the UEP-2 scheme.

substantially increased complexity they typically result in modest video performance improvements in low-motion head-and-shoulders video sequences, such as the "Akiyo" clip [4]. Additionally, only the immediately preceding frame was used for motion search, which results in a reduced computational complexity compared to using multiple reference frames and error concealment was performed using the motion vector recovery algorithm of [4]. Moreover, for the sake of reducing the computational complexity imposed, we limited the number of iterations between the outer code and rate-1 inner precoder to $I_t = 10$. For the sake of increasing the confidence in our results, we repeated each 45-frame experiment 160 times and averaged the generated results. The performance of the system was evaluated by keeping the same overall code rate as well as video rate for both the Equal Error Protection (EEP) and UEP coding schemes. In the UEP-1 scheme of Table I based on the specific concatenation order [A-B-C] of partition A, followed by partition B and then C, the highest error protection was provided for partition A, while partition B was more strongly protected than C. By contrast, the UEP-2 partitions arrangement [B-A-C] of Table I represents another error protection scheme, in which the highest level of error protection is provided for partition B, followed by partition A and C.

The rationale of the UEP-2 arrangement in comparison to the UEP-

1 scheme, where partition A had the strongest protection is that in low-motion video sequences the slices corrupted due to the loss of partition A are concealed using the motion vector recovery algorithm of [4], which results in less annoying artefacts due to their high correlation across the adjacent frames. However, the loss of partition B along with a correctly received partition A of a particular slice implies that the MV and header information received in partition A will be used to decode the MBs of this slice, encoded either in intra- or inter-frame prediction mode, while the all-important MBs of partition B containing intra-frame coded MB coefficients and intra-MB CBP bits were completely lost. Additionally, when the intra-frame coded MB updates are corrupted, this results in avalanche-like error propagation to the future video frames. Therefore, in low-motion video sequences partition B is also very important and hence its corruption results in noticeable PSNR degradation of the decoded video sequence. Hence, by providing more error protection to partition B, the correct reception of partition B is guaranteed for every correctly received partition A, which avoid error propagation to the future video frames due to predictive video coding. Furthermore, typically partition B has the lowest number of bits, therefore the employment of a strong subcode does not result in a significant increase of the overall rate. As a result, using the remaining fraction of the bit-rate budget, a reasonable protection can still be provided for partitions A and C.

Figure 6 presents the attainable BER performance of partitions A, B and C when using the EEP, UEP-1 and UEP-2 schemes of Table I, while employing regular/irregular outer channel codes. Naturally, the three partitions of the EEP scheme experience a similar BER, since they are protected equally with the same code rate. In the UEP-1 scheme the partitions B and C have a higher BER than partition A, which is due to their higher code rate relative to partition A. Likewise, partition A of the UEP-2 scheme of Table I has a higher BER relative to partition B, which in turn has a lower BER than partition C.

The performance trends expressed in terms of the PSNR versus E_b/N_0 curves are portrayed in Figures 7. It may be observed in Figure 7 that the UEP-2 scheme provides the best PSNR performance among the three different error protection schemes of Table I across the entire E_b/N_0 region considered. From the simulation results of Figure 7 it is observed that the UEP-1 scheme of Table I results in the worst PSNR performance, when employing of our 0.7-rate IRCC in conjunction with the rate-1 inner precoder. By contrast, an E_b/N_0 gain of 1 dB is attained using UEP-2 relative to UEP-1 at the PSNR degradation point of 1 dB. Additionally, using the UEP-2 scheme of Table I, an E_b/N_0 gain of about 0.5 dB may be achieved over the identical-rate EEP benchmarker scheme using the regular RSC channel code of rate 0.7. Finally, the subjective video quality achieved by the proposed EEP employing regular convolutional code

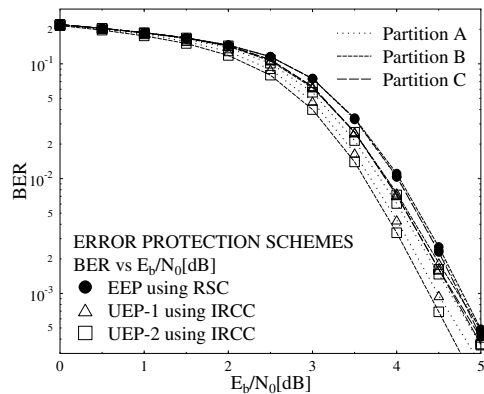


Fig. 6. BER performance of partitions A, B and C of the various error protection schemes.

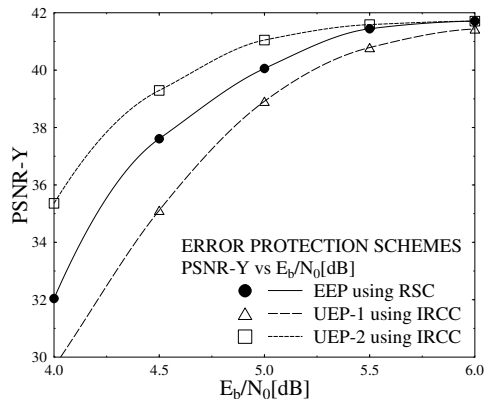


Fig. 7. PSNR-Y performance of the various error protection schemes.

along with UEP-1 and UEP-2 error protection schemes employing irregular convolutional code as outer code was recorded in Figure 8 at channel E_b/N_0 value of 4 dB. In order to have a fair subjective video quality comparison, we present both the average and cumulative-error results of both the luminance and chrominance components of the 30 "Akiyo" video test sequences described in Section VII, decoded using the H.264 video codec after transmission through our proposed system for each type of setup.



Fig. 8. Subjective video quality of the 45th "Akiyo" video sequence frame in terms of (from top) average and cumulative-error video quality using (from left) EEP, UEP-1 and UEP-2 error protection schemes summarised in Table I at $E_b/N_0 = 4$ dB.

VIII. CONCLUSIONS

In this paper we proposed UEP H.264/AVC coded video transmissions using IRCC. We considered various error protection schemes designed for the transmission of H.264/AVC coded video using a

serially concatenated turbo transceiver, consisting of an EXIT chart-optimised outer IRCC and an inner rate-1 precoder. Additionally, the convergence behaviour of the system was analysed using EXIT charts. We have demonstrated that by using UEP employing an appropriate bit-rate budget allocation to different partitions of the H.264/AVC coded video based on their relative importance resulted in useful PSNR improvements for the transmitted video sequence.

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