UNIVERSIDADE FEDERAL DO AMAZONAS DEPARTAMENTO DE CIÊNCIA DA COMPUTAÇÃO PROGRAMA DE PÓS-GRADUAÇÃO EM INFORMÁTICA

# EXPLOITING SAFETY PROPERTIES IN BOUNDED MODEL CHECKING FOR TEST CASES GENERATION OF C PROGRAMS

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# Outline

- 1. Introduction
- 2. Background
- 3. Related Work
- 4. Proposed Method
- **5.** Experimental Results
- 6. Conclusions and Future Work
- 7. Questions?

## **Software Applications**









# **Model Checking**

#### Model Checking

- Area of research in formal verification;
- Software categories.

#### The main challenges

- State space explosion problem;
- Integration with test environments;

#### And what are we proposing?

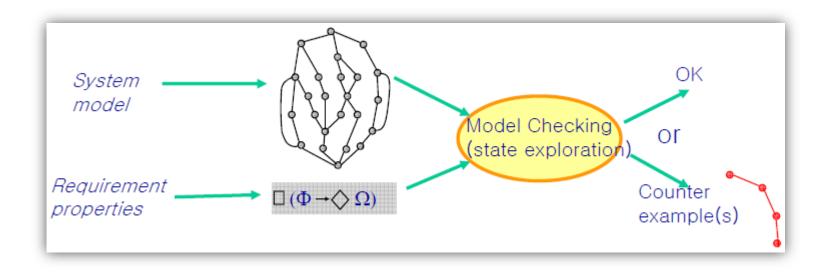
 According to [Baier and Katoen 2008]<sup>1</sup> safety properties are often characterized as "nothing bad should happen".



1- Principles of Model Checking. MIT Press. 2008

## What is Model Checking?

The procedure normally generates an exhaustive search in the state space model to determine whether a given "property" is valid or not [Baier and Katoen 2008]<sup>2</sup>.



2- Principles of Model Checking. MIT Press. 2008

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# **Efficient SMT-Based Bounded Model Checker (ESBMC)**

ESBMC<sup>3</sup> is a bounded model checker for embedded ANSI-C software based on SMT (Satisfiability Modulo Theories) solvers, which allows [Cordeiro et al. 2009] <sup>4</sup>:

- Out-of-bounds array indexing;
- Division by zero;
- ✓ Pointers safety
- Dynamic memory allocation;
- ✓ Data races;
- ✓ Deadlocks;
- Unwinding of the loops;
- Underflow e Overflow;
- Softwares Multi-threaded.

<sup>3-</sup> http://users.ecs.soton.ac.uk/lcc08r/esbmc/

<sup>4-</sup> SMT-Based BoundedModel Checking for Embedded ANSI-C Software. ASE 2009.

## **Exploting Safety Properties in Software Testing Strategies**



 Software testing is the process of executing a program with the goal of finding faults [Myers and Sandler 2004]<sup>5</sup>

The CUnit Framework

The problem is "how to creat test cases aimed at checking safety properties?"

5- The Art of Software Testing. John Wiley & Sons. 2004

# **Related Work**



**Test sequences generation from LUSTRE descriptions: GATEL.** [Marre e Arnould, 2000]<sup>6</sup>

Scenario-oriented modeling in Asml and its instrumentation for testing. [Barnett et al. 2003]<sup>7</sup>

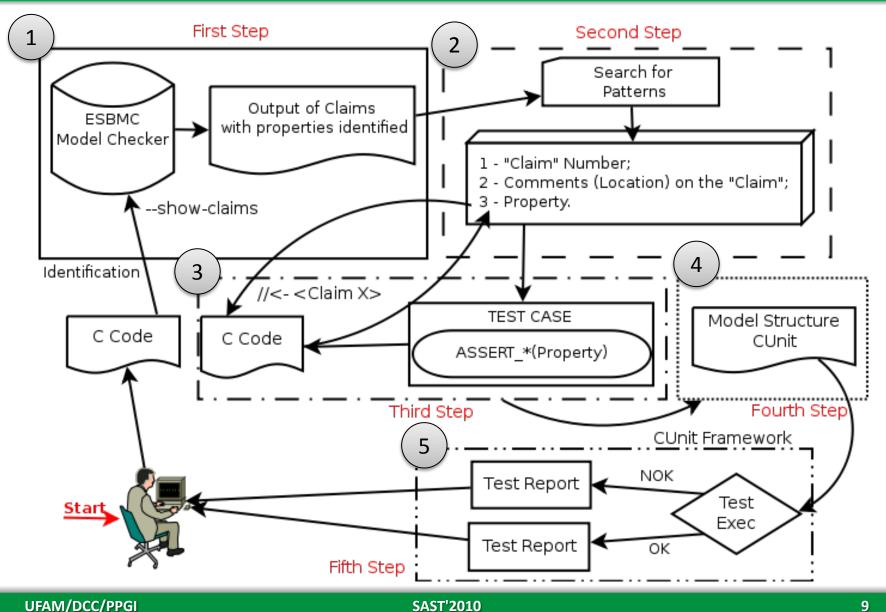
**Test generation with Autolink and Testcomposer.** [Schmitt et al. 2000]<sup>8</sup>

**Execution generated test cases: How to make systems code crash itself.** [Cadar and Engler 2005]<sup>9</sup>

- 7- Scenario-oriented modeling in Asml and its instrumentation for testing. SCESM. 2003
- 8- Test generation with Autolink and Testcomposer. SAM. 2000
- 9- Execution generated test cases: How to make systems code crash itself. 12th MCS, Stanford Technical Report CSTR 2005-04. 2005

<sup>6-</sup> Test sequences generation from LUSTRE descriptions: GATEL. ASE. 2000

#### **Proposed Method**



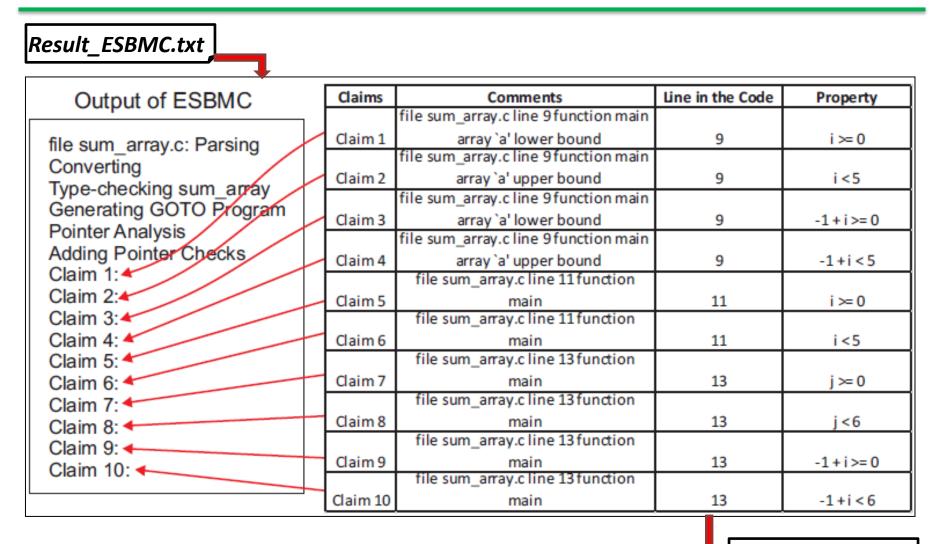
#### In order to explain the main steps of the proposed...

```
1 #include <stdio.h>
<sup>2</sup> int a[5], b[6];
  int main(){
3
   int i, j, temp; a[0]=1;
4
   for (i=1; i<5; i++)
\mathbf{5}
       a[i] = a[i-1] + i;
6
      b[0] = 1;
\mathbf{7}
    temp = a[i]*(i+1);
8
       for (j=1; j < temp; j++) \{ b[j] = b[i-1] + (temp * 2); \}
9
10
11
```

## First step: Identification of Safety Properties

Result\_ESBMC.txt

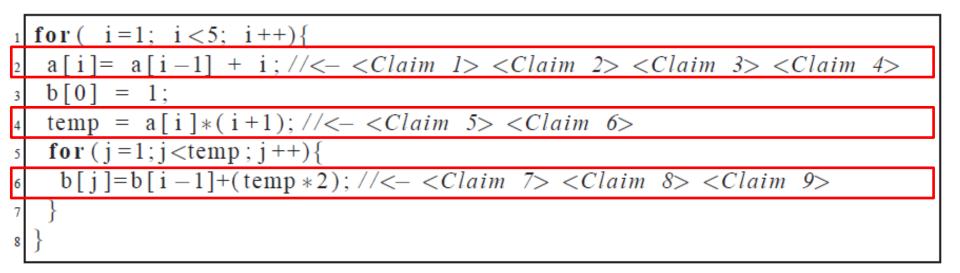
## Second step: Safety Properties Information Collection



result\_claims.txt

## Third step: Asserts Inclusion

# (i) First phase

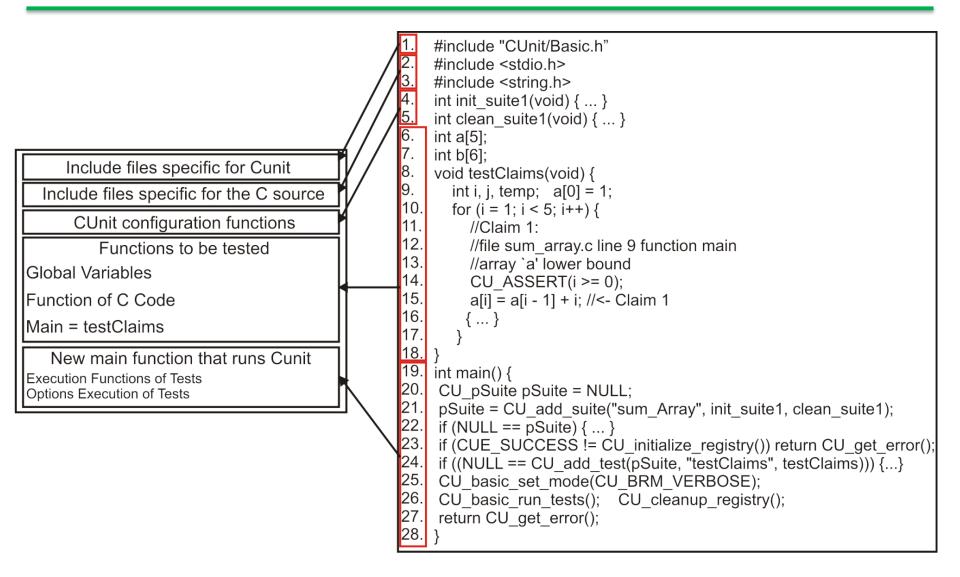


## Third step: Asserts Inclusion

# (ii) Second phase

1	//Claim 1: file sum_array.c line 6 //array 'a' lower bound
2	$CU_ASSERT(i \ge 0);$
3	//Claim 2: file sum_array.c line 6 //array 'a' upper bound
4	$CU_ASSERT(i < 5);$
5	a[i]= a[i-1] + i ; b[0] = 1 ; //<- <claim 1=""> <claim 2=""></claim></claim>

## Fourth step: Implementing Unit Test in CUnit Framework



# Fifth step: Running CUnit Tool

CUnit - A	Unit tes	sting fram	nework fo	or C - V	Version 2	2.1-0		
http://cunit.sourceforge.net/								
Suite: sum_Arr	ay							
Test: testClaims FAILED								
sum_Array.c:86 - j < 6								
Run Summary:	Туре	Total	Ran	Passed	Failed			
	suites	1	1	n/a	0			
	tests	1	1	0	1			
	asserts	404	404	327	77			
Pressione [Ent	er] para	fechar o	terminal	1				

## **Experimental Results**

The steps of the proposed method have been implemented using the ESBMC v1.11, and the framework CUnit v2.1.

Number	Module	LOC	Identified	Violated
1	EUREKA_bf20_det.c <sup>10</sup>	49	33	0
2	EUREKA_Prim_4_det.c <sup>10</sup>	78	30	0
3	SNU_bs_nondet.c <sup>11</sup>	120	7	0
4	SNU_crc_det.c <sup>11</sup>	125	15	0
5	SNU_insertsort_nondet.c <sup>11</sup>	94	14	6
6	SNU_qurt_det.c <sup>11</sup>	164	6	0
7	SNU_qsort-exam_det.c <sup>11</sup>	134	49	-
8	SNU_select_det.c <sup>11</sup>	122	39	6
9	WCET_cnt_nondet.c <sup>12</sup>	139	16	0
10	<b>10</b> Oximeter_log_det.c <sup>13</sup>		4	2

Codes are available at : https://sites.google.com/site/fortesmethod/

- 10- www.ai-lab.it/eureka/bmc.html
- 11- http://archi.snu.ac.kr/realtime/benchmark
- 12- http://www.mrtc.mdh.se/projects/wcet/benchmarks.html
- 13- Semiformal verification of embedded software in medical devices considering stringent hardware constraints . ICESS 2009.

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# Identifying and verifying errors

#### This task is not trivial

```
1. void insertLogElement ( Data8 b ){
2. buffer[next] = b ;
3. next = (next+1)% buffer_size ;
4.}
```

A specific Oximeter\_log\_det.c a code fragment

void initLog(Data8 max)

#### **Properties Identified:**

- 1. next < 6400 -> Line 2
- 2. "(unsigned int)buffer size != 0 -> Line 3

#### **Verification Results**

#### **First situation**

Suite: log Test: testCla	aims	FATI ED			
1. log CUn			< 6400		
Run Summary:	Туре	Total	Ran	Passed	Failed
3	suites	1	1	n/a	Θ
	tests	1	1	Θ	1
	asserts	12801	12801	12800	1

#### **Second situation**

Suite: log Test: testCla	aims	FATLED					
1. log_CUn			ned int	)buffer	size	!=	0
Run Summary:	Туре	Total	Ran	Passed	Fail	ed	
58	suites	1	1	n/a		Θ	
	tests	1	1	Θ		1	
	asserts	2	2	1		1	

#### **Conclusions and Future Work**

#### **Proposed Method**

- The experimental results, although preliminary, have shown to be very effective;
- We identify some improvements;
  - About code structure and block delimiters;
  - About the verifying pointer and dynamic memory allocation.

#### **Future work**

- We intend to investigate the application of verification techniques, such as:
  - Running code on symbolic inputs [Cadar and Engler 2005]<sup>14</sup>;
  - Mutation testing[Jia and Harman 2010]<sup>15</sup>.

14- Execution generated test cases: How to make systems code crash itself. 12th MCS, Stanford Technical Report CSTR 2005-0415- An analysis and survey of the development of mutation testing. IEEE TSE 2010





# Thank you for your attention!

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