Mid-infrared photonics devices in SOI

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ABSTRACT

In this paper we present silicon photonics devices designed for the 3-4 μ m wavelength region including waveguides, MMIs, ring resonators and Mach-Zehnder interferometers. The devices are based on silicon on insulator (SOI) platform. We show that 400-500 nm high silicon waveguides can have propagation losses as low as ~ 4 dB/cm at 3.8 μ m. We also demonstrate MMIs with insertion loss of 0.25 dB, high extinction ratio asymmetric Mach-Zehnder interferometers, and SOI ring resonators. This combined with our previous results reported at 3.4 μ m confirm that SOI is a viable platform for the 3-4 μ m region and that low loss mid-infrared passive devices can be realized on it. **Keywords:** Mid-infrared, silicon, silicon-on-insulator, Mach-Zehnder interferometer, multimode interference, ring resonator.

1. INTRODUCTION

The mid-infrared (mid-IR, 3-20µm) offers applications in diverse areas due to strong absorption bands of many chemical and biological molecules throughout this wavelength range. Applications might be found in medicine, environmental sensing and pollution monitoring, industrial process control, and toxic gas or chemical detection for defence and security. This has recently driven interest in mid-IR group-IV photonics, which offers a potential route to compact, portable sensors in which photonic and electronic functions are integrated on a single chip [1].

Most such sensors would rely on a library of integrated photonic components such as waveguides, splitters, couplers, filters, interferometers, modulators, sources and detectors, among others. For near-infrared (NIR) silicon photonics the design and fabrication of most of these components is now well explored. However, in moving towards longer wavelengths there are new challenges that must be overcome, of which the first is that silicon-on-insulator (SOI), the most commonly used material platform for NIR silicon photonics, has SiO_2 as the cladding material, which begins to absorb heavily for $\lambda > 3.6 \mu m$. Transmission has been reported through a number of alternative waveguide platforms to SOI at different wavelengths through the mid-IR, such as silicon-on-porous silicon [2], silicon-on-sapphire [3, 4], silicon membrane [5, 6], and germanium-on-silicon [7]. However, as SOI is now so well understood for photonics and is readily available, wherever it is possible to use the material it will likely be the platform of choice.

In our work we explore the transmission properties of SOI waveguides throughout the 3-4µm range in order to establish the propagation loss of different SOI waveguide configurations at these wavelengths, and to demonstrate passive components, including multimode interferometers (MMIs), ring resonators and Mach-Zehnder interferometers (MZIs) in SOI material. We also discuss here how the design of some of these devices varies with wavelength.

2. MID-INFRARED CHARACTERISATION SETUP

Our mid-IR characterization setup has been described in detail elsewhere [2]. A HeNe source at 3.39µm with 2mW optical power and a Quantum Cascade Laser that is tunable from 3.72-3.90µm with a maximum power of 150mW have been used. Light is coupled into a fluoride based mid-IR fibre using a ZnSe lens, and these fibers are used for butt coupling to and from the chip. When light from a NIR broadband source is first launched into the fibre, in combination with a NIR camera it is used to confirm that light is being coupled into the waveguide. A liquid nitrogen cooled InSb detector is used for detection of mid-IR light. In each of the measurements described in this paper, only TE light is coupled into the fibres, and we find that polarization change in the fibre is minimal. Both the HeNe and QCL lasers emit

linearly polarized light, and we use a half-wave plate and polarizers that are suitable for this wavelength range for additional polarization control.

3. SOI WAVEGUIDES

For all of the waveguide types presented here, the propagation loss was measured by the cut-back method, where waveguides of different lengths are fabricated on the same chip, and the loss is measured from the relationship between waveguide length and optical transmission. Each chip was prepared for measurement by polishing of the chip facets by abrasive discs, to ensure sufficiently smooth waveguide facets.

1.1. 2µm height rib waveguides

We first reported propagation loss measurements of SOI rib waveguides at $\lambda = 3.39 \mu m$, with dimensions width (W) × height (H) = $2 \mu m \times 2 \mu m$ and etch depth = $1.2 \mu m$, with a $2 \mu m$ BOX thickness [2]. The waveguides were fabricated by UV lithograhy and RIE etching. Their propagation loss was measured to be $0.6 \pm 0.2 dB/cm$. More recently we have published propagation loss results for these same waveguides at the wavelengths $3.73 \mu m$ and $3.80 \mu m$, for which the losses were measured to be $1.5 \pm 0.2 dB/cm$ and $1.8 \pm 0.2 dB/cm$ respectively [8]. The loss values given here are the losses measured after the waveguide roughness was reduced by thermal oxidation, which lowered the propagation loss. We note that the propagation loss increases quite significantly even over this wavelength range. Part of this increase can be accounted for by increased optical mode interaction with sidewall roughness, though the remainder reflects the increased absorption in SiO₂ for $\lambda > 3.6 \mu m$.

1.2. Submicron strip and rib waveguides

Strip waveguides with smaller dimensions are able to have low loss for much smaller bend radii than larger rib waveguides, hence tighter integration of components on chip for a smaller device footprint is possible. However, because of greater interaction of the optical mode with both the sidewalls and the BOX layer, there is larger propagation loss. We have fabricated and characterised SOI strip waveguides with H=500nm, a $3\mu m$ BOX layer thickness and width variations of $1.0\mu m$, $1.2\mu m$ and $1.4\mu m$, for which we have reported the results in [9]. The waveguides were fabricated by e-beam lithography and ICP etching, and were treated by thermal oxidation after lithography, which produced a 20-30nm thick layer of thermal oxide over the waveguides. Simulations suggested that the propagation loss would decrease as the width of the waveguides was increased, and this was confirmed by the experimental measurements. The simulated losses were 6.6dB/cm, 5.1dB/cm and 3.5dB/cm, while measured losses were $5.9\pm0.4dB/cm$, $5.2\pm1.6dB/cm$ and 4.6 ± 1.1 dB/cm for W = $1.0\mu m$, $1.2\mu m$ and $1.4\mu m$ respectively.

We have also fabricated 400nm high SOI waveguides with 350nm etch depth, $1.1\mu m$ width and $2\mu m$ BOX thickness. These waveguides were also fabricated by e-beam lithography and ICP etching, though they have no thermal oxide layer. The measured propagation loss was 9.0dB/cm, which is similar to the losses of the 500nm high strip waveguides before roughness reduction. We expect that thermal oxidation of these waveguides and increasing their width would reduce their losses to similar values to those measured for 500nm high waveguides.

4. PASSIVE DEVICES

The passive devices for which the results are presented in this section make use of the waveguides with submicron Si layer thickness; they are based either on strip waveguides with 500nm Si height or rib waveguides with 400nm Si height. The fabrication methods in each case are the same as those used for waveguides, although thermal oxidation has not been used to reduce the losses of these structures, except where stated.

4.1. 1×2 Multimode interferometers

Multimode interferometers are devices that work on the principal of self-imaging in a wide multimode waveguide, and are used as splitters or couplers for different numbers of input and output waveguides. We have demonstrated MMIs with one input and two outputs, that have tapered input and output ports to reduce the insertion loss into the devices, as was demonstrated by Thomson *et al* [10]. Fig. 1 shows an SEM image of one of our fabricated MMIs, with the design variables marked on the image.

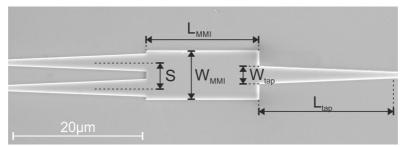


Figure 1. SEM image of fabricated 1×2 MMI, with tapered input and output ports. Design variables are marked on the image.

Soldano [11] gave the locations of the self-imaging points of a 1×2 MMI along its length, z, and the location at which two-fold self-imaging occurs, in a 2D approximation, is:

$$z = \frac{n_{eff}W_{eff}^2}{2\lambda},\tag{1}$$

where n_{eff} is the effective refractive index of the waveguide and W_{eff} is the effective width of the MMI, as defined in [12]. From this equation we see that for a given device width, the length of the device is inversely proprtional to the wavelength, so devices will have a smaller footprint at longer wavelengths. This equation was used for an initial estimate of the device length, followed by detailed 3D simulation of the device using the Photon Design Fimmprop software package to refine the design.

Devices were fabricated that were based on both the 500nm strip waveguide design and the 400nm rib waveguide design. For the device with 500nm Si layer thickness, the designed dimensions were $W_{MMI}=8\mu m$, $L_{MMI}=21.83\mu m$, $W_{tap}=3.0\mu m$, $L_{tap}=20\mu m$ and $S=4.0\mu m$, with simulations predicting an insertion loss of 0.11dB. For the device with 400nm Si layer thickness, the designed dimensions were $W_{MMI}=8\mu m$, $L_{MMI}=21.0\mu m$, $W_{tap}=2.6\mu m$, $L_{tap}=20\mu m$ and $S=4.18\mu m$, with simulations predicting an insertion loss of 0.08dB. The insertion loss of fabricated devices was measured by chaining different numbers of pairs of MMIs together and measuring the transmission through them. Initial fabrication of MMIs with 500nm SOI was reported in [9] as having an insertion loss of $3.6\pm0.2dB/MMI$ at $\lambda=3.74\mu m$. This was much higher than expected and was attributed to non-optimised dimensions and higher sidewall roughness. A second fabrication run, where the fabricated devices corresponded well to design dimensions gave a much smaller loss of $0.29\pm0.17dB$. The devices with 400nm thick Si layer exhibited insertion loss of $0.25\pm0.02dB/MMI$. Fig. 2 shows the chart of transmission plotted against number of MMIs from which the loss figure was calculated [13]. The loss figures for both of these devices rival the best achieved loss figures for NIR silicon photonics [10, 14], and are a significant advance over those achieved so far in the mid-IR [6, 9].

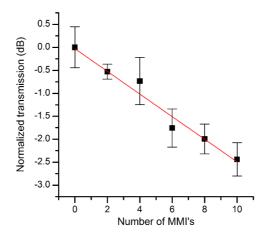


Figure 2. Number of MMIs vs. normalized transmission for MMI's with H = 400nm, D = 350nm, W_{MMI} =8 μ m, L_{MMI} = 21.0 μ m, W_{tap} = 2.6 μ m, L_{tap} = 20 μ m and S = 4.18 μ m. The slope of the graph gives the insertion loss/MMI of 0.25 \pm 0.02dB/MMI [13].

4.2. Racetrack and ring resonators

Ring resonators are used as wavelength filters and for converting a phase change into an amplitude change in either modulators or sensors. We have demonstrated racetrack resonators based on 500nm high strip waveguides [9]. The devices had a bending radius of $100\mu m$, a coupling region length of $50\mu m$, and an edge to edge spacing between the bus waveguide and ring waveguide of $1.2\mu m$. They were treated by thermal oxidation to reduce surface roughness. Fig. 3 shows an SEM image of one of the fabricated resonators.

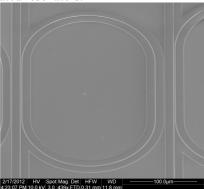


Figure 3. An SEM image of a racetrack ring resonator in SOI material with 500nm Si thickness [9]. The bending radius is 100μm, coupling region length is 50μm and ring-bus waveguide edge to edge separation is 1.2μm.

For filters an important property is their free spectral range (FSR), which for a ring/racetrack resonator is given in [12] as:

$$FSR = \frac{\lambda_r^2}{n_g d},\tag{2}$$

where λ_r is the wavelength of the nearest resonant peak, n_g is the group index of refraction of the waveguide, and d is the length of the waveguide that makes up the ring/racetrack. In moving to longer wavelengths, the length of the ring waveguide will need to increase proportionally to the square of the wavelength to maintain a particular FSR.

The transmission spectrum of the racetrack resonator was measured using the tunable QCL. The FSR of the device was measured to be 4.12nm with an associated group index of 4.64, compared to theoretical values of FSR = 4.24nm, and n_g = 4.53. The experimentally measured resonance peaks exhibited an extinction ratio up to 10dB, and an average Q-factor of 8.2k. From simulations the Q-factor was expected to be in the 30-40k range. This discrepancy might arise due to the QCL having mode-hopping behaviour, which could reduce the effective linewidth of the measurement, and be reflected in a lower Q-factor value.

4.3. Mach-Zehnder interferometers

Mach-Zehnder interferometers for the mid-IR have been reported in the literature in [15] for use in a group index measurement of photonic crystals, and in [16] as part of an optical modulator in silicon. Neither paper investigated the characteristics of the MZI itself, so we report here the extinction ratios and insertion losses of asymmetric MZIs based on both 400nm and 500nm high Si waveguides that have one input and one output, and that use the MMIs reported above as splitters and couplers.

Like ring resonators, Mach-Zehnder interferometers can be used as wavelength filters, or for converting a refractive index change into a change in optical transmission. The advantage of MZIs is that they are far less sensitive to thermal variations than rings, though they also typically have a much larger device footprint. The FSR of an asymmetric 1×1 MZI is given by:

$$FSR = \frac{\lambda^2}{n_e \Delta L - \lambda} \tag{3}$$

where ΔL is the arm length difference [13]. The MZI with 400nm Si height waveguides had $\Delta L = 300 \mu m$, and the MZI with 500nm high waveguides had $\Delta L = 350 \mu m$. The transmission spectrum of the 400nm high MZI is shown in Figure 4, and that of the 500nm high MZI is shown in Figure 5.

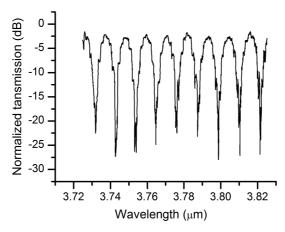


Figure 4. Normalized transmission spectrum of an MZI in SOI based on 400nm high rib waveguides with 350nm etch depth [13].

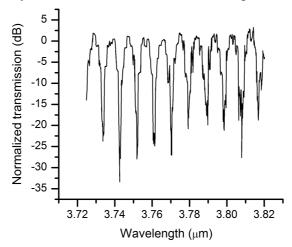


Figure 5. Normalized transmission spectrum of an MZI in SOI based on 500nm high strip waveguides [13].

The MZI based on 400nm high waveguides exhibited an extinction ratio of up to 27dB and insertion loss of 1.6-2.4dB, while for the device based on 500nm high waveguides the extinction ratio of up to 34dB was measured with the insertion loss being too small to measure due to noise in the spectra of the MZI and of normalization waveguides.

5. SUMMARY

We have investigated silicon on insulator as a material platform for integrated photonics in the 3-4 μ m wavelength region by fabricating rib and strip waveguides, and other passive devices, and testing them at wavelengths through this range. Rib waveguides with 2 μ m Si device layer thickness had losses as low as 0.6-0.7dB/cm at 3.39 μ m, and increasing to 1.8dB/cm at 3.80 μ m. Strip waveguides with 500nm height had propagation loss as low as 4.6±1.1 dB/cm at λ = 3.74 μ m. These results show that SOI can be a very useful material platform at wavelengths up to almost 4 μ m.

We have demonstrated multimode interferometers, racetrack and ring resonators and Mach-Zehnder interferometers based on submicron strip and rib waveguides. MMIs exhibited very low insertion losses of only 0.25 ± 0.02 dB/MMI, which approaches the best reported figures in near-infrared silicon photonics. The tested racetrack resonators had extinction ratios of up to 10dB, and a Q-factor of 8.2k. The MZIs had extinction ratios of up to 34dB, and negligible insertion loss.

The fabricated passive devices would likely be very important components of any integrated photonic system, for example for sensing, and confirm that the design methods that are used for NIR silicon photonics are transferable to wavelengths in the mid-infrared.

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