

Low-cost Top-down Zinc Oxide Nanowire Sensors through a Highly Transferable Ion Beam Etching for Healthcare Applications

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Abstract—In this work, we demonstrate a wafer-level zinc oxide (ZnO) nanowire fabrication process using ion beam etching and a spacer etch technique. The proposed process can accurately define nanowires without an advanced photolithography and provide a high yield over a 6-inch wafer. The fabricated nanowires are 36 nm wide and 86 nm thick and present excellent transistor characteristics. The pH sensitivity using a liquid gate was found to be 46.5 mV/pH, while the pH sensitivity using a bottom gate showed a sensitivity of 366 mV/pH, which is attributed to the capacitance coupling between the top- and bottom-gates. The maximum process temperature used in the fabrication of the nanowire sensors is optimized to be 200°C (after wet oxidation) which makes it applicable to low-cost substrates such as glass and plastic. The Ion Beam Etching (IBE) process in this work is shown to be highly transferable and can therefore be directly used to form nanowires of different materials, such as polysilicon and molybdenum disulfide, by only an adjustment of the etch time.

Keywords—*ion beam etch, nanowire, zinc oxide, pH sensing, biosensor.*

I. INTRODUCTION

Over the past decades, Ion Sensitive Field Effect Transistors (ISFETs) have been intensively researched for a wide range of sensing applications, such as ion [1], pH [2], DNA [3] and protein [4] sensing. Silicon-based nanowires have been of particular interest as their large surface-to-volume ratio offers a high sensitivity [4, 5]. Nanowires fabricated in a top-down process are the preferred solution for commercialization of the technology by the semiconductor industry due to precise dimension and position control. However, when compared to conventional ISFETs, top-down silicon nanowires require costly e-beam or deep UV lithography and Silicon-on-Insulator (SOI) substrates [6, 7]. To significantly reduce the fabrication cost, we have proposed a thin-film technology (TFT) alternative using polysilicon nanowires formed by reactive-ion etching (RIE) through a spacer etch technology [8]. However, the formation of polysilicon and gate dielectrics require high temperature processes such as silicon recrystallization and thermal oxidation. The high temperature budget makes this process incompatible with low-cost substrates and increases the overall cost. To overcome the temperature constraints we propose the use of zinc oxide (ZnO) as the semiconducting layer and alumina as the gate dielectric.

ZnO has been a popular metal oxide semiconductor in electronics for its low cost, wide bandgap and self-doping characteristics [9, 10]. Recently, top-down ZnO nanowires were reported to be fabricated using conventional lithography, atomic layer deposition (ALD) and RIE [11]. However, the transistors of [12] were formed at chip level and cannot be directly transferred to wafer level due to issues such as chemical loading effects and plasma condition changes. The plasma-based chemical etch processes normally require complicated optimizations and are not transferable to other materials and wafer-size changes.

In this work, we report a low temperature zinc oxide nanowire sensor process that uses Ion Beam Etching (IBE) and a spacer etch technique. The proposed IBE process is highly transferable to other materials and can form ZnO nanowires over a 6-inch wafer with a high yield. The maximum temperature used is 200°C after underlying layer formation and is therefore fully compatible with low-cost substrates, which is of particular interest for point-of-care (PoC) healthcare applications. Our fabricated ZnO nanowire sensors show excellent transistor characteristics in air and high sensitivity to pH measurements. Top-gate sensing sensitivity was measured to be 46.5 mV/pH whereas bottom-gate sensing provided a sensitivity of 366 mV/pH. The results are comparable with those of reported CMOS nanowire sensors [13].

II. EXPERIMENTAL PROCEDURE

The ZnO nanowire fabrication process is schematically shown in Figs 1a-d. The fabrication process started with a 100 nm silicon dioxide layer grown on a 6-inch n-type Si substrate by a wet oxidation and then a 280 nm silicon nitride (Si_3N_4) layer deposited by low pressure chemical vapor deposition (Fig. 1a). Using conventional photolithography and anisotropic RIE etching 100 nm deep steps were defined on the Si_3N_4 layer (Fig. 1b). A 46 nm ZnO layer was subsequently deposited at 190°C (Fig. 1c) by an Oxford Instrument Plasma Technology (OIPT) FlexAl Atomic Layer Deposition (ALD) system using diethyl zinc (DEZ) precursor (dose time of 50 milliseconds, pressure of 80 mTorr) and oxygen plasma (O_2 flow of 60 sccm, pressure of 15 mTorr and RF power of 100 W) [12]. Subsequently, a spacer etch was done to fully remove the ZnO layer leaving ZnO nanowires along the Si_3N_4 step sidewalls (Fig. 1d). The etching was done in an OIPT Ionfab 300 plus IBE system and the etching conditions were set as argon flows of 5 sccm for the beam and 12 sccm for the neutralizer, beam bias of 500 V and beam current 300 mA, beam acceleration voltage of 400 V, sample tilt angle of 20° and chuck rotation rate of 20 rpm. The IBE was first developed on 20 mm × 20 mm chips where the etch time was varied (1.5, 2 and 2.5 min) and the etching result was characterized by inspections with a Field Emission Scanning Electron Microscope (SEM). Unlike plasma etching processes, the ion beam etching process is a physical process based on ion bombardment and thus is not affected by the size of the area to be etched. This characteristic of IBE makes it possible to directly use a process developed at chip level to etch a full wafer. Therefore, after the optimization of the etching time using the small chips the IBE process was successfully applied to a 6-inch wafer to form the ZnO nanowires.

The fabricated ZnO nanowires can be subsequently used to form transistors/sensors capable of liquid measurements using the process described in Figs. 1e-h. During the IBE, the source and drain areas were protected by photoresist (Fig. 1e). After the etching, a 17.5 nm aluminum oxide (Al_2O_3) layer was deposited on the ZnO as a passivation layer by a thermal ALD process at 200°C (Fig. 1f) and then partially removed by a time controlled IBE process to open metal/ZnO contact windows. To etch the Al_2O_3 the same IBE process as for the ZnO was used by adjusting the etching time. The etch rate for Al_2O_3 was found to be 5.1 nm/min which is expected for a hard material. The accurate and slow etch rate achieved by the IBE process allowed the full removal of Al_2O_3 over the ZnO layer. The ellipsometry measurements showed that the overetching of Al_2O_3 caused a ZnO etch of about 14 nm leaving enough ZnO to form good contacts with the metal layer. Ti/TiN contacts were then formed by sputtering and lift-off (Fig. 1g). Finally, an SU8 layer was formed using photolithography and then fully cross-linked to function as a passivation layer (Fig. 1h).

Fig. 2a shows a fabricated ZnO nanowire sensor including 100 parallel 40 μm long nanowires with 30 μm wide SU8 sensing windows. The fabricated sensors were first characterized as transistors in a dry environment and then their performance in detecting ionic changes was assessed by pH measurements. Fig. 2b shows the configuration for pH sensing. We use the sensors in two different modes of operation; top-gate mode using a liquid gate formed by a Ag/AgCl electrode, and bottom-gate mode using the backside of the substrate. For the top-gate operation, the substrate (V_b) is grounded and the liquid-gate (V_{lg}) is swept between -2 and 1 V with the source/drain bias set at 100 mV. For the bottom-gate operation, the liquid-gate (V_{lg}) is grounded and the substrate (V_b) is swept between -40 and 10 V with the same source/drain bias.

The pH measurements were carried out using universal buffer solutions (0.1 M NaCl, 0.01 M citric acid and 0.02 M boric acid adjusted to pH values ranging from 2 to 9 by titration with a 1 M NaOH solution). The pH buffer solutions were consecutively applied at a decreasing pH order in the sensor's sensing window by pipetting. Between each buffer change, the sensors' surface were washed thoroughly with deionized wafer.

III. RESULTS AND DISCUSSIONS

Fig. 3 shows cross-section SEM images of an IBE nanowire (a) before etching and after (b) 1.5 minutes, (c) 2 minutes, and (d) 2.5 minutes etching. Fig. 3a shows that a 46 nm ZnO layer covers the 100 nm Si_3N_4 step conformally. After a 1.5 minute IBE etch (Fig. 3b), the ZnO on the step was thinned down to 24 nm while the ZnO width near the Si_3N_4 sidewall was barely affected. A 2 minute etching (Fig. 3c) thins down the ZnO

layer such that only a very thin 10 nm ZnO layer is left while the ZnO layer width next to the Si₃N₄ sidewall is also reduced by about 10 nm. After 2.5 minutes of etching (Fig. 3d), the ZnO layer is completely removed from the top and bottom planar surfaces of the step leaving only a trapezoidal shaped ZnO spacer next to the step. The formed spacer is effectively an 86 nm thick and 36 nm wide nanowire.

The cross-section SEM measurements (Fig. 3) were plotted as a function of ZnO etch time as shown in Fig. 4. It can be seen that the ZnO height decreases reasonably linearly with the etch time while the nanowire width remains almost the same for up to 1.5 minutes of etching at which point it starts to reduce and achieves a width of 36 nm in 2.5 minutes. The reduction of the width of the nanowire during etching can be attributed to the non-vertical Si₃N₄ sidewall. The Si₃N₄ sidewall in this work is 75° which allows etching of the ZnO deposited on its surface. The ZnO IBE etch rate was found to be 18 nm/min by fitting the ZnO height change with etching time. This slow etch rate can be very beneficial when etching ultra-thin layers as it can provide a fine dimension control for the formation of thin and narrow structures such as nanowires. When the etching characteristics are considered, it can be concluded that the width of the nanowire can be defined by controlling the semiconducting film thickness and angle of the Si₃N₄ step sidewall whereas the thickness of the nanowire can be defined by controlling the Si₃N₄ step height.

Fig. 5a shows I_d-V_{ds} characteristics of a typical sensor with 100 nanowires in parallel, each of which is 40 μm long, measured in air with the substrate biased in the range of -10 to 10 V. The I_d-V_{ds} characteristics of Fig. 5a are linear, indicating that an ohmic contact is formed between ZnO and Ti. As it can be seen in Fig. 5a, the drain current increases with an increasing substrate bias, which indicates that the substrate can function as a bottom-gate. Fig. 5b shows an I_d-V_b characteristic of the same sensor in air using a bottom-gate sweep. The subthreshold slope (SS) extracted from the characteristic of Fig. 5b is 1.41 V/dec. This is an excellent value for an equivalent bottom-gate oxide thickness of 235 nm. To evaluate the yield of the fabrication process we measured the I_d-V_{ds} characteristics of 54 out of the 336 transistors over the 6-inch wafer. It was found that 51 of the transistors were functional with a conductance in the expected range whereas only 3 were found non-working. Therefore, the IBE process can be used to successfully form nanowire transistors over 6-inch wafers at a very high yield.

The results from the pH measurements using the top-gate are shown in Fig. 6a. I_d-V_{lg} measurements were used to extract the sensitivity to pH changes. When a buffer of different pH is introduced in the sensing window the subthreshold I_d-V_{lg} curve shifts because the ions of the solution change the surface potential of the sensor. As seen in Fig. 6a, the curves obtained from applying different pH buffers are parallel. Therefore, by selecting a reference current (I_{ref}), the effect of pH change can be measured by evaluating the liquid gate voltage (V_{lg}) shift at this reference current as shown in Fig. 6b. It can be seen that the voltage shift has a linear relation with pH values between 3 and 9, and the per pH voltage shift can be extracted from the plot to be 46.5 mV/pH. This voltage shift closely follows the literature values for an Al₂O₃ surface on nanowire sensors [13, 14]. Fig. 6c shows bottom-gate subthreshold (I_d-V_b) characteristics of a typical sensor when different pH buffers were applied. The curves for different pH buffers again are approximately parallel. By defining a reference current, the voltage shift was extracted and plotted as a function of pH value in Fig. 6d. Similar to the top-gate sensing, the bottom gate shift is linear with pH values, with a value of 366 mV/pH. Compared to the top-gate voltage shift of 46.5 mV/pH, the bottom gate thus results in a significantly higher voltage shift, which is far beyond the Nernstian limit of 59 mV/pH [1]. This higher bottom-gate voltage shift has been reported previously for silicon nanowire transistors [15-17] and is explained by the effect of the capacitance coupling between the top- and bottom-gates [18]. To take advantage of the bottom-gate sensing, the proposed thin-film technology compatible fabrication process can be easily modified to integrate individual bottom-gates to sensors instead of using the substrate. Therefore, the fabricated ZnO nanowire sensors' pH performance is comparable to that from other silicon based technologies but it is delivered at the temperature of 200°C, which makes it compatible with low cost substrates such as glass and plastics.

The physical etching mechanism of IBE is known for its low selectivity over different materials, however, in a spacer etch process, it offers an excellent control over the etching and its high transferability enables the

formation of top-down nanowires of different materials. The same IBE process, with only an etch time adjustment, can be used to form nanowires of various materials. Fig. 7 shows nanowires made from two different materials. The polysilicon nanowire in Fig. 7a was formed by a 100 nm Si deposition by PECVD and an IBE etch with a dimension of 85×64 nm. The spacer etch can be finely controlled as the poly-Si etch rate is only 17 nm/min. The molybdenum disulfide (MoS_2) nanowire (Fig. 7b) was also formed by a MoS_2 deposition by CVD and an IBE with a dimension of 73×40 nm. The etch rate of MoS_2 was found to be 14 nm/min which is slow enough to accurately control the resulting dimensions.

IV. CONCLUSION

In this work, we have proposed a fabrication process for zinc oxide nanowire sensors using an ion beam etching and a spacer technique to form nanowires over a 6-inch wafer with standard 2 μm minimum feature size photolithography. The results from pH measurements with the fabricated ZnO nanowire sensors showed a top-gate (liquid gate) pH sensitivity of 46.5 mV/pH and a bottom-gate sensitivity of 366 mV/pH, which follow with those of Si nanowire sensors in the literature. The ZnO nanowire process is a low temperature process with a maximum temperature of 200°C and can be applied to low cost and disposable substrates for health care applications; this is not achievable with silicon processes because of their high temperature budgets. The ZnO fabrication process is compatible with thin film technologies and is therefore able to deliver sensors with individual bottom-gates, which can provide a very high voltage sensitivity. Finally, we have demonstrated that the IBE process together with the spacer etch technique is highly transferable and can be used to form top-down nanowires of other materials, including polysilicon and MoS_2 .

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Figures

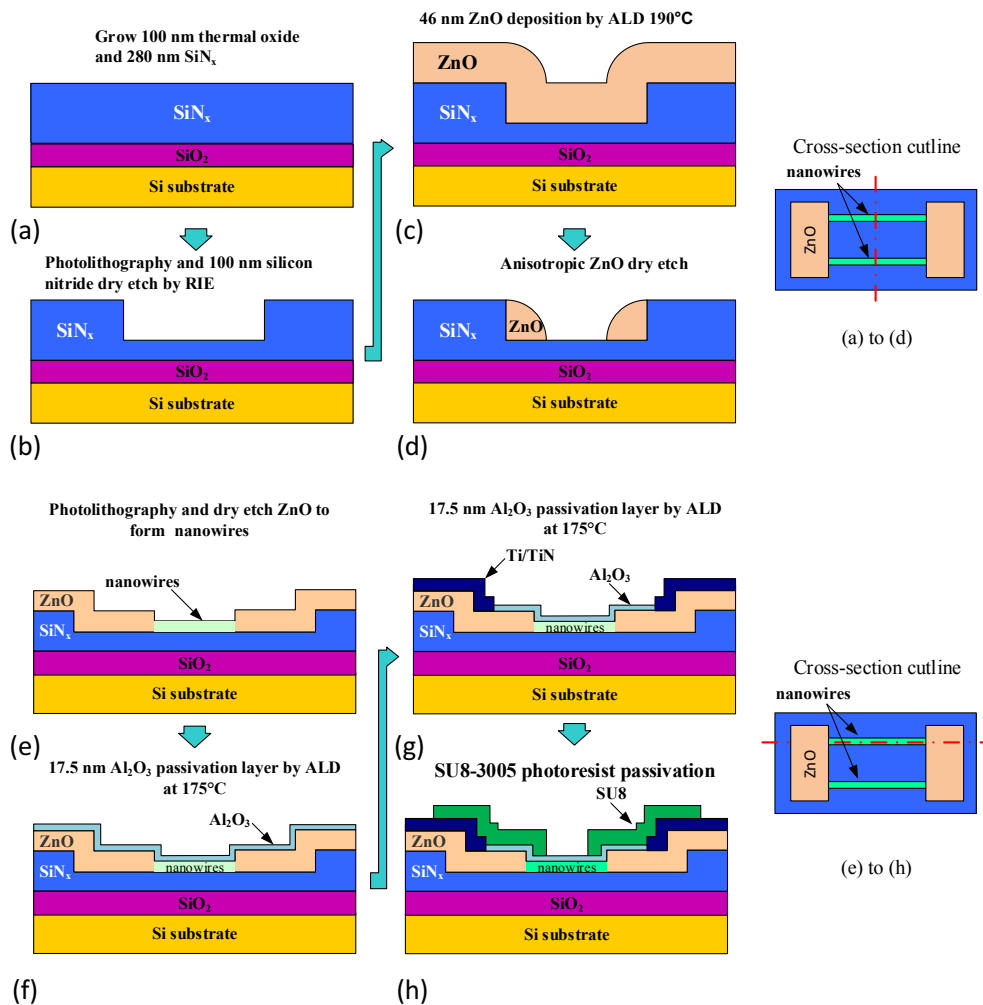


Figure 1 Schematic of the nanowire sensor fabrication process. Spacer nanowire formation (cross-section in direction perpendicular to nanowires) through dielectric formation (a), step definition by RIE silicon nitride etch (b), ZnO deposition by ALD (c) and ZnO etch by IBE (d). Sensor fabrication (cross-section in direction parallel to nanowires) through ZnO formation (e), Al_2O_3 passivation formation by ALD (f), Al_2O_3 IBE patterning and Ti/TiN electrodes by sputtering and lift-off (g), and SU8 photoresist passivation (h).

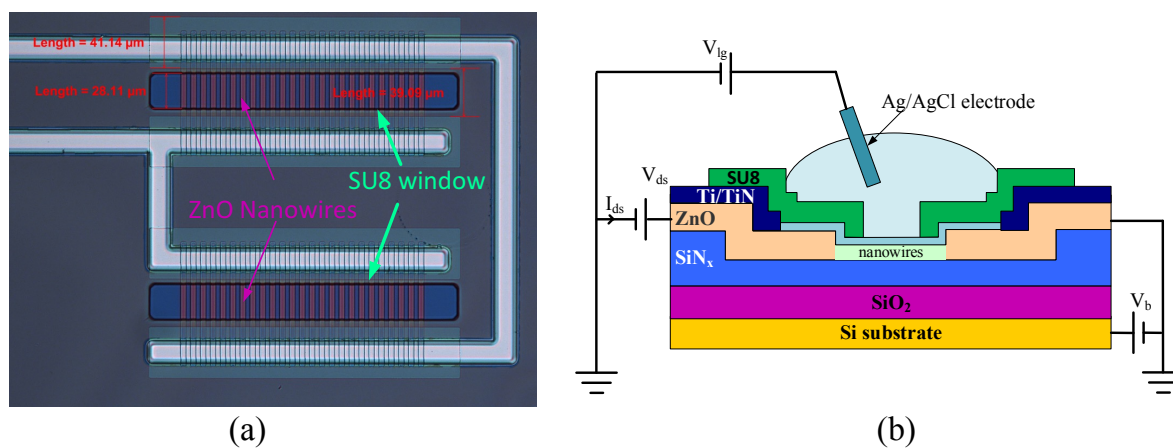


Figure 2 (a) optical micrograph of formed ZnO nanowire sensor and (b) pH measurement configuration.

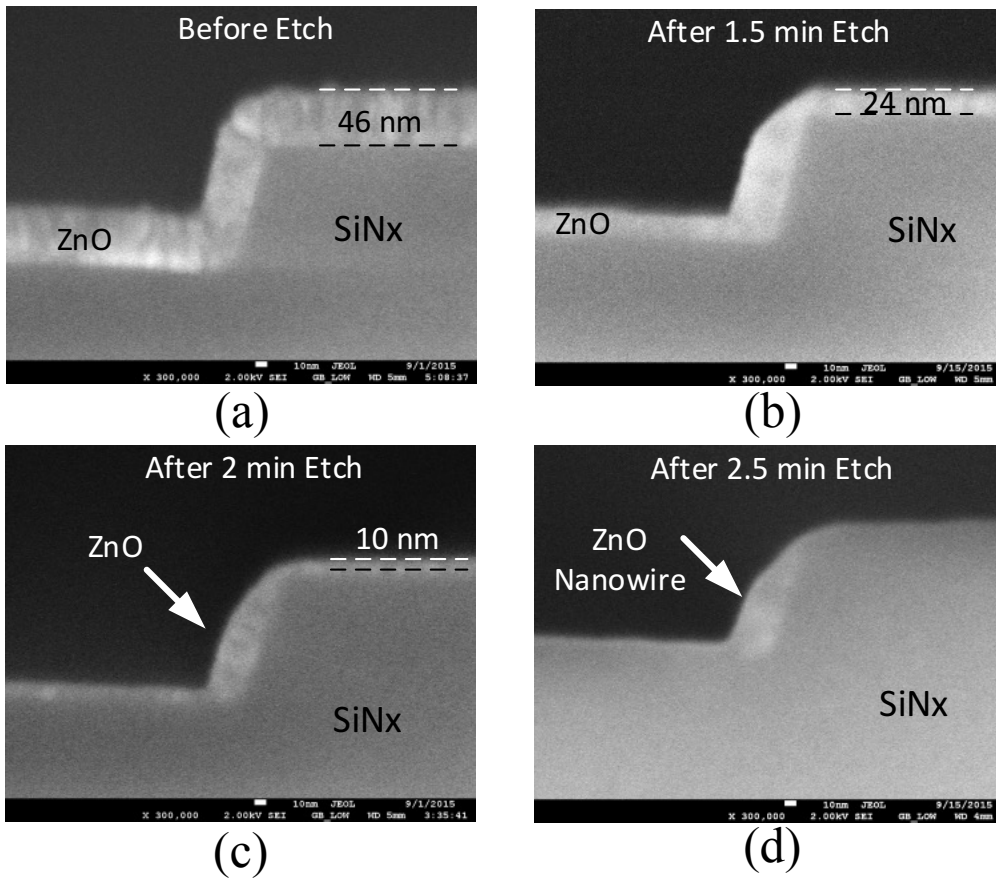


Figure 3 Cross-section SEM micrographs of ZnO with (a) no etch, (b) 1.5 min etch, (c) 2 min etch and (d) 2.5 min etch.

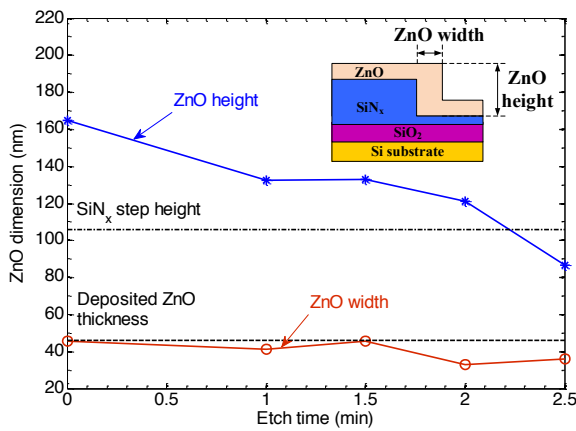


Figure 4 Nanowire width and height dimensions, measured by cross-section SEM, as a function of etch time.

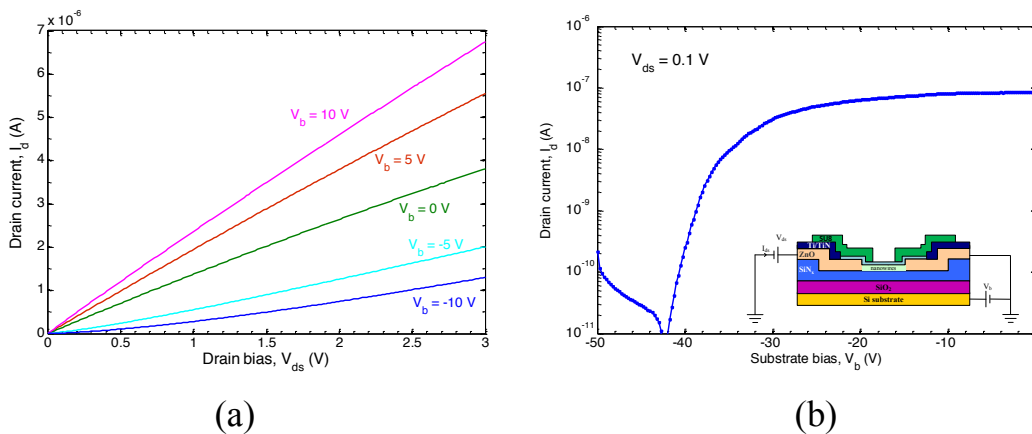


Figure 5 Nanowire sensor characterizations of (a) I_d - V_{ds} measurements at different substrate biases, V_b , in range of -10 to 10 V and (b) I_d - V_b subthreshold measurement at a drain bias of 0.1 V.

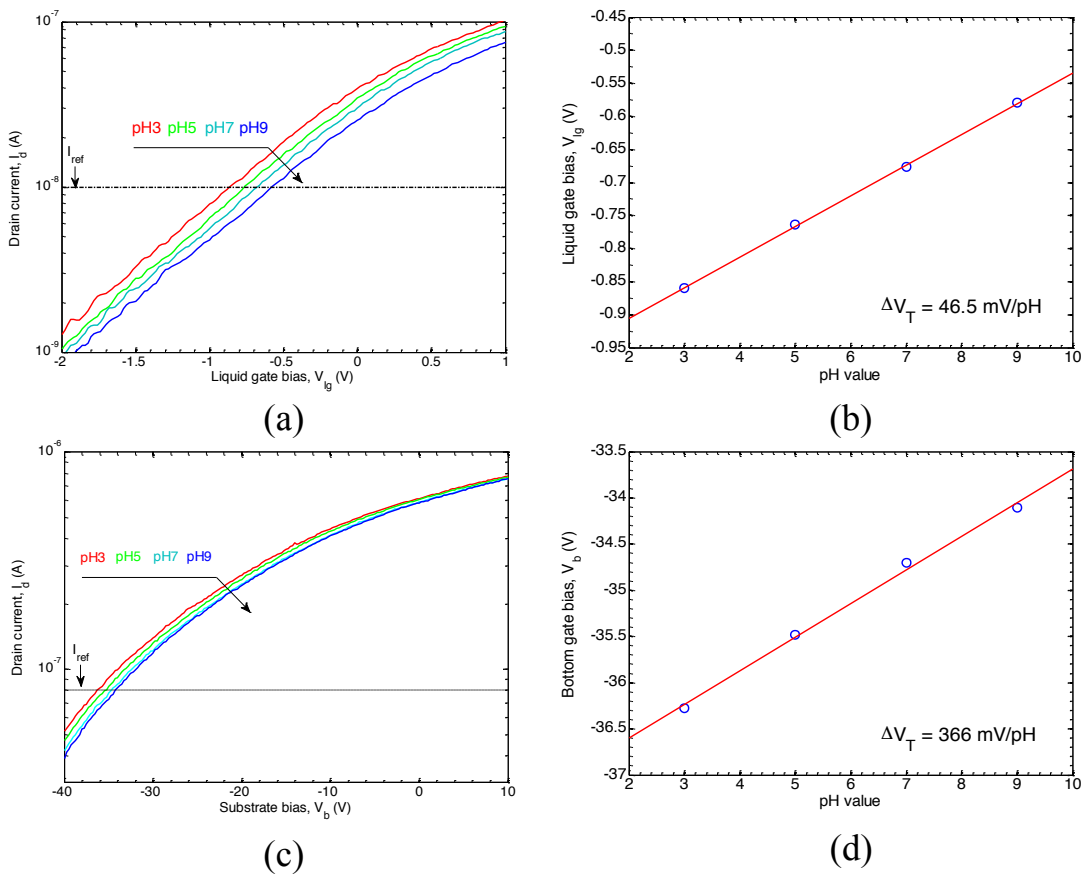


Figure 6 pH sensing measurements using a typical ZnO nanowire sensor of (a) top-gate subthreshold (I_d - V_{lg}) plots at different pH values, (b) liquid gate bias as a function of pH, measured at the reference current in (a), (c) bottom-gate subthreshold (I_d - V_b) plots at different pH values, (d) bottom-gate bias as a function of pH, measured at the reference current in (c).

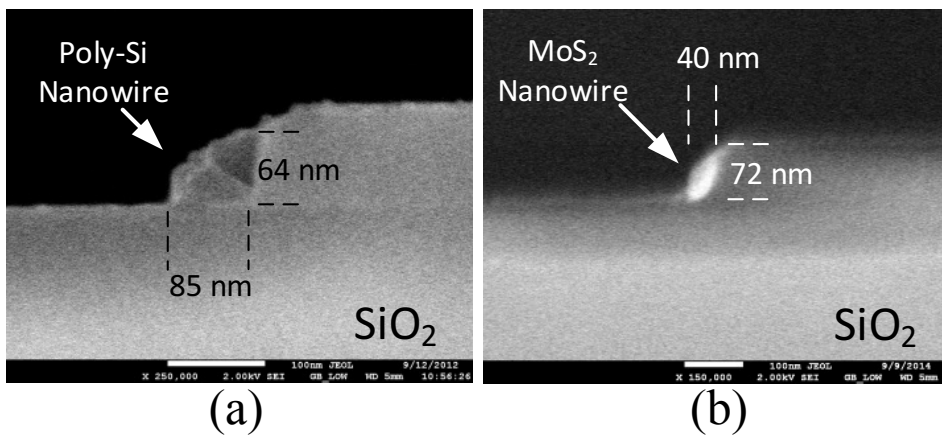


Figure 7 Cross-section SEM micrographs of nanowires formed by the same IBE process; (a) polysilicon nanowire and (b) MoS_2 nanowire.