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ARRANGEMENT OF THE MAIN COMPUTATION NETWORK
FOR THE SEA AROUND THE BRITISH ISLES

A part of the electronic model
for tides and storm surges

S. Ishiguro
1980

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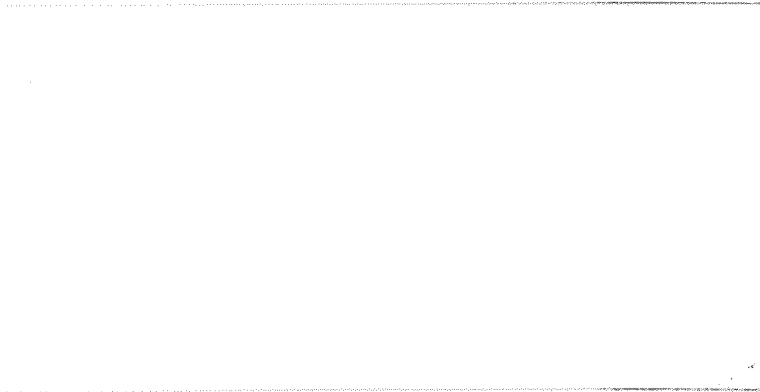
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ARRANGEMENT OF THE MAIN COMPUTATION NETWORK

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53 pages, 3 tables, 47 diagrams.

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ABSTRACT

The construction of the 'Main Computation Cabinet' of the electronic model for tides and storm surges has been explained, which includes the grid-socket matrix boards, input-data patch boards, output-data multiplexers, and floating DC-power supply. Then, a model scheme for the sea around the British Isles has been described with the full data for its time-independent model parameters. The model has actually been assembled to this scheme.

1. INTRODUCTION

The outline of the electronic model for tides and storm surges has been described in a separate paper (Ref. 1). 'Grid cards' which are building blocks of the main computation network of the model have also been described in detail in the same paper. Peripheral instruments including the control cabinet of the model have been described in another paper (Ref. 5).

In the first half of this paper, the construction of the 'main computation cabinet', which contains the computation network and its directly related components, has been explained mainly for its user's aid. In the last half, an example of setting the network to the sea around the British Isles has been described with the full data, while the model can be set to any continental-shelf sea.

2. CONSTRUCTION OF THE MAIN COMPUTATION CABINET

Fig. 1 shows a general view of the computation cabinet. Fig. 2 shows the schematic diagram of the circuits contained in the cabinet, and Fig. 3 shows the locations of components in the cabinet.

The main computation cabinet ($169 \times 67 \times 64 \text{ cm}^3$) consists of four identical vertical drawers, a rear panel with a cover and the base of the cabinet ($67 \times 105 \text{ cm}^2$) with four rollers. The mechanical details of the cabinet have been described in a separate paper (Ref. 6).

Other than the grid cards, the cabinet contains:-

Grid-card matrix boards	One board per drawer, 240 cards per board
Floating DC-power supply units	240 units per drawer
Patch board <u>A</u> for time-dependent input	60 sections per drawer, 4 terminals per section
Output multiplexer with impedance converters	240 sets per drawer, 3 channels per set
Inter-drawer connectors with patch board <u>B</u>	150 channels per drawer
Common power transformer	One

The above-mentioned components are electrically linked with external circuits or instruments by:-

Connector CN P	240V AC power line (3 lines)
Connectors CN21 to CN26	Time-dependent input for normal grids, from the input memory (300 lines)
Connector CN100	Time-dependent input for non-coastal boundary, from the input memory (50 lines)
Connector CN15	Output data to the output memory (6 lines); and the multiplexer control signals (4 lines), from the control cabinet

Although the cabinet can contain up to 990 grid cards, only 330 cards are connected at the moment. See the footnote on page 18 for the reason.

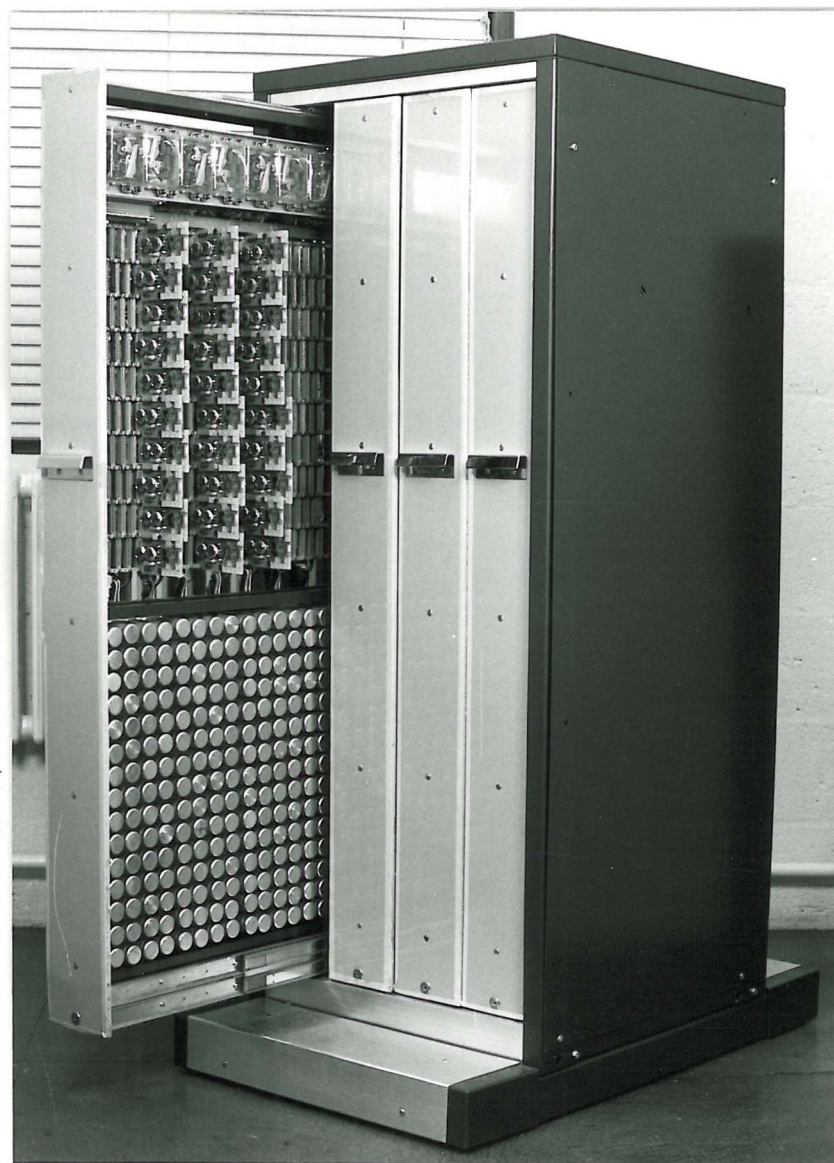
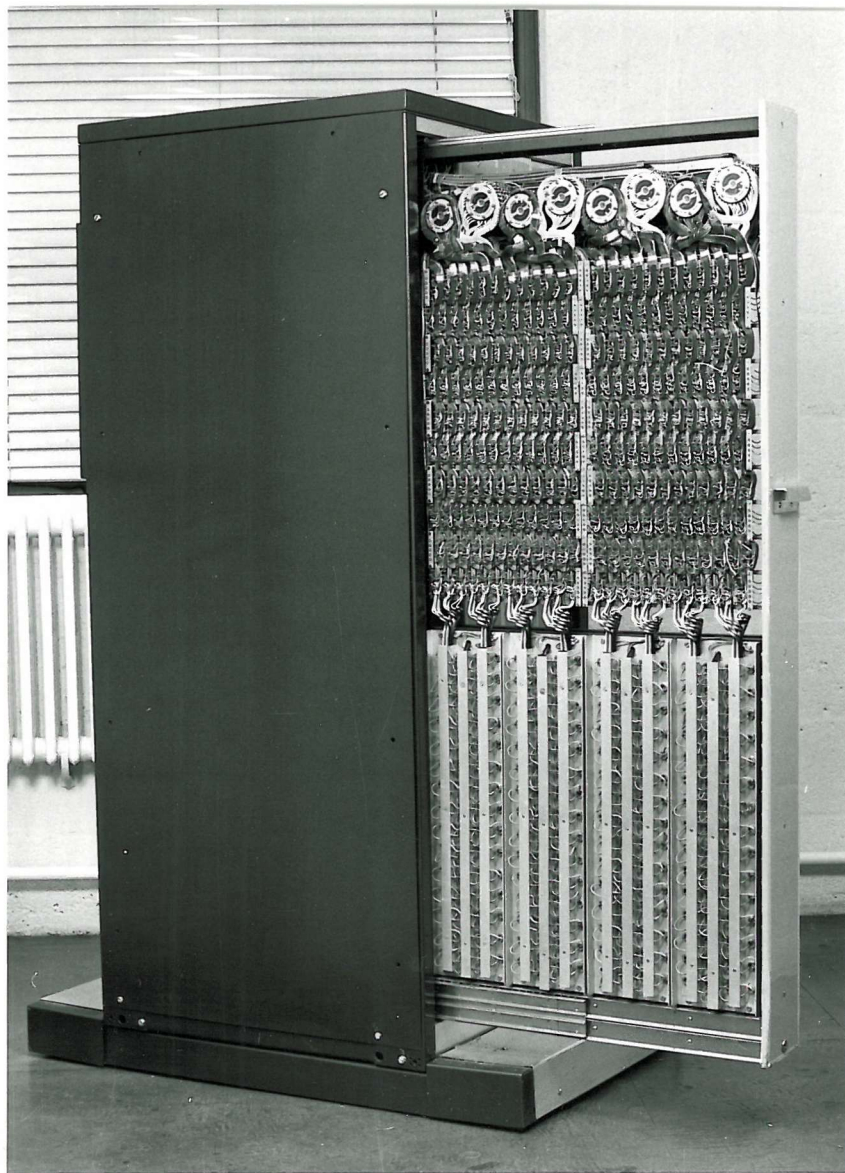


Fig. 1 Main computation cabinet.

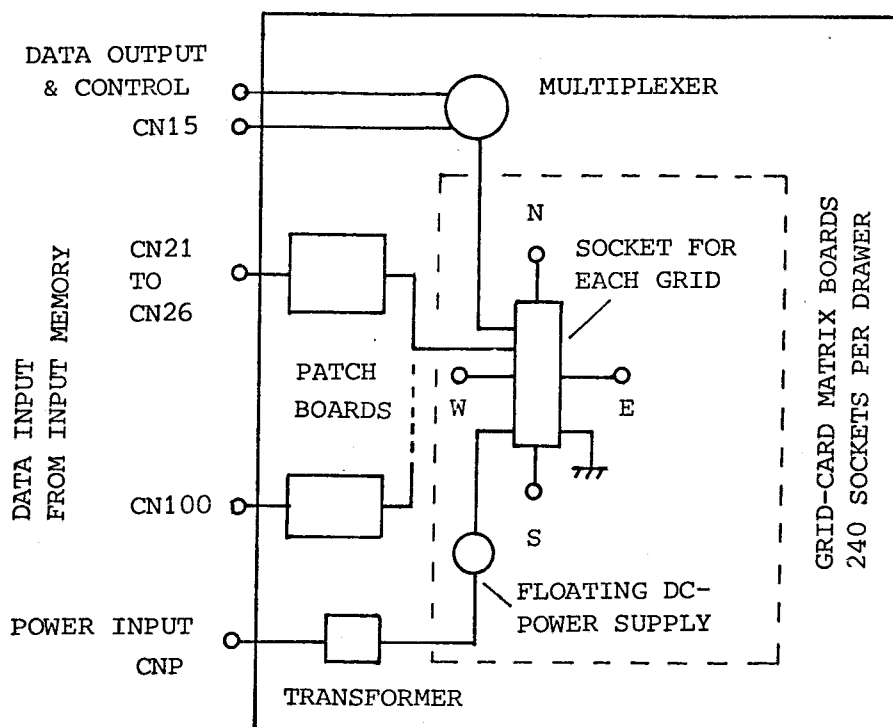


Fig. 2 Schematic diagram of the circuit in the main computation cabinet.

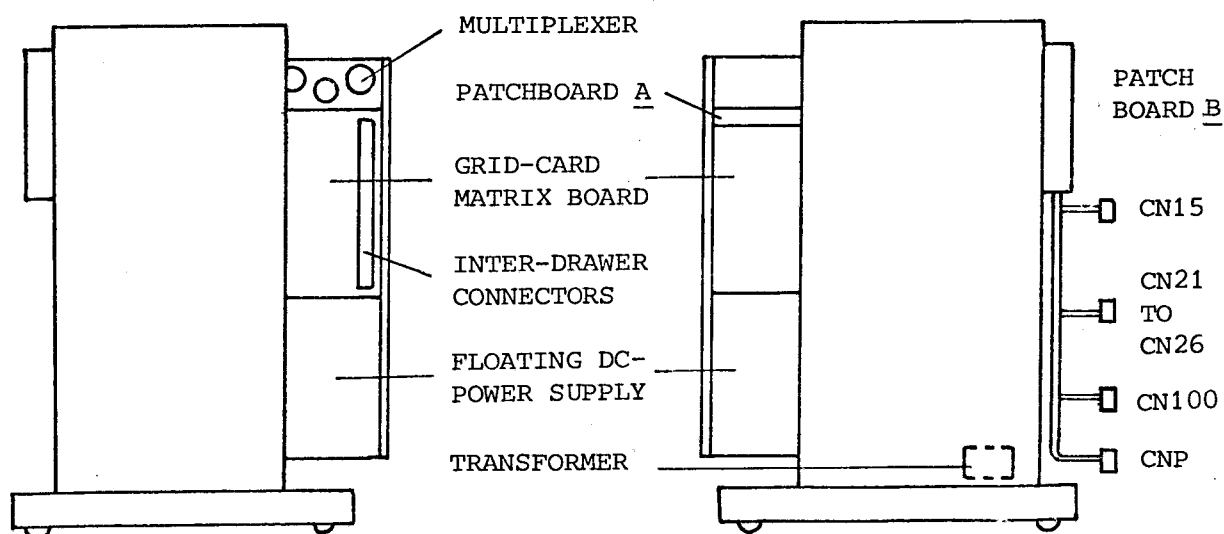


Fig. 3 Physical arrangement of components in the main computation cabinet.

2.1 Power supply

Fig. 4 shows the schematic diagram of the power supply system in the cabinet. The power supply unit for each grid card is operated from a 240V AC power line, but its DC output (10V) terminals are 'floating' from the common conductor (ground line) with a high degree of AC/DC isolation. The isolation is achieved by an isolation transformer specially designed for this purpose (Ref. 1). It is important that all the AC power-line conductors in the cabinet are electro-statically screened. For this reason, the power-supply unit rack is separated from the grid-card rack, as shown in Fig. 5. Fig. 6 shows the details of the screened wiring rack for the power supply units.

All the power supply units are electrically and physically identical, except for minor differences in their stray capacitances (see Appendix 2). Each power unit is physically fixed to the rack with a single screw, and electrically connected with 4 'micro' plugs: 2 to the AC power line sockets, and 2 others to the DC output line sockets. Each set of DC power-line sockets is permanently connected to a particular grid socket. They can be identified by a common number for a power supply unit and its grid socket (Fig. 5).

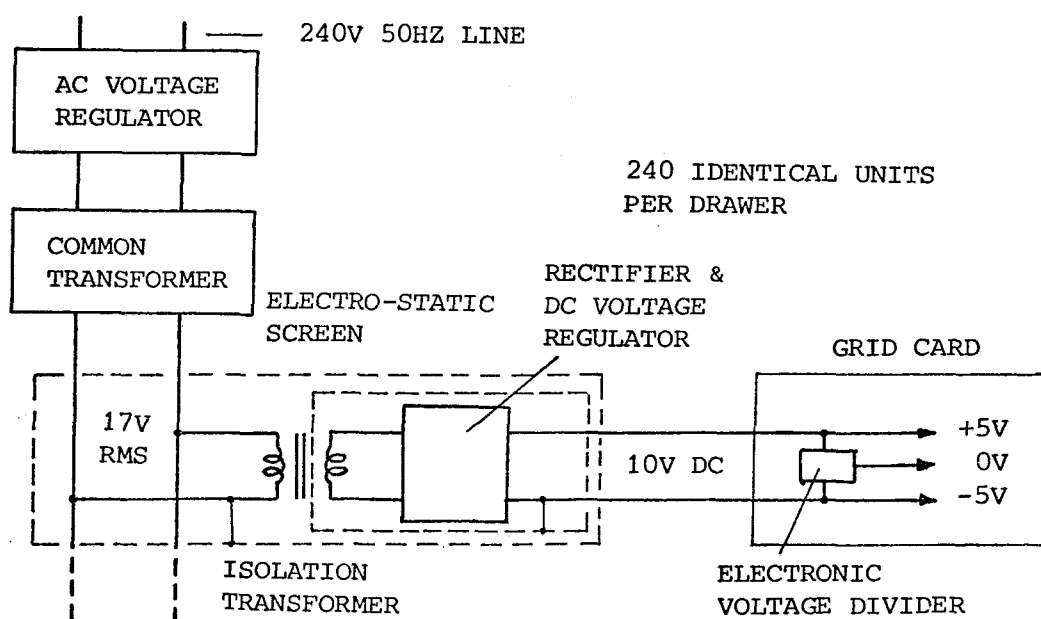


Fig. 4 Schematic diagram of the power supply system in the main computation cabinet.

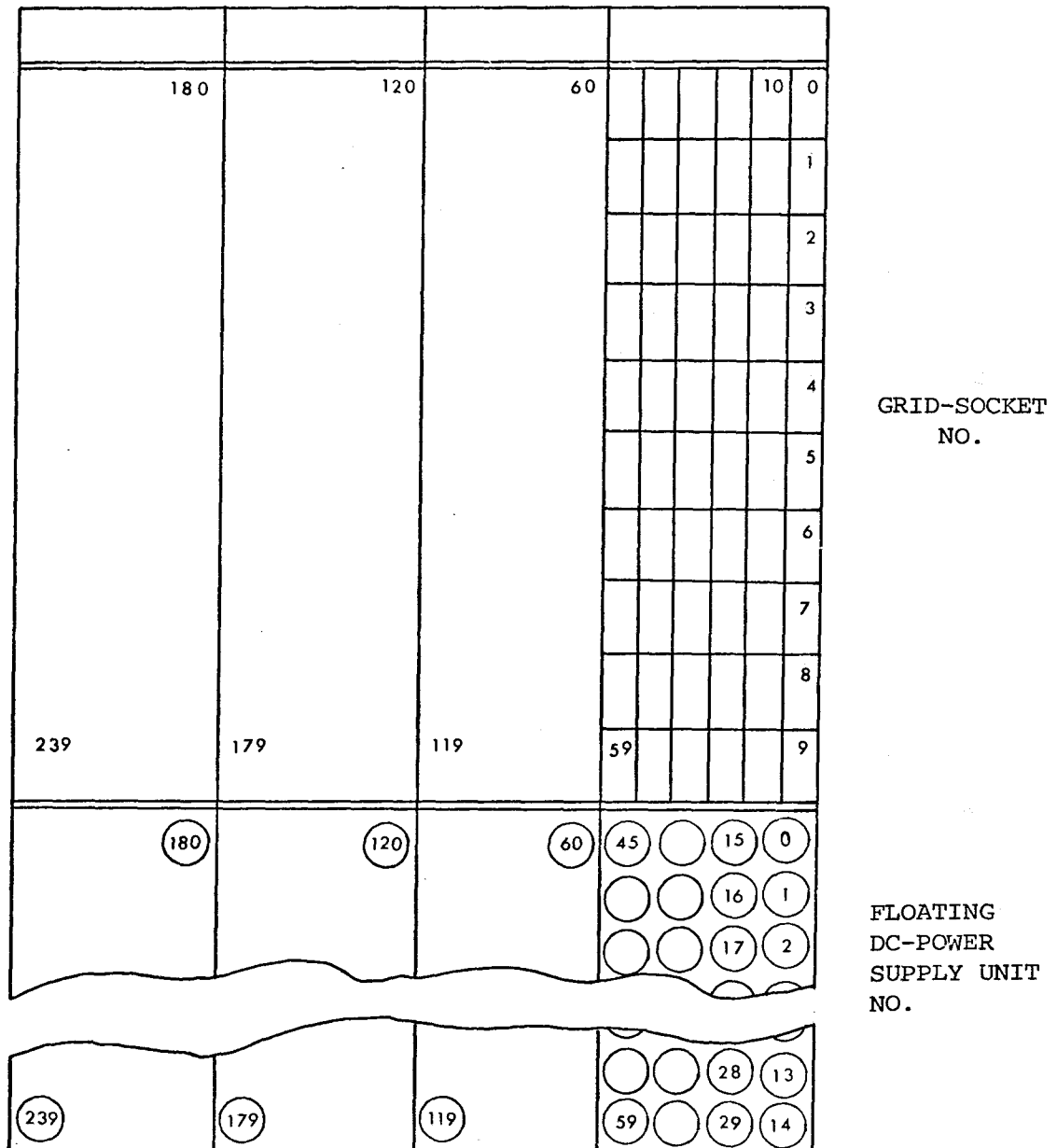
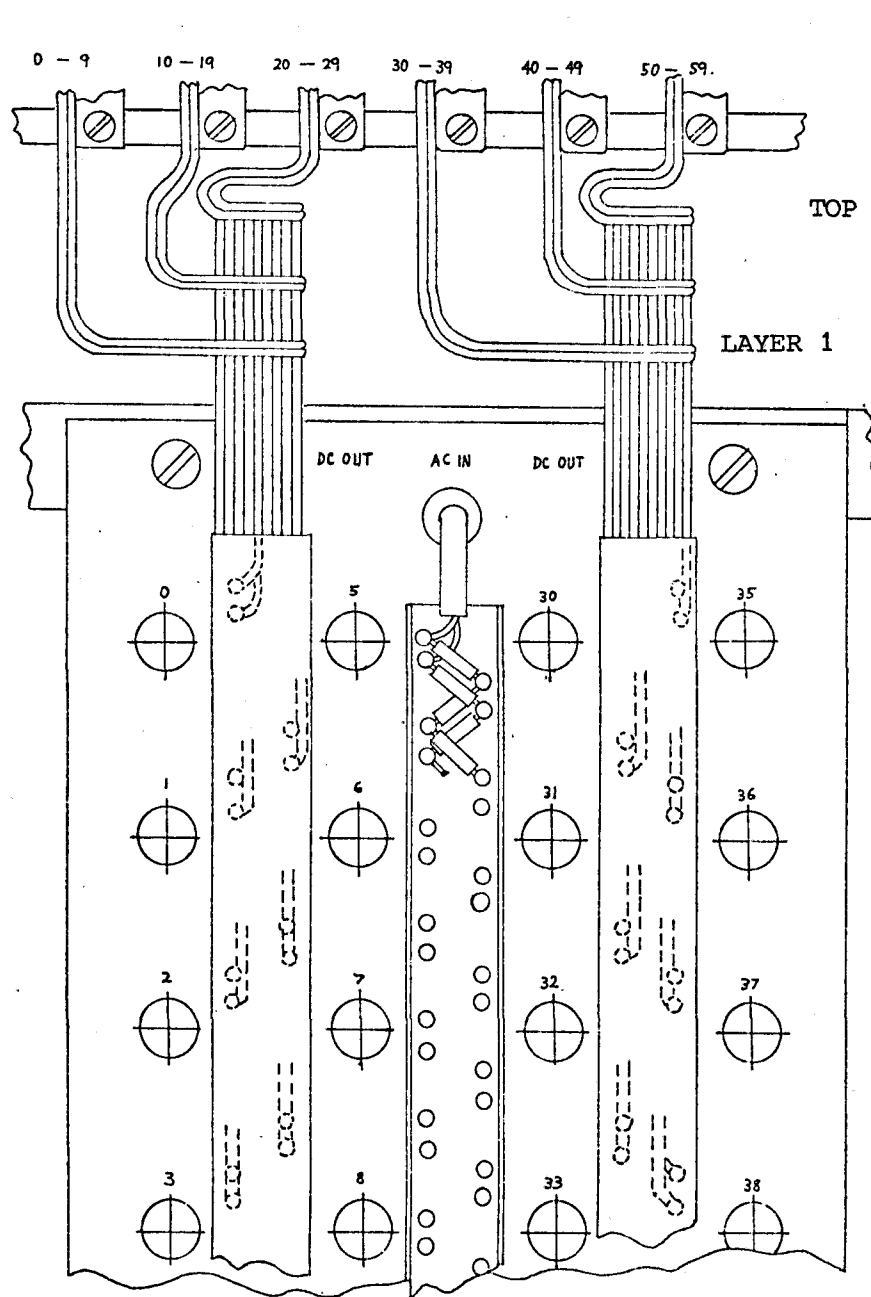
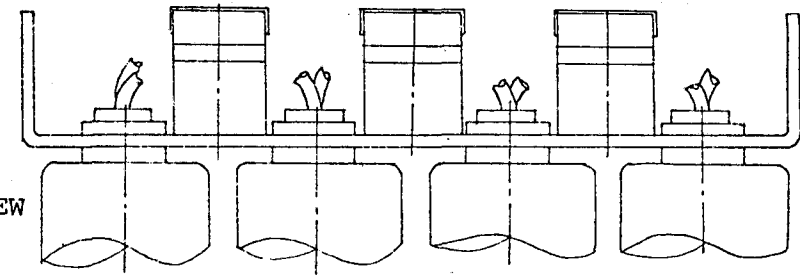


Fig. 5 Grid sockets and floating DC-power supply units in a drawer. A grid-socket is internally connected to the power supply unit which has the same number.



TOP VIEW

LAYER 1



LAYER 2

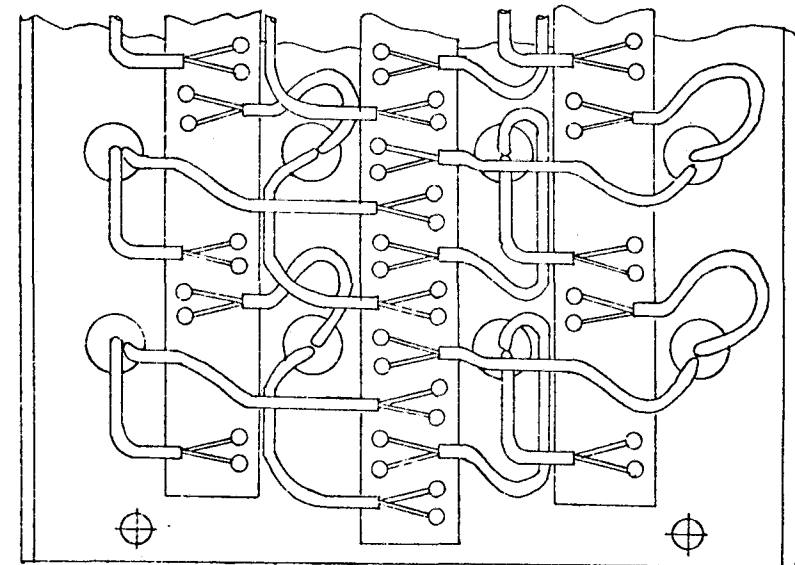


Fig. 6 Physical arrangement of the screened wiring-rack of the power supply within a drawer. 1/4 of the drawer is shown.

2.2 Inter-grid connections

'Grid sockets' are physically fixed in the top half of the rack in each drawer of the cabinet, forming a 10×24 matrix. Each grid socket is used for holding a grid card physically, and for connecting electric circuits with 16 terminals (including 2 unused terminals). 10 of these terminals used are permanently connected within the rack: 2 for the DC power supply lines; one for the ground line; 4 for the input-data line; and 3 for the output-data lines. The remaining 4 terminals, N, E, W, S, are left for programming the network.

The 4 terminals of each grid socket should be connected according to a model scheme, so that the whole grid forms a two-dimensional network, e.g. see Fig. 17. Fig. 7 shows the 4 terminals of a grid socket, and its typical connections to the adjacent sockets. This example shows a case where the 4 adjacent grid sockets are physically adjacent to the socket in question. Although such a case is easy to imagine, it is not always possible to actually arrange. A socket in a physically remote position in the drawer (or another drawer) can be used as an adjacent grid socket by using a long connection wire.

Standard wires with micro plugs for connecting the grid sockets vertically (N-S direction) or horizontally (E-W direction) are colour coded as shown in Fig. 7.

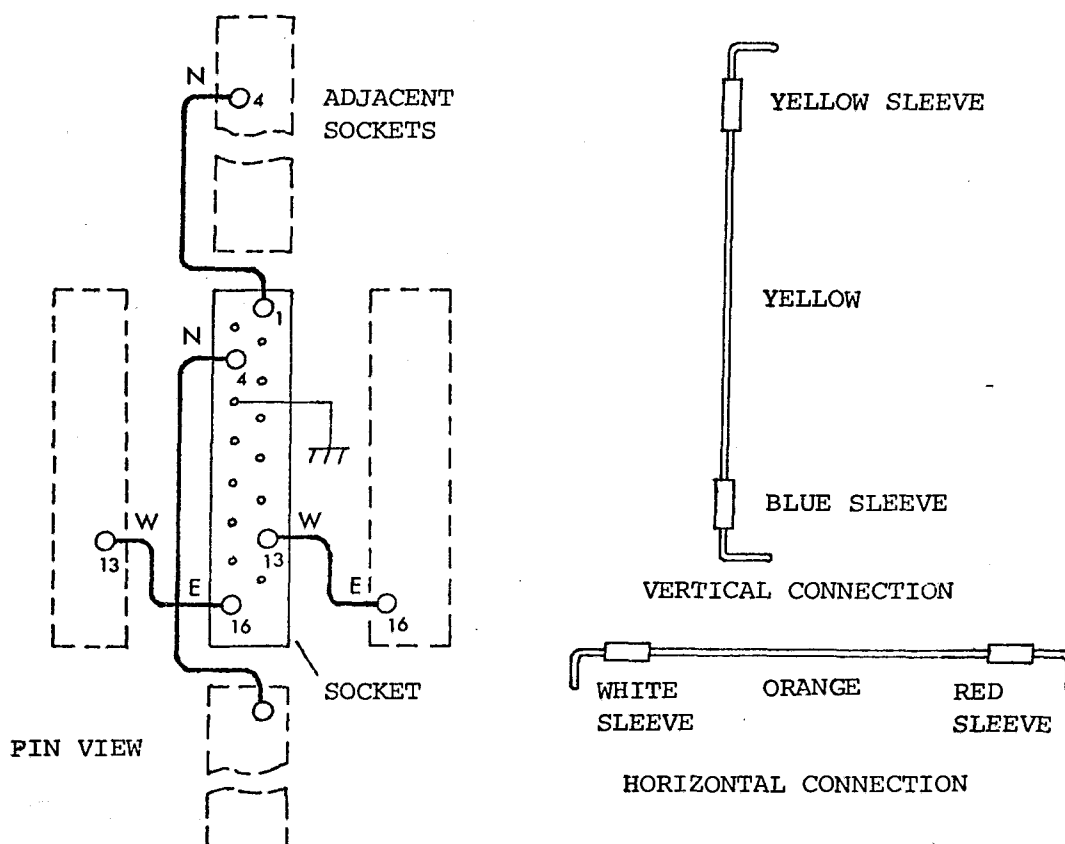


Fig. 7 Inter-grid connections and colour-coded wires.

2.3 Time-dependent variable input

Time-dependent variable input represents, for example, the atmospheric pressure or wind on the sea surface; while the time-independent variable input represents, for example, the water depth or latitude of a certain part of the sea. The former is fed into each grid card from the Input Memory (Ref. 2) through a pair of opto-electronic couplers (N and E components), while the latter is stored in the grid.

Generally the size of a 'sea grid' (a grid representing a quantized section of the hydrodynamic system of the sea) is smaller than that of a 'meteorological grid'. This is mainly due to the scales of the two systems limited by their boundaries, and the numbers of grids available for the systems. An example, described in chapter 3, employs 50 km square grids for the sea, and 100 km square grids for the meteorological system, i.e. 1-to-4 ratio in their area.

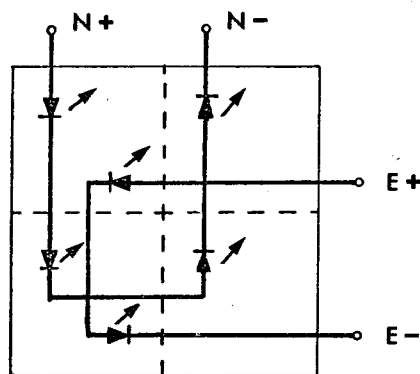


Fig. 8 Typical connections for driving LEDs in opto-electronic couplers. This example shows a group of four grids.

Fig. 8 shows the typical connections of LEDs in the opto-electronic circuits of 4 adjacent sea grids (or one meteorological grid). Note, the opto-electronic output from the LEDs is proportional to their currents (not voltages), and each set of LEDs should be connected in series.

The currents for each set of grid cards (typically 4 cards) are generated by the 2 channels of the input memory which has many independent channels. In order to obtain versatile combinations in the input memory channels and grid sockets, 'patch board A' has been prepared between the memory and sockets (the top part of each drawer; see Fig. 2).

Fig. 9 shows the details of patch board A and the grid socket matrix board in the same drawer. Each section of the patch board consists of 4 sockets, N-, N+, E-, E+, and each section has a serial number. These 4 sockets are permanently connected to the grid card which has the same number as the patch board section. When a grid card is connected to a grid card socket, the opto-electronic input terminals of the grid card are automatically connected to the 4 sockets of the patch board section, as shown in Fig. 10 (the left side diagram).

Each output channel of the input memory has a set of two plugs which ends near patch board A, as shown in Fig. 10 (top-left diagram), so that this is directly connected to appropriate terminals of the patch board, according to the programme of the model. Jump wires which have the same type of plugs as the former have also been prepared (bottom-left diagram in Fig. 10) for necessary connections as shown in Fig. 8.

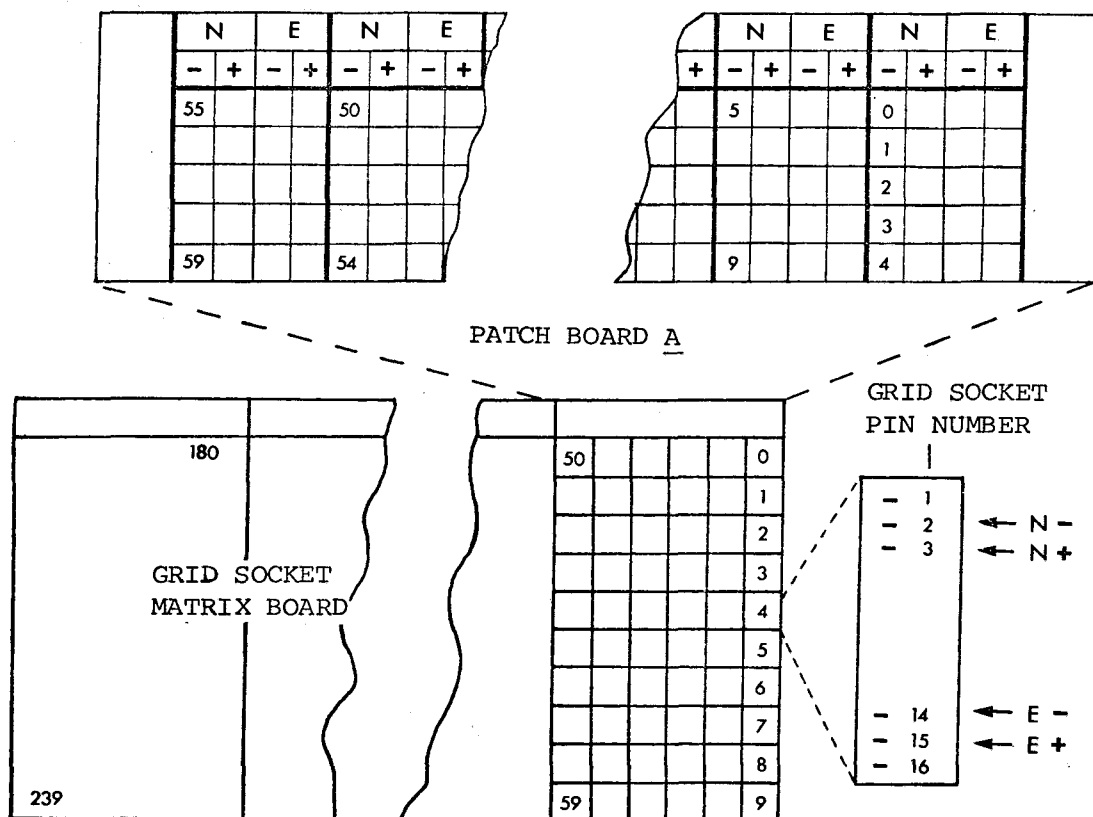


Fig. 9 Identifications of channel no. and polarities of a grid-card terminal, patch board sockets and wires.

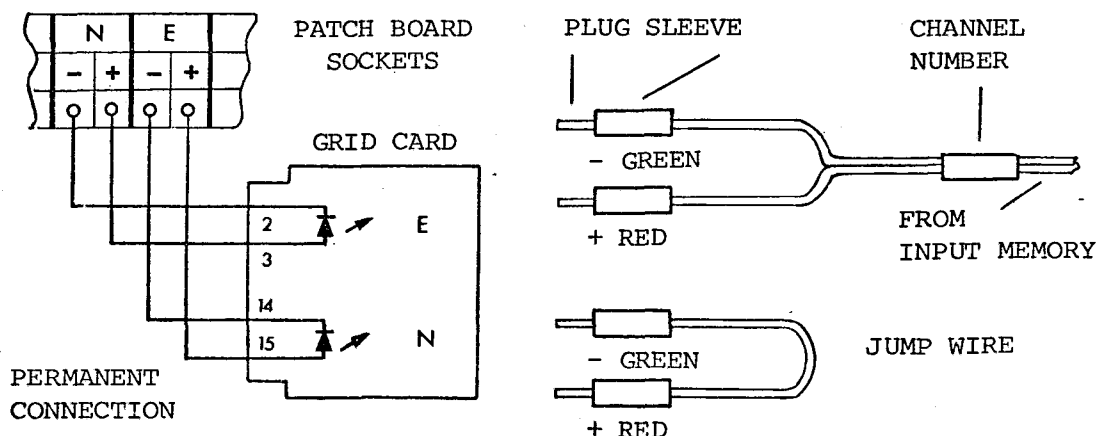


Fig. 10 Relationship between grid socket no. and terminal no. on patch board A.

2.4 Non-coastal boundary

A boundary of a model representing other than a coastal line of the sea should have the following characteristics:-

- a Waves which are going through the boundary are not reflected by it, and
- b waves which are coming through the boundary are artificially generated at the boundary. Ideally, waves reflected beyond the boundary should also be generated in the same way.

The model grids for the boundary have been designed to simulate these conditions, i.e.

- a' The model boundary is terminated with a grid whose dynamic impedance is equal to the dynamic impedance of the modelled area seen through the boundary, and
- b' simulated external waves are fed from the input memory through the model boundary.

The waves in b' are again simulated by the input memory. However, an interface circuit (a different type from the opto-electronic coupling used for general grids) is used between each channel of the input memory and a boundary grid. Fig. 11 shows a circuit, using such an interface, which satisfies the requirements in a' and b'. Fig. 12 shows the actual circuit board on which 24 channels of interface circuit are assembled.

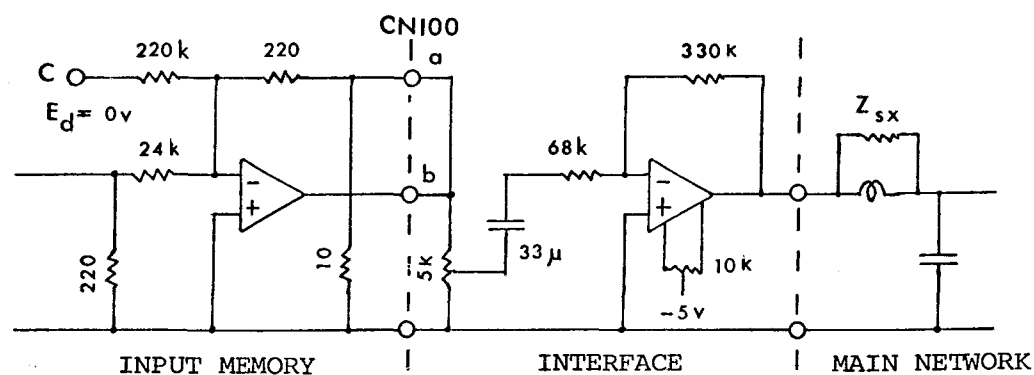


Fig. 11 Circuit diagram of a non-coastal boundary grid.

In order to adjust a terminating impedance (Z_{sc} in Fig. 11), 40 independent potentiometers ($10 \times 10 \times 5 \text{ mm}^3$ each) have been assembled on a single circuit board, 'board Z'. This can be used for a boundary with external waves or without external waves (the interface circuit is replaced simply by a short circuit).

Fig. 13 shows a typical arrangement of the circuits from the input memory to the 'inter-drawer connector' (see Section 2.5) in each drawer, for a non-coastal boundary.

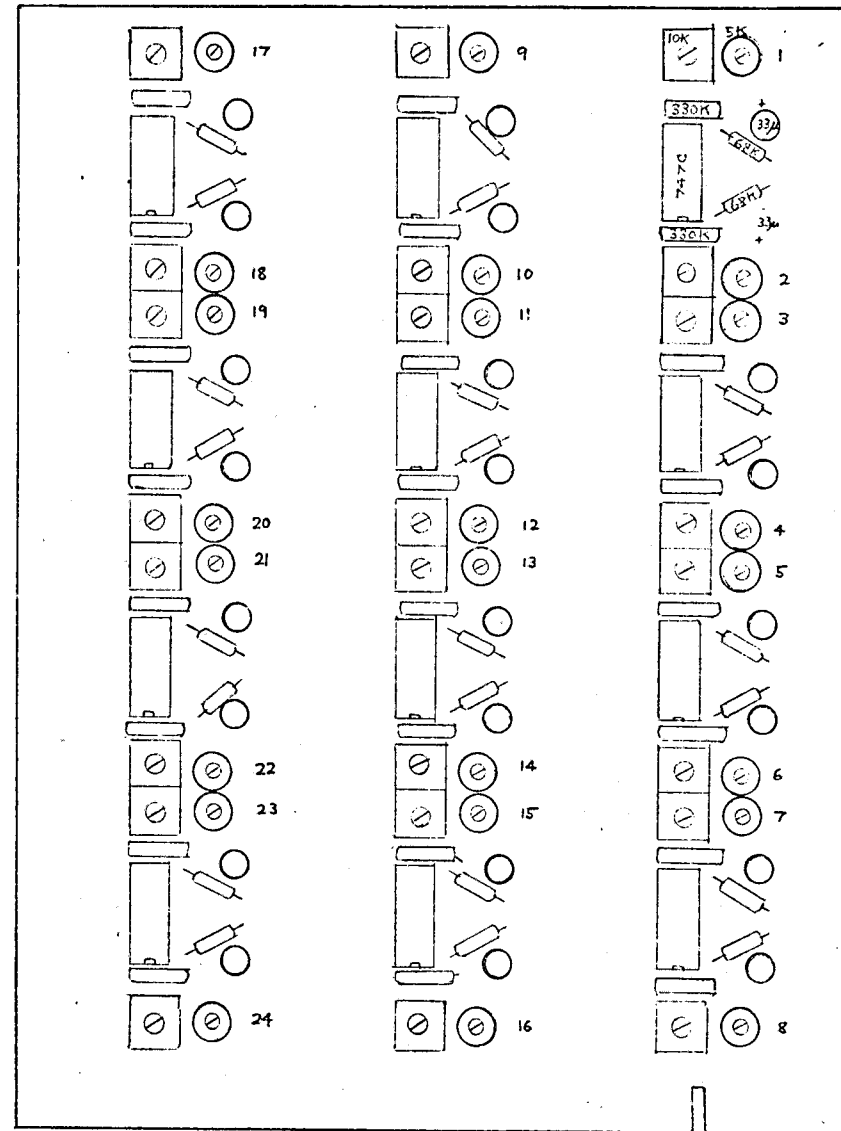
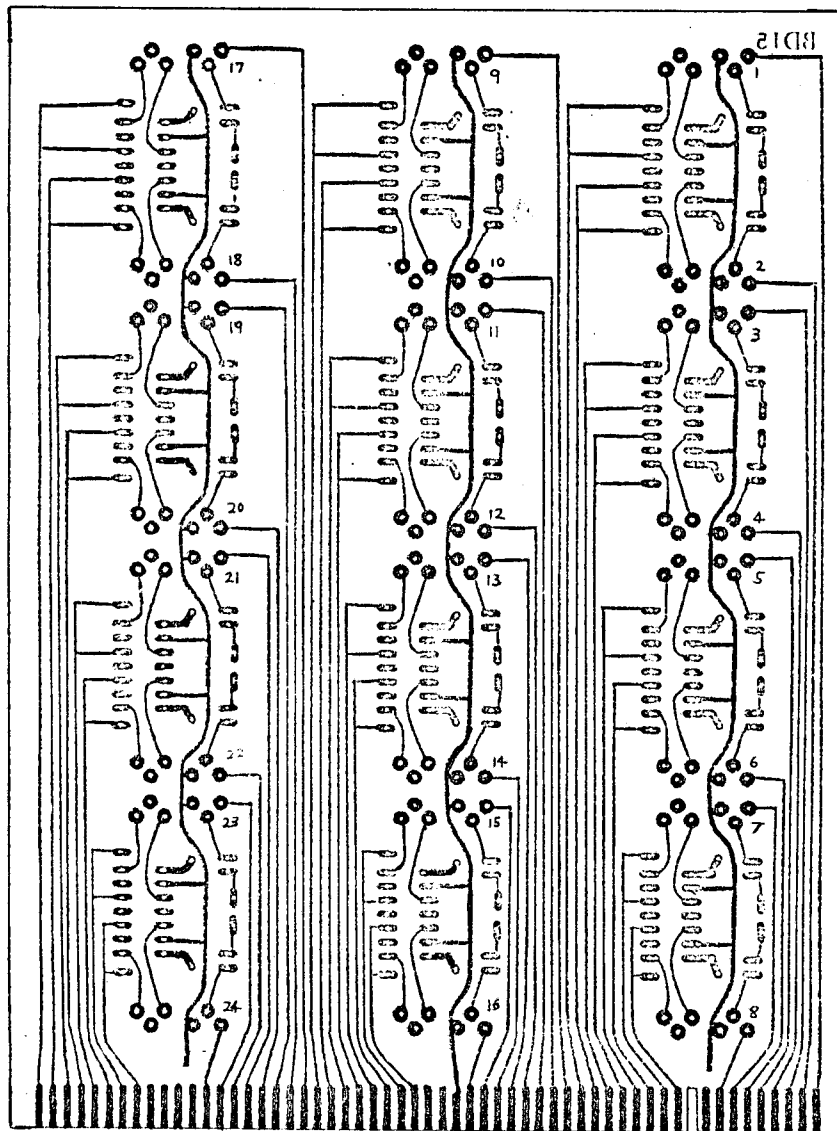


Fig. 12 Details of boundary grid circuit board, BD-AS.

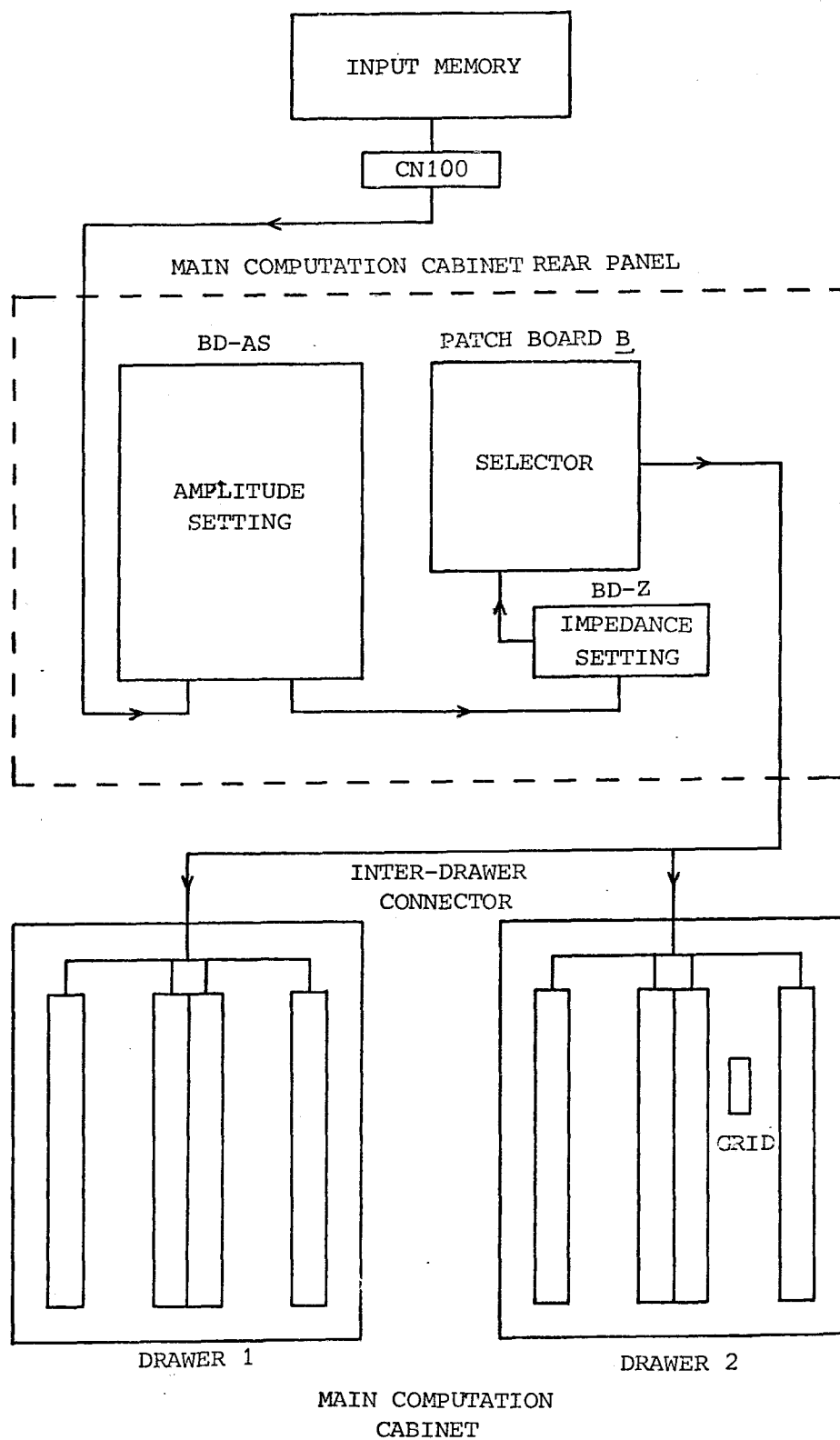


Fig. 13 Schematic diagram from the input memory to boundary grids.

2.5 Output circuits

Each grid card has three sets of output-data terminals:

X component (N-S component of water current)

Y component (E-W component of water current)

Z component (the water level)

The terminals for the first two sets of output are electrically 'floating' from the ground line, and their voltages are taken out through two differential amplifiers, so that the voltages can be referred to the ground line. One of the last set of terminals is grounded at the first place.

The output from all the grid cards are finally fed into the 'Output Memory' (Ref. 3) through a multiplexer.

Fig. 14 shows the scheme of the multiplexer* which includes above-mentioned differential amplifiers (which also work as impedance converters). 5 lines from each grid socket are connected to terminals X, X', Y, Y', Z of a 'grid selector' (No. 1 in the example shown in Fig. 14), which covers up to 30 grid sockets. The output of this selector (again 5 lines) is connected to one 'position' of the 'section selector', through impedance converters. The section selector has 30 positions (only 12 positions are used at the moment), i.e. all the selectors can cover up to 900 grid cards (or 2700 sets of output). The 5 lines of the final selector are connected to the output memory. Fig. 15 shows the details of the impedance converter board. The driving circuit for the multiplexer has been described in a separate paper (Ref. 4).

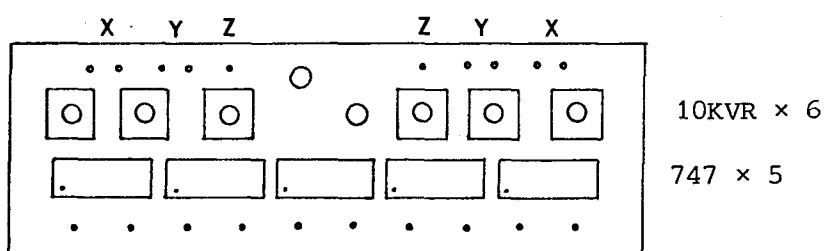


Fig. 15 Impedance-converter board.

2.6 Inter-drawer connections

In order to facilitate any connections between different drawers, 'inter-drawer connection terminals' have been prepared as shown in Fig. 16. Each wire of this connection has one terminal near the edge of a drawer, and the other terminal in patch board B, which is on the rear panel of the cabinet. Each drawer has 150 inter-drawer terminals.

*The design of this multiplexer was carried out in 1972. Although electronic analogue-multiplexers were available at that time, their price was more than ten times that for magneto-mechanical multiplexers. This was the main reason why the latter was employed for this scheme.

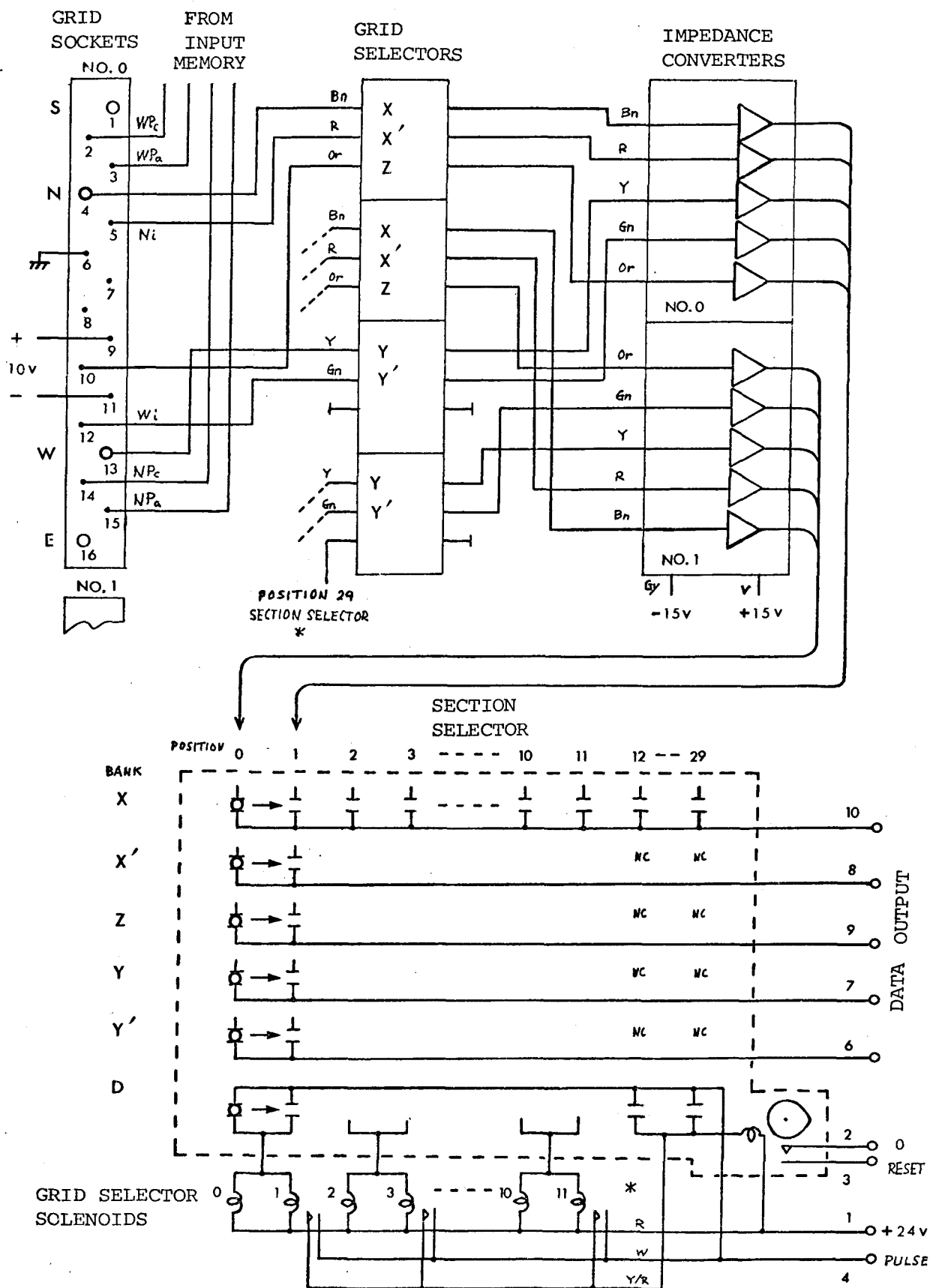


Fig. 14 Multiplexer, from each grid to connector CN5.

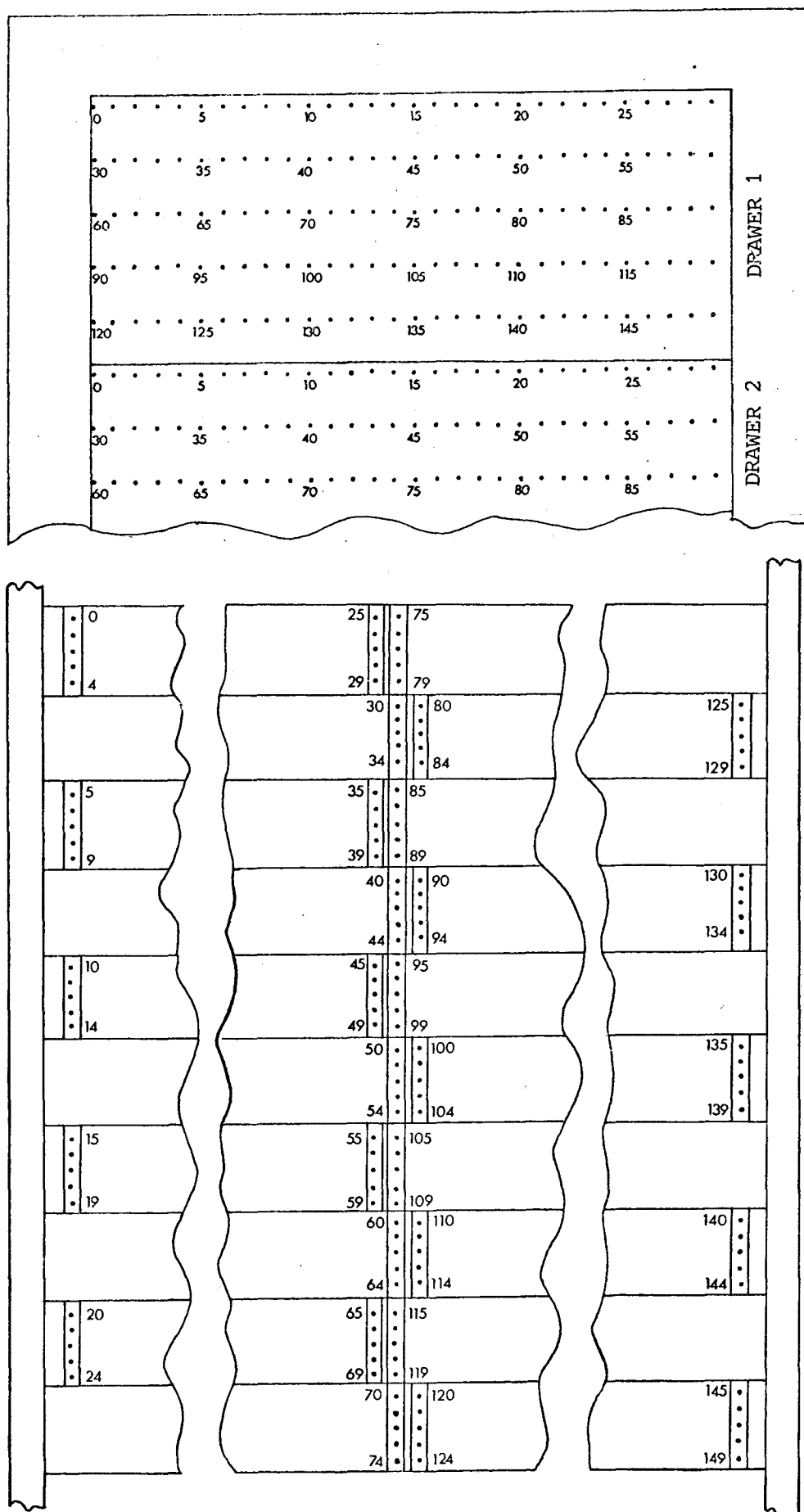


Fig. 16 Inter-drawer connection terminals in a drawer (bottom: scale 1/4 approx.), and patch board B (top: full scale).

3. GRID ARRANGEMENT FOR THE SEA AROUND THE BRITISH ISLES

An example of setting the model to the sea around the British Isles is described in this chapter, while the model can be set to any continental shelf sea.

Fig. 17 shows the grid scheme which requires 330 units* of 50 km \times 50 km square grids. This scheme has been designed to match the 100 km \times 100 km square grids of a meteorological model (a standard grid scheme designed by the Meteorological Office). Therefore, one meteorological grid corresponds to 4 sea grids in this scheme.

3.1 Grid parameters

The method of designing the grids, including the formula, has been described in a separate paper (Ref. 1). The model parameters are shown in Fig. 18 (18 diagrams on pages 20 to 37) by dividing the model into 6 areas. The following symbols and rules are used:-




	Boundary of model areas (electrically no boundary)
	Coastal boundary
	Boundary to an open sea (or non-coastal boundary)
Ω_e	In <u>ohms</u> , shown by the figure in the centre of each box.
R	In <u>ohms</u> , shown by the figure along the edge of each box. The value for $r = 0.24$ cm/s.
L	In <u>mH</u> , the value for a grid is shown by the figure in the centre of each box; and the average value for the grid and its adjacent grid is shown by the figure along the edge of each box.
F	In <u>mV</u> , the value for each grid is shown by the figure in the centre of each box; and the average value for the grid and its adjacent grid is shown by the figure along the edge of each box.

Table 1 (page 38) shows data for the non-coastal boundary termination.

Each grid card can be calibrated to the data given in this paper by using the grid-card calibrator (Ref. 3).

*This number is limited by an artificial reason: a particular committee who did not appreciate the details of the scheme in 1973 ordered the cessation of the manufacture of the grids, after 330 units had been made.

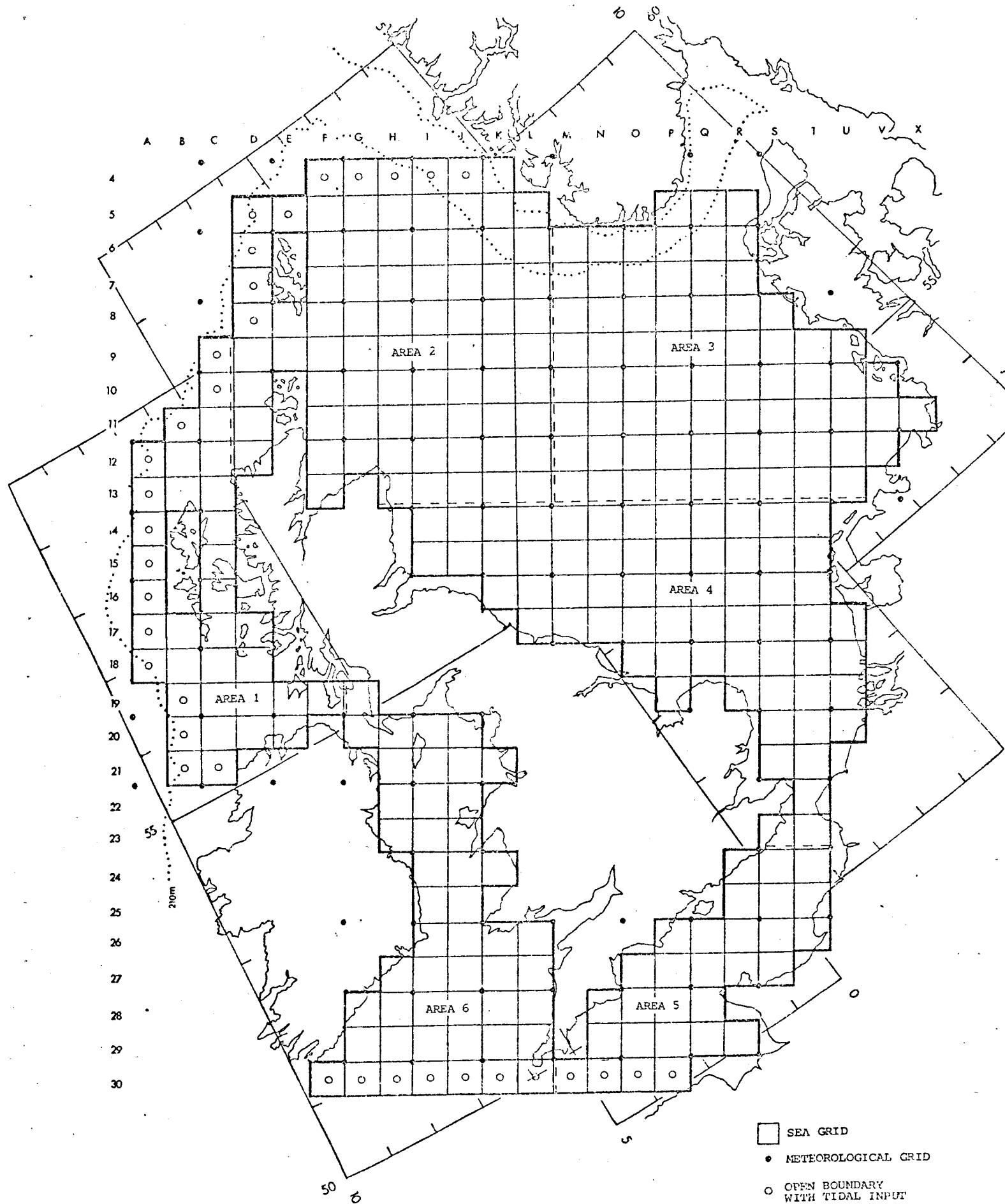


Fig. 17 Grid scheme of a model of the sea around the British Isles.

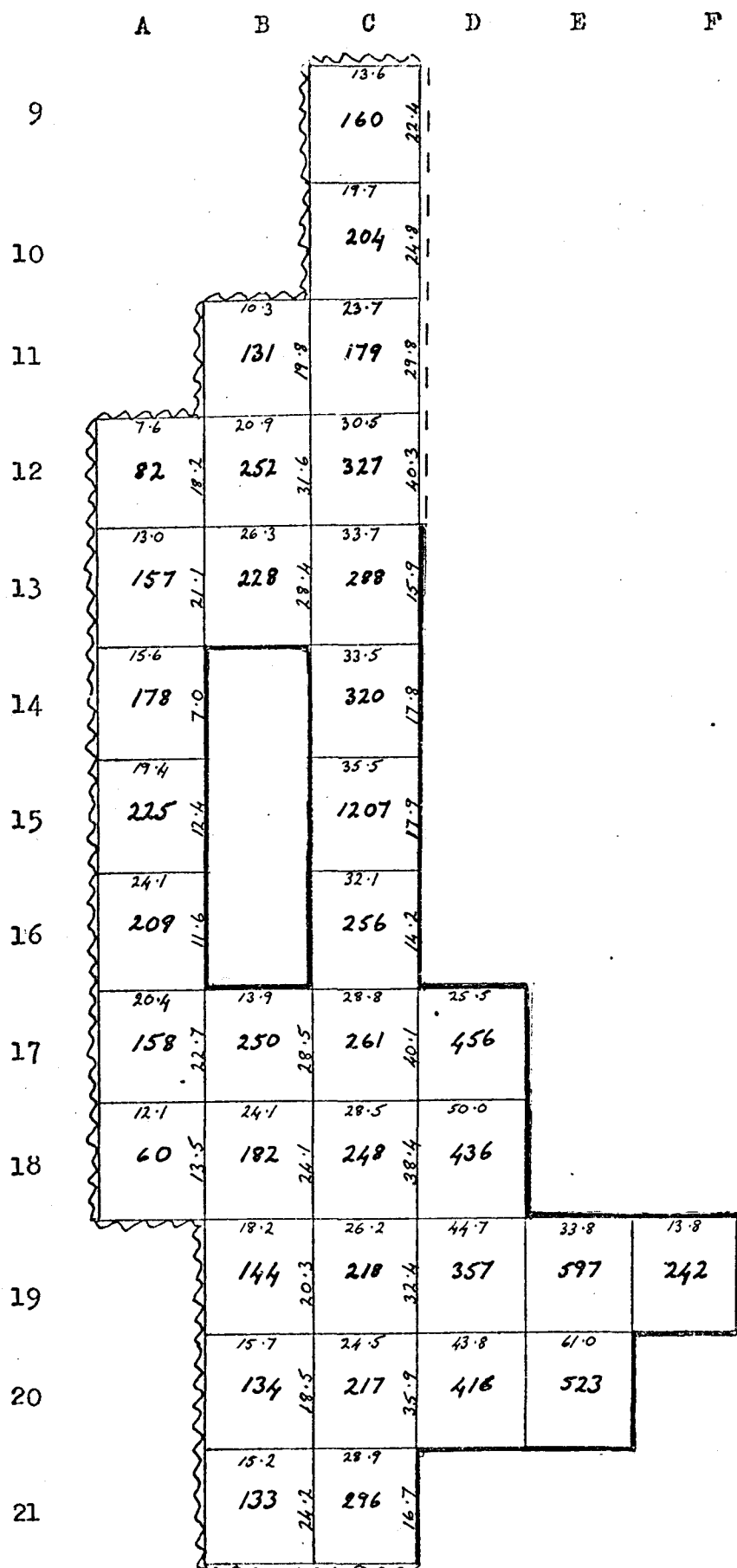


Fig. 18 (18 diagrams on pages 20 to 37). Model parameters.

A B C D E F

9

41.9

55.5

69.0

87.5

79.5

28.4

59.2

12.4

106

97.1

88.1

100

143

175

178

180

167

114

117

109

114

119

26.3

114

109

224

96.9

47.5

83.6

58.2

32.2

6.26

24.7

76.1

43.1

24.7

83.6

109

114

26.1

32.2

32.0

94.5

157

121

199

84.7

32.4

58.6

32.2

32.0

157

314

365

416

272

530

436

229

643

375

106

328

21

20

19

18

17

16

15

14

13

12

11

10

AREA 1
R

A B C D E F

9

35.2
70.3
80.3
90.2

10

29.0
96.6

11

29.0
57.9
80.5
103.0

12

18.1
36.2
74.1
112.0
85.0
124
129
145.6

13

26.5
69.8
107.0
101.9
115
129.0
136

14

39.8
79.6
74.7
136
143.6
131

15

50.5
100.9
90.3
131
145.6
130.7

16

42.2
94.4
97.7
115.8

17

82.9
71.3
113.3
116
118.5
118
164
208.0

18

13.6
27.2
49.3
98.1
117
157
199.9
204

19

32.9
65.8
82.9
99.9
107
132
164.4
192
220
275.5
194
112.0

20

30.3
61.8
80.9
99.9
146
192.3
202
211.6
244

21

30.7
61.4
98.7
135.9
118

AREA 1
L

	D	E	F	G	H	I	J	K
3			9.4 116 11.8	9.5 105 10.0	8.8 100 8.8	4.4 81 9.1	4.8 88 4.9	
4			14.3 152 17.3	14.8 171 17.9	15.1 199 16.3	9.9 103 10.2	9.4 85 9.9	5.3 97 5.3
5	11.4 102 13.9	15.4 158 17.1	16.7 161 17.9	18.4 173 20.1	21.6 202 20.5	15.2 178 15.7	10.7 112 11.1	10.3 93 10.5
6	14.3 166 9.9		17.4 163 17.8	18.3 167 19.4	21.2 191 20.9	20.1 194 19.0	14.5 155 13.9	10.5 100 10.5
7	19.0 189 10.2		18.6 183 19.3	18.4 172 19.0	20.1 180 20.1	20.9 190 20.7	18.8 189 20.3	15.3 182 17.8
8	21.2 205 26.5	15.5 286 26.2	20.6 197 20.7	18.9 174 18.7	19.0 170 19.0	20.1 179 21.8	22.4 220 26.9	24.8 270 31.6
9	24.8 254 28.3	30.0 268 26.2	22.3 213 21.7	20.0 185 19.9	19.0 179 17.9	17.9 148 18.8	22.7 194 26.2	30.4 290 33.3
10	27.5 253 13.8		25.5 254 25.1	21.3 204 21.3	19.9 185 19.0	17.0 161 20.0	21.4 194 26.6	29.4 251 28.8
11	30.9 314 27.2		31.8 326 30.5	33.8 230 24.7	22.2 218 23.4	20.2 206 25.6	24.9 257 28.4	28.1 256 28.0
12	37.3 368 20.2		40.6 413 40.6	30.5 324 33.1	27.3 275 31.1	27.2 285 32.1	30.5 292 30.8	28.8 261 27.7
13			50.0 472 27.2		32.4 308 35.0	33.7 321 33.7	32.1 283 30.4	29.1 260 28.3

AREA 2

$$F$$

$$\Omega_e$$

	13	12	11	10	9	8	7	6	5	4	3
D		229	165	106	106	106	106	106	106	106	106
E			180	109	119	134		43.1	41.9	37.1	9.11
F	416	291	89.9	70.2	75.5	64.3	55.5	43.1	41.9	42.0	21.4
G		180	85.7	64.2	57.3	50.6	49.1	45.6	48.3	46.9	21.4
H	161	131	81.4	58.2	53.8	48.3	53.8	60.2	66.6	64.3	10.9
I	180	141	72.8	44.3	37.1	53.8	60.2	62.2	52.1	17.4	10.7
J	141	149	114	64.3	64.3	81.4	60.2	40.2	20.6	12.0	12.7
K	119	119	114	109	134	125	55.5	16.8	14.4	15.6	

	D	E	F	G	H	I	J	K
3			25.1 50.2	22.9 45.7	18.0 35.9	17.8 35.5	19.4 38.8	
4			58.2 66.2	60.1 74.4	61.5 87.1	40.4 45.3	38.2 37.6	40.2 42.8
5	22.3 44.5	34.5 68.9	67.6 68.9	74.9 75.5	87.9 88.6	61.9 78.4	43.4 49.2	42.1 41.3
6	58.4 72.3		70.1 71.3	74.4 73.3	86.5 84.3	82.1 85.7	59.1 68.9	42.9 44.5
7	77.6 82.9		76.1 80.9	74.7 76.1	80.6 76.9	85.0 84.3	76.6 84.3	62.7 80.9
8	86.6 90.2	125.9	84.0 87.1	76.7 77.2	76.2 75.5	81.5 79.6	91.2 98.0	101 121.4
9	101 112.0	122 118.5	90.8 94.4	79.7 82.2	77.6 79.6	72.9 66.2	92.4 87.1	124 125.9
10	101 112.0		104 113.3	86.6 91.0	81.3 82.9	69.3 72.3	87.1 87.1	120 113.3
11	121 139.6		129 145.6	97.0 103.0	90.5 98.0	82.5 92.7	101 115.8	115 115.8
12	155 164.4		165 185.3	124 145.6	111 124.3	111 129.0	124 132.4	117 118.5
13			203 221.6		132 140	138 146	131 129	119 119

AREA 2

L

Ω^e

F

AREA 3

	5	6	7	8	9	10	11	12	13
L	5.5	9.3	14.3	24.5	30.3	34.5	37.4	32.8	26.9
M	4.5	9.2	12.8	24.2	31.0	36.7	35.2	38.0	27.0
N	4.5	9.2	16.8	32.2	37.5	40.0	37.4	30.5	29.6
O	5.0	9.0	18.4	36.3	39.5	40.9	39.1	41.4	37.5
P	7.94	10.3	20.4	41.5	46.5	50.6	54.3	61.0	69.2
Q	8.9	28.5	59.1	58.8	50.6	55.6	58.4	62.5	59.7
R	46.3	124.7	86.0	67.5	55.5	48.2	52.4	58.2	59.7
S				52.0	71.5	79.2	64.2	62.5	52.9
T					81.9	57.9	54.8	54.6	41.8
U					12.3	12.0	80.1	67.6	69.5
V					139	206	120	83.4	68.2
X					139	382	370	114	62.5

	L	M	N	O	P	Q	R	S	T	U	V	X
5	16.8 15.6				4.22 9.71	15.2 612	1208 1963					
6	14.4 13.1	11.7 11.7	11.7 12.9	14.0 16.1	18.2 79.6	14.1 1430	2718 2011					
7	34.8 31.5	28.1 38.2	48.3 53.3	58.2 112	16.5 388	610 957	1303 1057					
8	157 159	161 171	180 205	229 266	302 456	610 710	810 1170	1527 1369				
9	175 195	215 230	245 258	271 327	382 269	455 503	550 879	1208 2324	3440 7170	10.9 6809		
10	125 158	191 203	215 209	202 271	339 378	416 259	416 513	610 909	1208 1963	2718 28709	55.0 28410	
11	99.7 133	166 171	175 202	229 364	499 525	550 525	499 525	550 597	643 780	916 1368	1820 28410	55.0
12	97.6 102	109 133	157 205	253 532	810 680	550 503	455 503	550 597	643 845	1047 1434	1820	
13	106 116	125 150	175 337	499 773	1047 751	454 396	338 419	499 609	719 1461	2202		

AREA 3
R

	L	M	N	O	P	Q	R	S	T	U	V	X
5	44.5				11.2	22.3	42.3	210	378			
	42.9				34.3	85.7	412					
6	41.3	41.3	40.6	46.3	46.3	129	566					
	42.9	41.3	41.0	43.5	46.3	87.7	348					
7	52.9	49.5	58.1	64.6	93.2	79	479					
	64.5	57.6	75.5	82.9	140	268	392					
	72.7	61.1	66.6	79.2	111	204	330					
8	136	136	146	164	188	268	309	425				
	129	136	141	155	176	228	289	367				
	140	148	158	172	200	250	282	402				
9	144	159	170	179	212	232	255	378	600	867	1133	
	135	152	165	175	196	222	244	317	489			
	133	155	165	167	206	227	234	323	489	850		
10	121	150	159	155	200	222	222	245	323	472	566	
	117	136	155	159	178	211	222	245	323			
	117	143	155	160	222	239	233	262	327	448		
11	112	136	144	164	243	255	243	265	276	303	329	
	114	124	140	154	204	249	249	266	276	396	463	
	110	125	140	189	276	255	238	255	276	291		
12	107	113	136	173	309	255	232	255	276	264	252	
	113	110	125	155	241	244	244	244	266			
	110	117	140	208	331	244	222	249	284	431		
13	112	121	144	243	352	232	200	243	291	510		
	116	117	133	194	298	292	215	222	267	400		

AREA 3
L

I	J	K	L	M	N	O	P	Q	R	S	T	U
40.6	32.5	29.5	29.0	32.5	39.6	63.9	91.2	73.1	52.9	61.0	76.1	14
407	297	265	270	311	287	257	842	779	494	541	691	15
52.5	36.8	32.5	32.9	35.2	45.6	95.1	92.2	75.9	61.3	67.0	92.4	16
530	347	312	311	310	414	911	808	543	570	610	895	17
												18
												19
												20
												21
												22
												23

AREA 4

F

 Ω_e

AREA 4
L

	I	J	K	L	M	N	O	P	Q	R	S	T	U
14	166 183	161 136	129 121	118 123	133 144	162 179	297 352	372 392	298 364	216 232	244 255	249 329	310
15	214 243	148 159	134 144	134 144	144 144	186 192	389 425	385 378	310 255	250 268	273 291	377 425	
16	152 195	144 144	144 144	144 144	183 173	340 255	305 232	273 232	272 291	342 276	342 392	444 463	
17	165 185	165 185	165 185	189 192	255 255	281 329	358 425	402 378	377 425	365 352	372 392	496 600	
18	292 510	505 680	595 680	623 566	496 425	402 425	351 276	409 425	444 463	370 276	421 463	798 1133	
19	1190 1699	444 463	370 276	314 352	389 515	459 566							
20													
21													
22													
23													

I	J	K	L	M	N	O	P	Q	R	S	T	U
291	157	125	131	175	271	1047	1303	1123	455	550	916	14
395	186	153	156	178	223	1288	1255	836	532	634	1223	15
499	215	180	180	180	314	1529	1208	550	610	719	1530	16
		198	178	178	314	1039	831	634	626	1011	1675	17
		215	175	175	314	550	455	719	643	1303	1820	18
			175	175	289	550	715	1124	925	1175	1561	19
			175	175	214	550	979	1529	1208	1047	1303	20
						2202	3914	2719	1529	643	1529	21
									1820	643	1047	22
										925	1433	23
										1208	1820	
										2561	1820	
										3914	1820	
										2867	1231	
											643	
											1208	
											799	
											1963	

AREA 4
R

N O P Q R S T

	P	G	H	I	J	K	L	M
30		192	214	204	198	228	330	255
29		249	224	210	209	246	275	
28		259	256	238	219	247	406	
27			397	280	255	286	425	
26				341	267	636	578	
25				354	290			
24				402	322	106		
23								
22								
21								
20								
19								

AREA 6
F
Q

[illegible]

Table 1. Non-coastal boundary termination.

Channel No of Input Memory *	No. on BD-Z	Z_s (K Ω)	Drawer No.	Inter- drawer Con. No.	Socket No.	Grid No.	Wire colour
0	1	1.39	1	19	409	J4	Bn
1	2	1.53	1	20	309	I4	B
2	3	2.13	1	21	209	H4	Or
3	4	1.97	1	22	109	G4	Y
4	5	1.85	1	23	009	F4	Gn
5	6	1.86	1	24	609	E5	Be
6	7	1.89	1	80	232	D5	Bn
7	8	1.52	1	81	231	D6	R
8	9	1.93	1	82	230	D7	Or
9	10	2.07	1	83	171	D8	Y
10	11	2.17	1	84	160	C9	Gn
11	12	1.92	1	85	193	C10	Be
12	13	2.17	1	86	182	B11	V
13	14	1.73	1	87	181	A12	Gy
14	15	1.37	1	88	180	A13	W
15	16	1.90	1	100	165	A14	Bn
16	17	2.04	1	101	164	A15	R
17	18	2.28	2	20	249	A16	Gy
18	19	2.21	2	21	248	A17	Gy
19	20	1.92	2	22	247	A18	Gy
20	21	1.19	2	23	256	B19	Gy
21	22	1.19	2	10	255	B20	Gy
22	23	1.78	2	11	254	B21	Gy
23	24	2.64	2	12	264	C21	Gy
N.Ch.	25	2.41	2	15	296	F19	Gy
Skag.	26	4.43	1	70	128	R5	Or
"	27	1.48	1	71	118	Q5	Y
"	28	1.08	1	72	108	P5	Gn
E.Ch.	29	4.43	1	75	220	T23	Bn
"	30	4.27	1	76	210	S23	R

$$Z_s = \left(\frac{K_i}{K_e} \right) \frac{1}{AL \sqrt{gh}} \sqrt{\frac{r}{h} \frac{T_e}{j 2\pi K_t} + 1} = \frac{K_i}{K_e} \frac{1}{AL \sqrt{gh}}$$

$$Z_s = 2.30 \times 10^5 \times 1/\sqrt{h} \quad \text{for} \quad Z_t = 3.60 \times 10^{13} \text{ cm}^2 \text{ s}^{-1} \text{ AV}$$

h in cm

* Boundary mode.

3.2 Arrangement of the grid cards

The diagram shown in Fig. 19a is the grid-card matrix board in Drawer 1 seen from the card side. Each socket is represented by a small square. Fig. 19b is the same as Fig. 19a except for Drawer 2 which is not fully used.

Each socket has a serial 'grid-socket number' as shown in Fig. 5. This number can be obtained in Fig. 19 by adding a number in its column to a number in its line.

Each grid card has a 'grid-card number', F13 for example, which indicates the location of the grid in a geographical map (or a grid scheme) shown in Fig. 17.

The scheme of connecting a certain grid card to a certain grid socket is indicated in Fig. 19 by putting the grid-card number in the small square which corresponds to the grid socket. For example, Fig. 19a shows that grid card F13 should be connected to grid socket 000 (top-right corner of this diagram).

The following symbols and rules are also used for Fig. 19:-






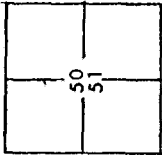

	A normal grid which is connected to the 4 adjacent grids two-dimensionally.
	A grid one of the edges of which (shown by a dotted line) is electrically connected to another grid (T13 in this example) in a remote position in a drawer.
	A grid on a coastal line of the sea. One of the edges facing the coast is indicated by a thick line.
	A grid on a boundary to an open sea (or a non-coastal boundary). One of the edges of the grid facing the open sea is indicated by a zig-zag line. This grid is terminated by a 'non-coastal boundary terminal' (page 38), and normally connected to the input memory in its 'boundary mode'. The channel numbers of the terminal and input memory are the same, and the number is indicated in small letters.
	The same as above, but without the input memory. Often for an experimental termination.
	The set of two numbers in the centre of a meteorological grid (or 4 sea grids) indicates the two channel numbers of the input memory which are connected to these sea grids.
	Unused grid-socket.

Fig. 20 shows the arrangement of the inter-drawer connections for the grid scheme shown in Fig. 19.

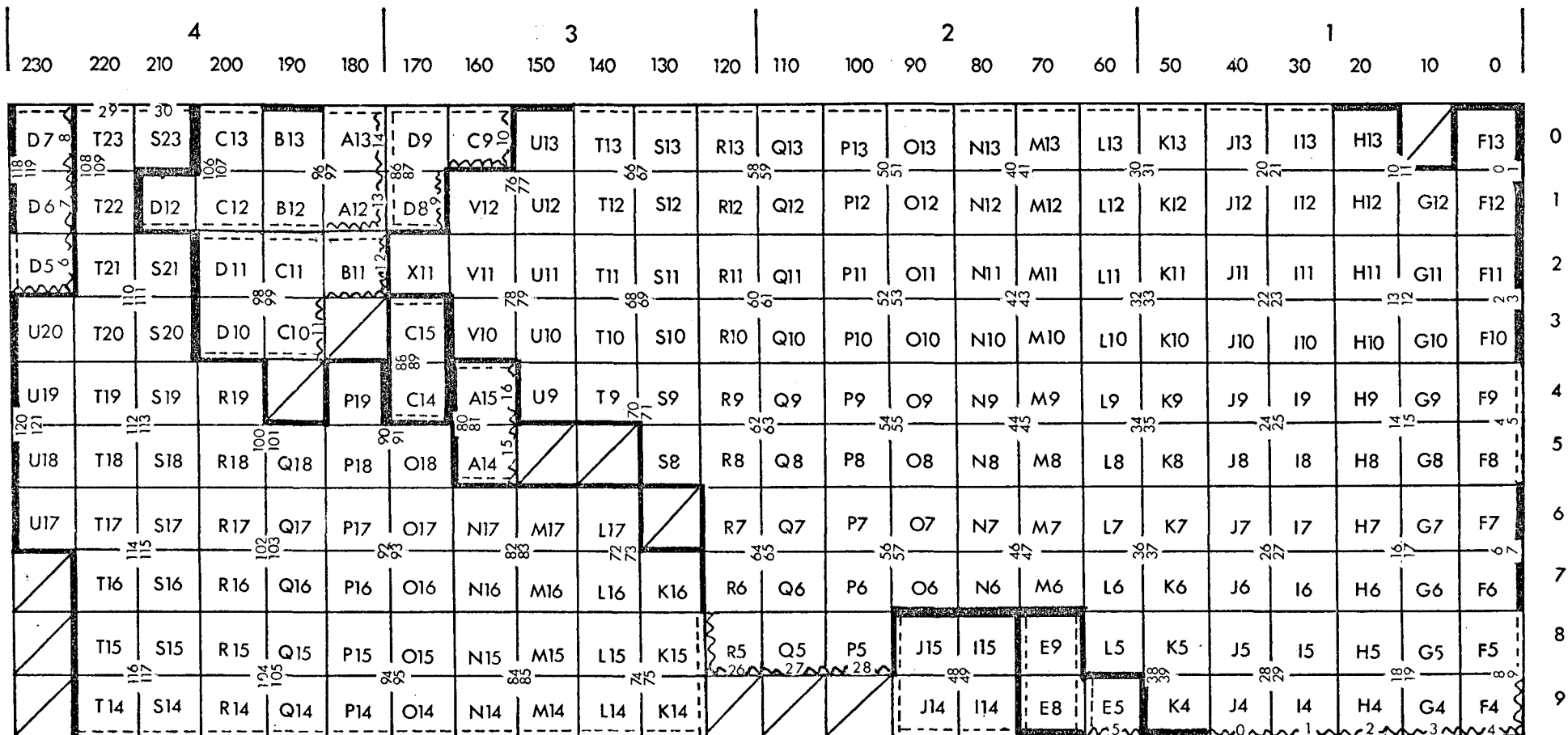


Fig. 19 Arrangement of grids on the socket matrix board for the scheme shown in Fig. 17 (continued to the next page).

6						5					
314	313	312	311	310	300	290	280	270	260	250	240
L30	K30	J30	I 30	H30	G30	F30		P30	O30	N30	M30
L29	K29	J29	I 29	H29	G29	R29	Q29	P29	O29	N29	
L28	K28	J28	I 28	H28	G28		Q28	P28	O28	N28	
L27	K27	J27	I 27	H27	S27	R27	Q27	P27	O27		
L26	K26	J26	I 26	T26	S26	R26	Q26	P26	C21 ²³	B21 ²²	
		J25	I 25	T25	S25	R25	E20	D20 ¹²⁸	C20 ¹²⁹	B20 ¹²²	
	K24	J24	I 24	T24	S24	F19 ¹³⁴	E19 ¹³⁵	D19 ¹³⁰	C19 ¹³¹	B19 ¹²⁴	
		J23	I 23	H23		R24		D18 ¹³²	C18 ¹³³	B18 ¹²⁵	A18 ¹²⁶
		J22	I 22	H22			G19	D17 ¹³²	C17 ¹³³	B17 ¹²⁶	A17 ¹²⁷
	K21	J21	I 21	H21	J20	I 20	H20	G20	C16		A16

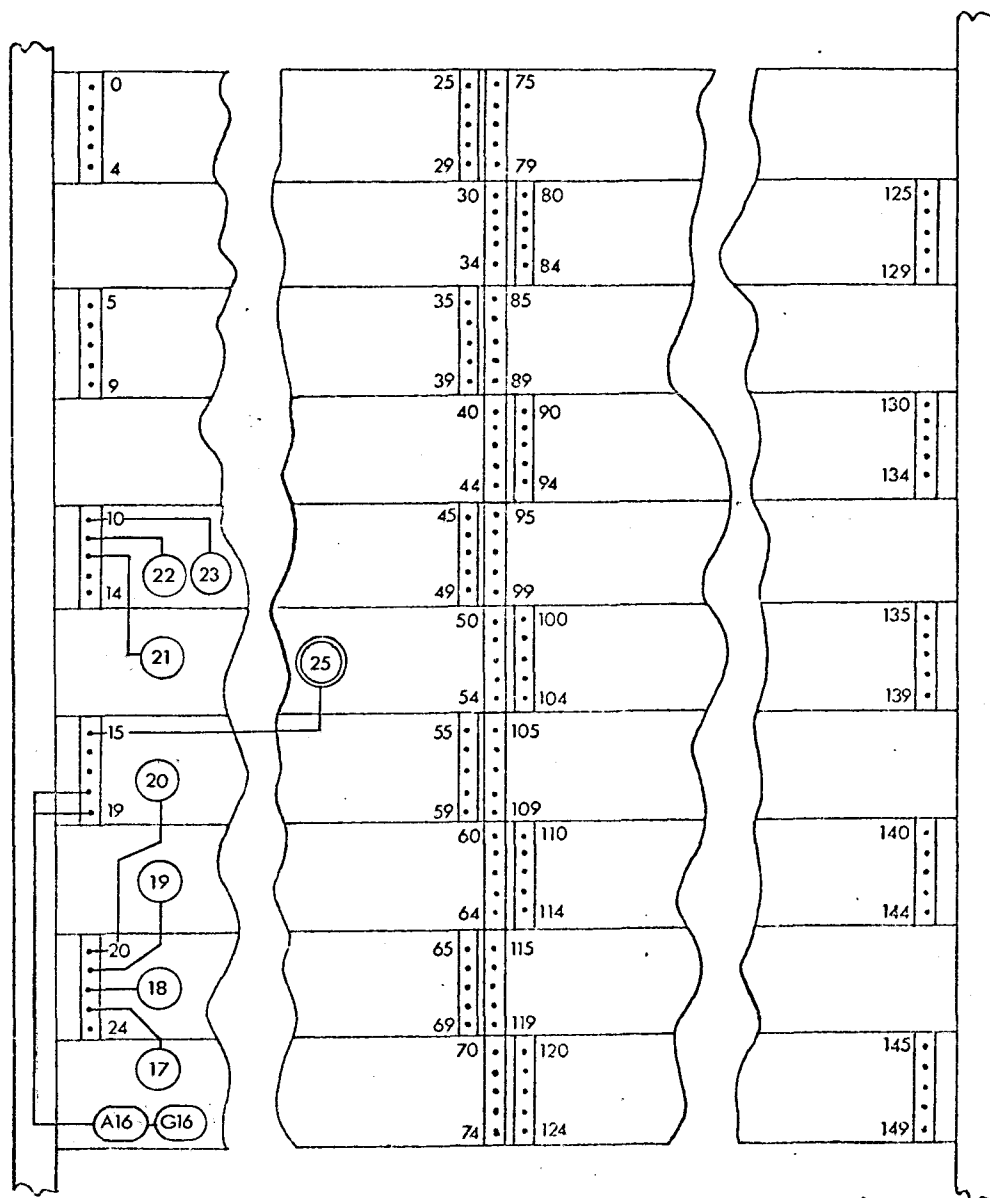


Fig. 20b Arrangement of the inter-drawer connection for the grid scheme shown in Fig. 17 (Drawer 2).

See the explanation on the previous page.

3.3 Arrangement of Patch Board A

Fig. 21 shows the physical arrangement of Patch Board A (see Fig. 9) for the scheme shown in Fig. 19. The following rules are applied:-

Each section of the patch board consists of four terminals representing N-, N+, E-, E+ (see Fig. 9). Each section has a serial number. This is the same as the grid socket number to which the patch board terminals are permanently connected.

A section number of the patch board is given in Fig. 21 by adding its Column Number (vertically written on the right side of the diagram) to Line Number (horizontally written on the top of the diagram).

Each terminal in a section can be identified by Fig. 9. Note, Fig. 21 has been made in the same way as Fig. 9.

A terminal to which the output of the input memory should be connected is indicated in Fig. 21 by the channel number of the input memory.

Two terminals, in the patch board, which should be connected directly are indicated in Fig. 21 by \longleftrightarrow generally. If two terminals, which should be connected, are not physically near in the diagrams, they are indicated, for example, by



An unused terminal is marked by X, if it has to be emphasised.

See Figs. 9 and 10 for other rules.

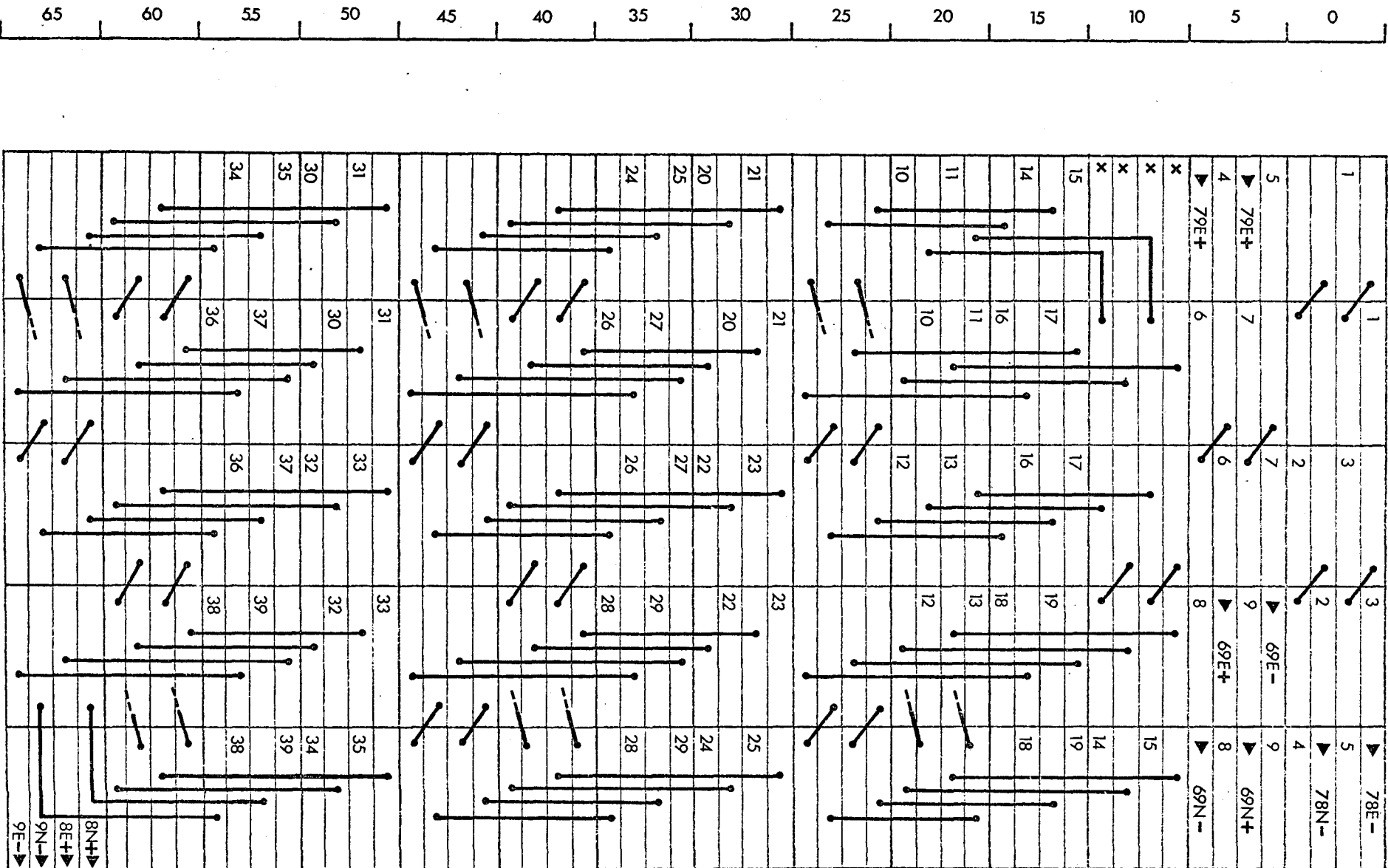
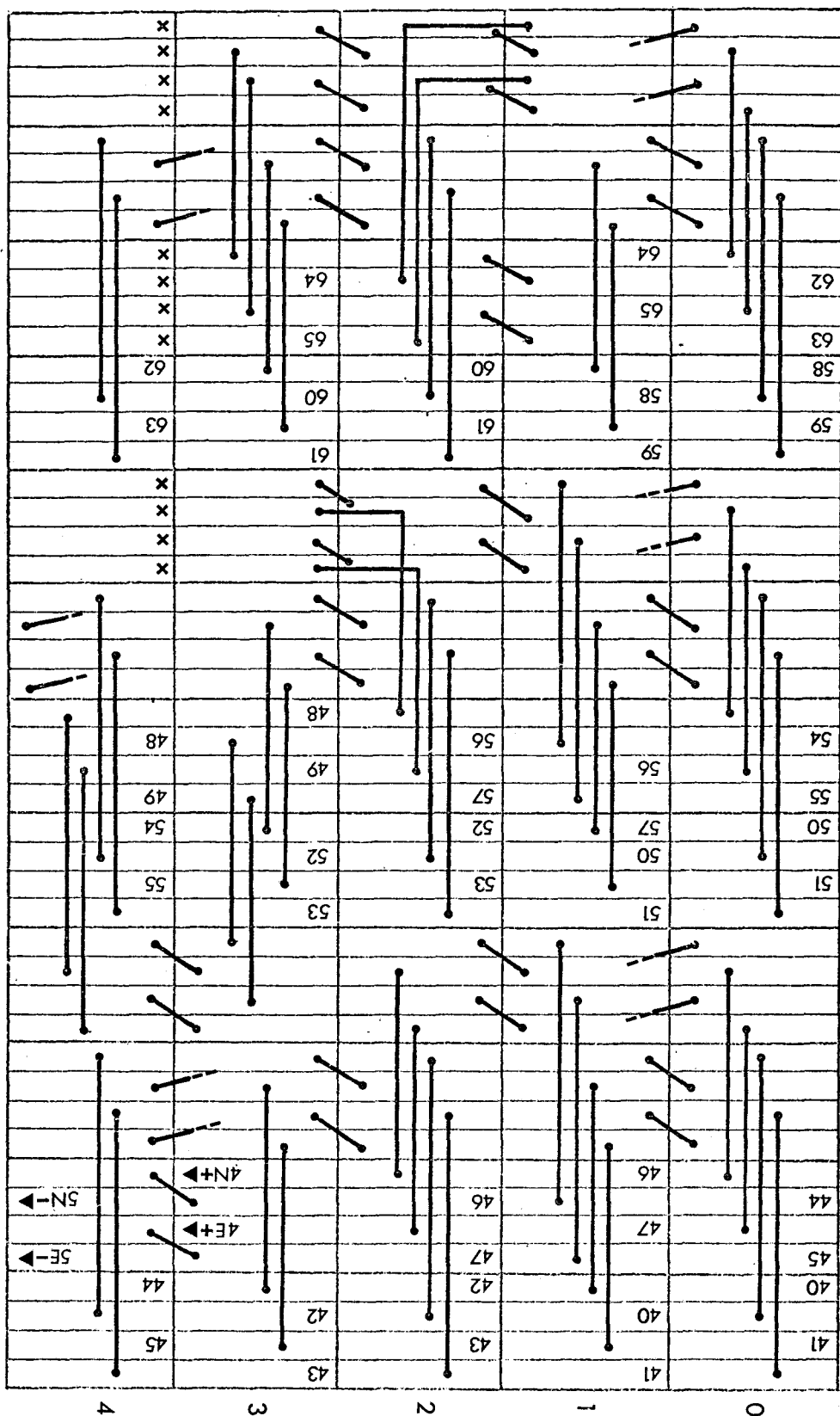
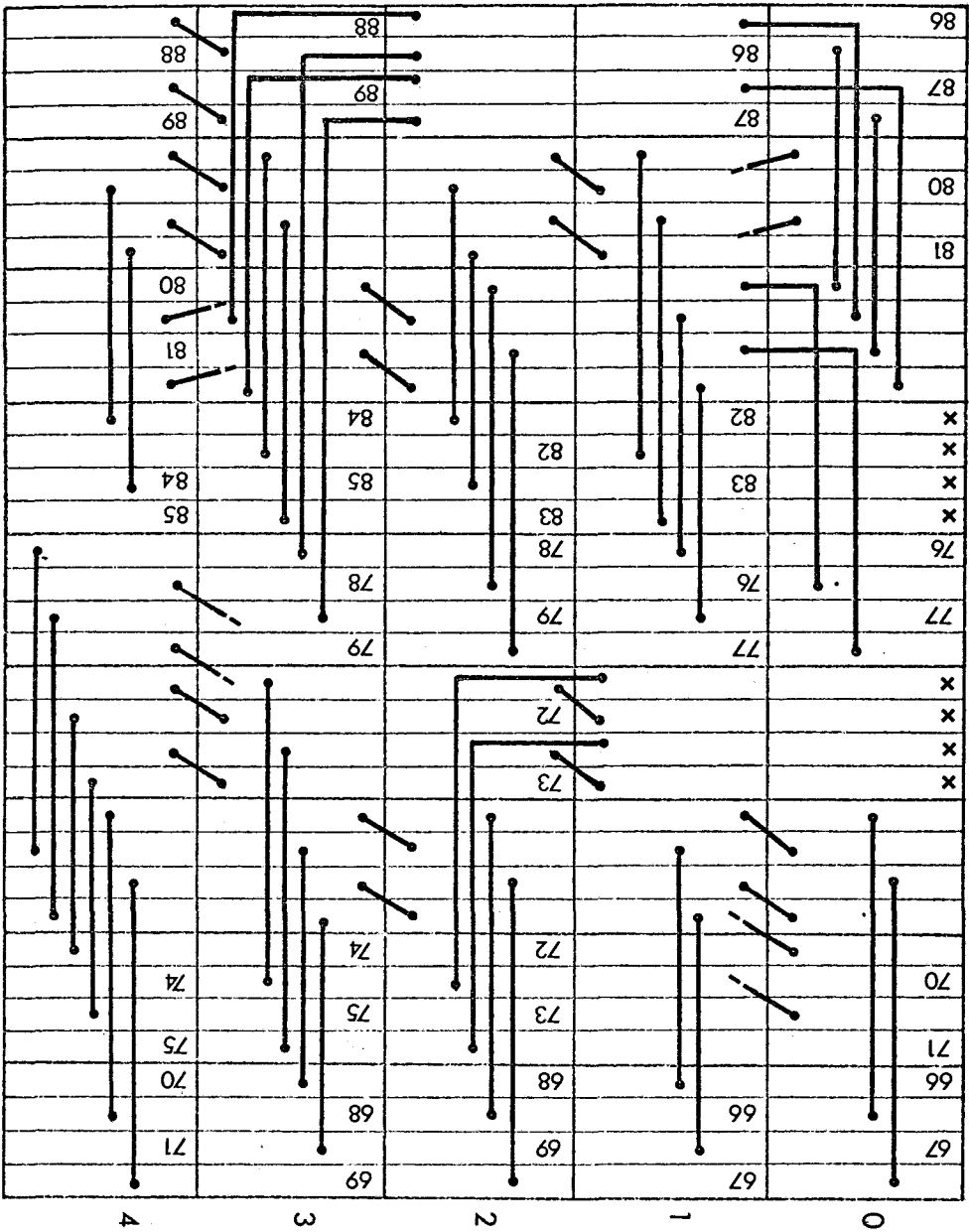
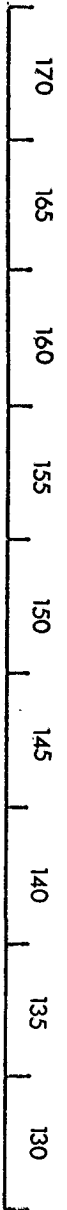
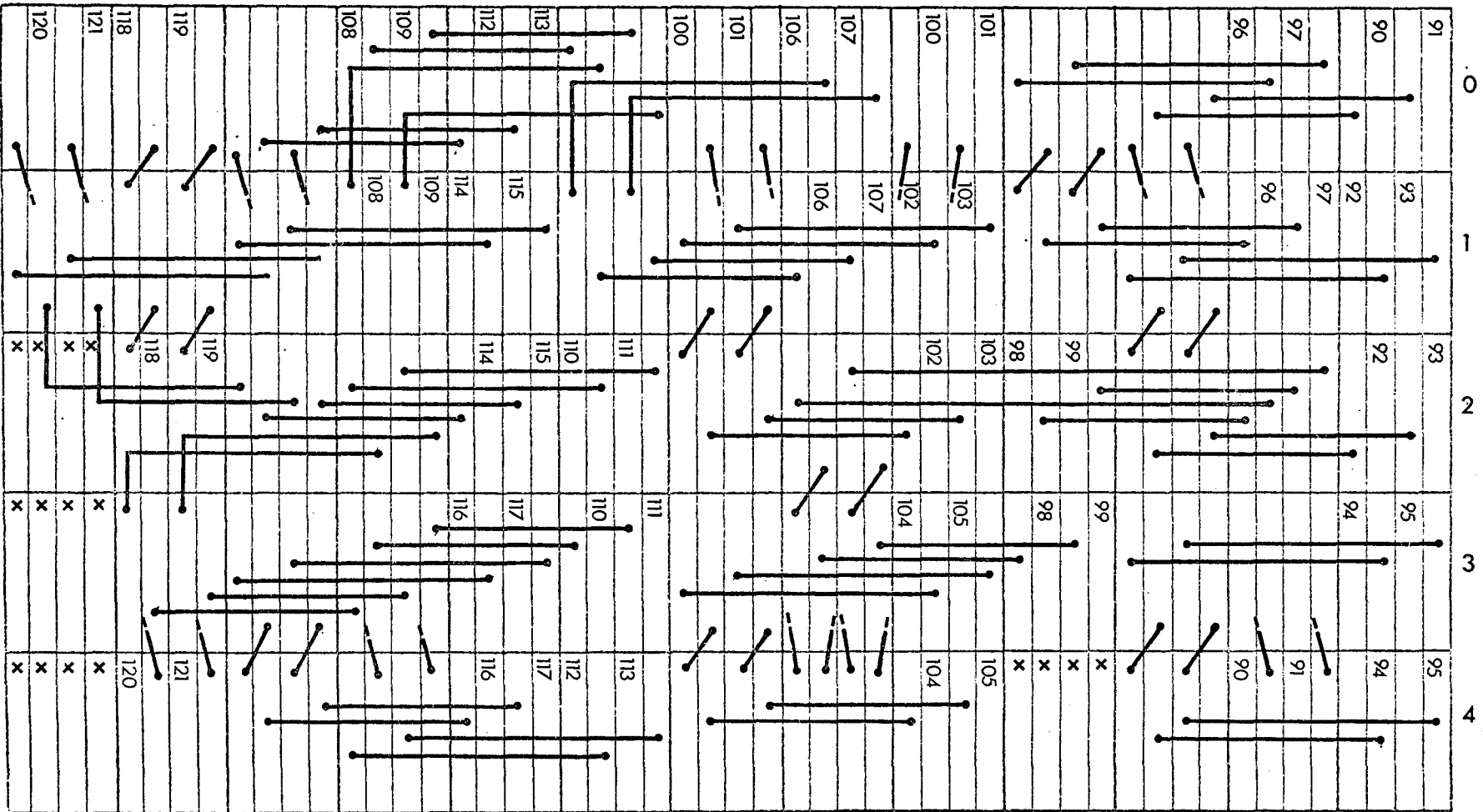
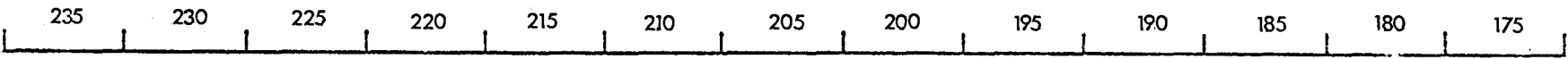
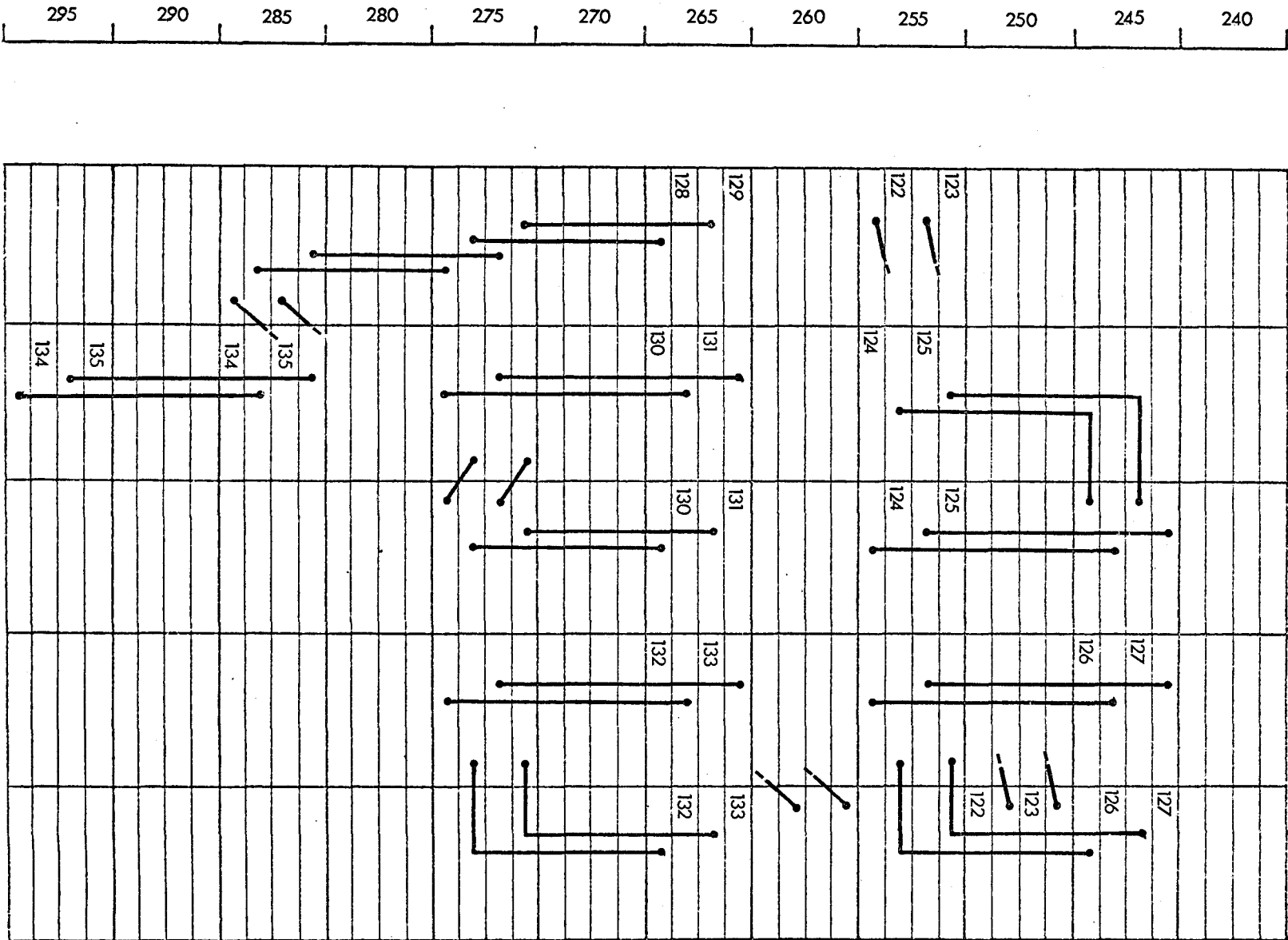


Fig. 21 (5 diagrams on pages 45 to 49). Physical arrangement of patch board B for the grid scheme shown in Fig. 17.









4. CONCLUSION

After the construction of the main computation network has been explained mainly for the user's aid, the scheme of a particular tide/surge model for the sea around the British Isles has been described with full data for its time-independent parameters.

ACKNOWLEDGEMENTS

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IOS Internal Document 92, 25 pp.

Fig. A1 shows the connections of a channel from the input memory to a grid card, through connectors CN21 etc and the patch boards within the main computation cabinet.

When the connections for each section (corresponding to one connector, e.g. CN21) of the patch board has been completed, it is recommended that the circuit connections are checked as follows:-

- (1) Connect all the grid cards involved to their sockets in the main computation cabinet.
- (2) Disconnect connectors CN21 etc from the input memory.
- (3) Measure the resistance between a pair of pins (No. n and No. n+1) by a resistance meter, with a constant polarity relationship (pin No. n for -, and pin No. n+1 for +), as shown in Fig. A2.
 - (a) If the resistance meter indicates approximately 50 k Ω , the connection in the channel is correct.
 - (b) If the meter indicates zero ohm, there is a short circuit.
 - (c) If the meter indicates infinite resistance, reverse the polarity of the meter leads. Then, if the meter indicates approximately 50 k Ω , there is a reversed connection.
 - (d) If the meter indicates an infinite resistance in the normal and reversed polarities, there is an open circuit.

An accidental open circuit can be traced on a resistance meter which is connected as shown in Fig. A3. Touch the circuit in question with the probe starting from one of the patch board pins, and continue through all the components. When a grid card is checked, Pin No. 2-3 (for N) or Pin No. 14-15 (for E) of each edge connector should be touched by the probe.

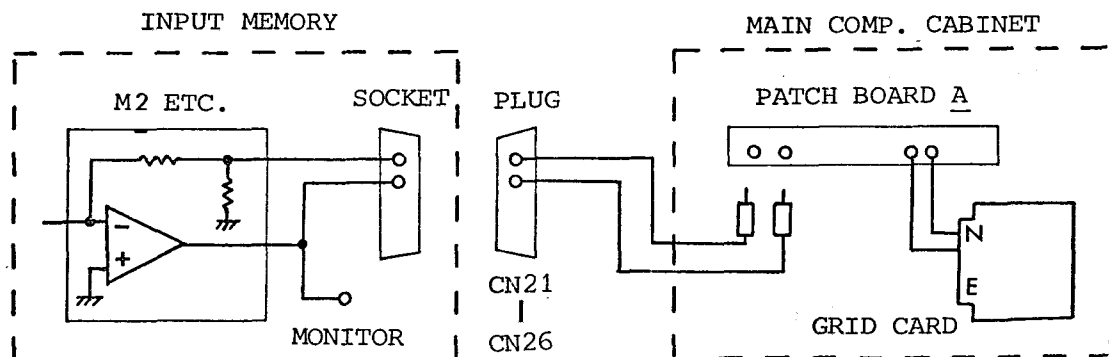


Fig. A1

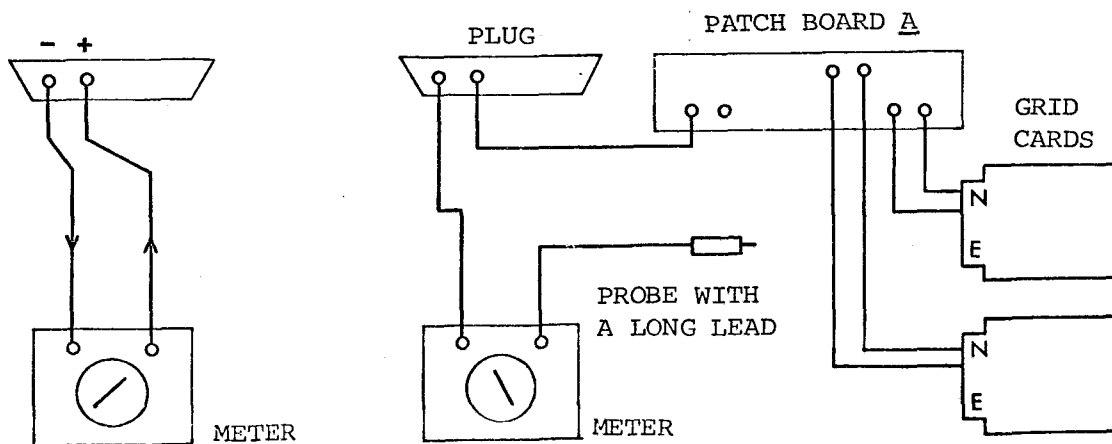


Fig. A2

Fig. A3

APPENDIX 2 STRAY CAPACITANCE

The main computation network has been designed so that the errors due to its stray capacitance do not exceed $\pm 1.5\%$ of the full scale of the final output.

Actual stray capacitances within the 'floating part' of the network can be neglected compared with those of fixed components.

The stray capacitance between the floating part of the network and earth is relatively large. The main components involved in this capacitance are the floating DC-power supply units, selectors of the multiplexer, grid sockets and wires.

Table A1 shows the values of the fixed capacitors, stray capacitance of one grid, and their deviations.

Table 1 Values of the fixed capacitors and stray capacitance of one grid, and their deviations.

	Nominal (PF)	Deviation (PF)	Temperature effect (%)			
			0°C	12°C	20°C	30°C
Fixed capacitors on grid card, C	15000 3300 680	± 150 ± 33 ± 20	+0.2	0.0	-0.2	-0.3
Stray capacitance between grid centre and earth, C _s *	280	± 58				
Total	19260	± 261				

$$19260 \pm 261 \text{ (PF)} = 19290^{+231}_{-291} \text{ (PF)} = 19290 \text{ (PF)} \begin{matrix} +1.2 \\ -1.5 \end{matrix} \text{ (\%)}$$

*The grid card is excluded. The multiplexer is included, but its wiper is not engaged with the grid. If it is engaged, the following value should be added:

Stray capacitance of the wiper alone $C_w = 150 \text{ (PF) max.} = 0.78 \text{ (\%)} \text{ of } 19290 \text{ (PF)}$

In order to avoid the increase of the stray capacitance other than that of the wiper, an impedance converter (see Fig. 15) is inserted between each wiper and its external circuit. Consequently, the increase of the stray capacitance due to the external circuits remains within 1 PF, independent from them.

Table A2 shows the values of C_s against grid-socket numbers.

Table A2 Values of C_s against each grid number.

	<u>0</u>	<u>10</u>	<u>20</u>	<u>30</u>	<u>40</u>	<u>50</u>	<u>60</u>	<u>70</u>	<u>80</u>	<u>90</u>
0	228	255	272	223	253	268	227	254	255	236
1	264	278	292	258	294	322	270	274	299	282
2	274	287	308	270	307	323	267	284	311	284
3	275	289	321	266	306	330	277	317	313	285
4	269	299	323	287	305	337	286	306	317	281
5	281	292	302	252	288	317	259	286	316	250
6	253	291	328	266	280	319	272	291	316	247
7	252	311	308	271	272	326	280	302	298	266
8	266	302	313	276	299	326	293	302	310	260
9	243	271	291	254	259	286	245	267	292	249
	<u>100</u>	<u>110</u>	<u>120</u>	<u>130</u>	<u>140</u>	<u>150</u>	<u>160</u>	<u>170</u>	<u>180</u>	<u>190</u>
0	222	291	222	243	256	224	248	261	223	250
1	288	307	264	290	298	258	282	335	278	284
2	300	314	262	296	314	267	288	322	269	302
3	310	320	278	317	317	280	297	307	282	311
4	300	362	272	311	312	280	307	327	272	305
5	298	303	257	293	312	257	283	319	291	283
6	298	312	262	301	319	249	298	323	270	304
7	293	316	367	294	312	277	307	334	281	302
8	301	329	265	302	308	271	296	318	272	330
9	270	312	215	278	291	252	272	302	242	277
	<u>200</u>	<u>210</u>	<u>220</u>	<u>230</u>	<u>240</u>	<u>250</u>	<u>260</u>	<u>270</u>	<u>280</u>	<u>290</u>
0	270	224	245	270	226	255	234	220	260	279
1	320	253	306	320	254	288	302	241	284	311
2	317	265	304	336	271	294	235	270	299	326
3	322	271	317	342	280	310	320	273	318	330
4	308	282	319	340	274	298	314	273	309	333
5	308	272	291	308	241	376	298	272	287	318
6	292	268	318	323	271	275	318	264	288	327
7	309	259	313	338	273	318	324	276	281	333
8	326	259	305	336	282	302	321	251	305	310
9	286	252	259	304	255	275	297	248	263	264
	<u>300</u>	<u>310</u>	<u>320</u>							
0	226	245	266							
1	259	299	304							
2	270	282	319							
3	284	311	301							
4	266	299	293							
5	300	262	308							
6	278	302	315							
7	264	287	338							
8	258	303	338							
9	249	274	301							

Grid Nos. 0 to 329

Capacitance in PF

Max 338

Min 222

Or 280 ± 58

