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Description:

This is a Verilog-A ReRAM model that corresponds to the behavior of a physical TiO_2-based sample manufactured in **Nano Group, ECS, University of Southampton.**

The model is ready for use and can be compiled in any electronics circuit simulator that supports Verilog-A models.

Date and location of the collected data for the model implementation procedure:

February 2017; University of Southampton, U.K.

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