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**University of Southampton – ECS – Ilias Vougioukas – [iv1g15@soton.ac.uk](mailto:iv1g15@soton.ac.uk)**

**Reference of publication:** *Ilias Vougioukas, Andreas Sandberg, Stephan Diestelhorst, Bashir M. Al-Hashimi, and Geoff V. Merrett. 2017. Nucleus: Finding the sharing limit of heterogeneous cores. ACM Trans. Embedd. Comput. Syst. (TECS)*

**The excel file contains experimental data for the paper. In particular:**

**Fig. 1.** InO core performance degradation from migrations when having no sharing for mibench.

**Fig. 2.** InO core performance degradation from migrations when sharing caches for mibench.

**Fig. 3.** InO core performance degradation from migrations when sharing L2 cache and TLB for mibench.

**Fig. 4.** InO core performance degradation from migrations sharing all caches and the TLB for mibench.

**Fig. 5.** OoO core performance degradation from migrations when having no sharing for mibench.

**Fig. 6.** OoO core performance degradation from migrations when sharing caches for mibench.

**Fig. 7.** OoO core performance degradation from migrations when sharing L2 cache and TLB for mibench.

**Fig. 8.** OoO core performance degradation from migrations when sharing all caches and the TLB for mibench.

**Fig. 9.** InO average performance degradation for mibench across all migration periods.

**Fig. 10.** OoO average performance degradation for mibench across all migration periods.

**Fig. 11.** InO to OoO core beneficial migrations when considering a no trade-off policy.

**Fig. 12.** InO to OoO core beneficial migrations when considering a policy for better EDP.

**Fig. 13.** OoO to InO core beneficial migrations when considering a no trade-off policy.

**Fig. 14.** OoO to InO core beneficial migrations when considering a policy for better EDP.

**Date of data collection:** from June 2016 – July 2017

**Information about geographic location of data collection:** ARM Research, Cambridge, U.K.

**Date the file was created:** July 2017

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