Transversal Symmetry Breaking in Novel Photonic Crystal Waveguide: Innovative Manner to Master Defect Band Dispersion Relation

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Abstract
We present a novel Photonic Crystal Slab (PhC) fabricated in Silicon (Si) with pattern edge along <11-2> direction. Such a design enabled a dry and wet etching processes combination to atomically flatten the sidewall surface defined by Si[111]. Moreover, a new method to tailor the relation dispersion of line defect PhC waveguide (W1) using this structure is introduced. By mismatching the upper and lower part of the waveguide in the propagation direction, FDTD simulations demonstrate a net shift of the Cut-Off wavelength from 1605nm to 1742nm for a mismatch of zero and half of the lattice constant of the crystal a, respectively. We discuss both use of this new PhC for repeatable photonic device on-chip integration and the benefit of this new waveguide design method to engineer large bandwidth mode.

1. Introduction
Despite the fact that PhCs are promised to a broad range of applications, their availability is untapped by their fabrication repeatability. The challenge is to overcome underlying fluctuations in the design due to submicron lithography technology preventing their inclusive integration on chip. Here we demonstrate the fabrication of a PhC reducing surface roughness by the use of TetraMethylAmmonium Hydroxide (TMAH) anisotropic wet etching post-processing already developed for loss reduction in slot waveguide [1], and its potential in PhC waveguide mode dispersion engineering with an original method which consist to mismatch the upper and lower part of the PhC waveguide. The transversal mirror symmetry breaking is pointed out by a design parameter so-called the mismatch shifting them in the propagation direction. We extracted from the FDTD simulations, the relevant photonic property of the band defect mode, the normalized dispersion relation. The photonic waveguide characteristics display a clear confinement transition in the dispersion curve by pinning the band edge at a much lower energy at the end of the Brillouin Zone for the maximum mismatch of half of a period compared to the unmodified waveguide.

2. Atomically Flat Sidewall Surface PhC
We fabricated a triangular PhC slab with the pattern edge aligned along <11-2> direction (Fig. 1) on a Silicon-On-Insulator (SOI) substrate with 220 nm thick layer of Si grown along <110> direction on 2 μm thick buried oxide (BOX). The pattern is defined by an equilateral rhombus with a lower angle of α=70.53°, an intrinsic angular separation between [111] planes and the longer diagonal proportional to the lattice constant by a ratio r highlighted on the Scanning Electron Microscope (SEM) imaging performed after fabrication (Fig. 1). The fabrication process [1] started by annealing the SOI substrates in order to thermally grow a 20nm thick layer of SiO2 on top, then spin coating 260-nm thick ZEP-520A positive e-beam resist layer. The subsequent e-beam lithography was continued by a dry etching process in an Inductively Coupled Plasma (ICP) etcher to etch down the pattern into the top oxide and Si layer pursued by the removal of the remaining resist in an O2 plasma asher. After fuming nitric acid and diluted hydrofluoric acid treatments for cleaning purposes, the sample was immersed in 25% aqueous solution of TMAH for 10 minutes at room temperature. Taking into account the anisotropic wet etching and the Si exposure of only sidewall surface due to the SiO2 hard mask protection on top and the BOX underneath, this step principally results in a drastic reduction of the surface roughness induced by the very slow etch rate normal to the Si [111] plane. Thereupon, another longer

![Fig. 1 SEM image of the fabricated triangular PhC slab with highlighted parameters: a the lattice constant 405 nm, r=0.7, α the angle between [111] planes in red and the <11-2> directions displayed with red arrows.](image1)

![Fig. 2. Mismatched PhC Geometry The lower part (blue) is displaced with respect to the upper part (red) along the propagation axis.](image2)
diluted hydrofluoric acid treatment was applied to the sample to suspend the Si membrane by removing the BOX and the SiO2 top layer.

To form a bandgap around the telecommunication wavelength $\lambda=1550$ nm, by considering the slab thickness and the refractive index of Si in this range $n=3.477$, design parameters were calculated to be $a=405$ nm and $r=0.7$. The SEM image of the fabricated PhC lab (Fig. 1) shows a smooth boundary of the pattern indicating a vertical interface with very low surface roughness. However, the edge between [111] planes is rounded due to the establishment of metastable surfaces at the corner, providing the opportunity to study the robustness of this metastable surface to a longer anisotropic wet-etching process. Such a process markedly reduces the surface roughness. Therefore, increasing the lattice constant and the ratio of the PhC to be $a = 405$ nm and $r = 0.7$, respectively. Another important feature of the design is that the line defect width $W$ had been squeezed decreasing the upper and lower part separation to $0.85\sqrt{3}a$. Then, we focused our study on the initial band defect mode escaping from the air band. The relation dispersion dependences on the mismatch are summarized in Fig. 3. The mismatch effect on the mode is to dip the energy level at the end of the Brillouin zone transforming a constant dispersion curve of the mode around this range of wave-vector into a sloped band. This alteration reflects an unexpected transformation of the guidance mechanism. If the wave-vector independence of the waveguide mode reflects a bandgap guiding of the mode, the slope of the band is the signature of the index guiding. In other words, the mismatch between both sides of the waveguide progressively forces the mode to rely on the index confinement rather than the PhC gap effect. This new method can be also used to control photonic interaction between defect modes bands in the gap region by fine-tuning their physical guiding nature.

Indeed, in our study, this transition of behavior regarding the mode confinement is expressed by the lowering of the energy minimum, as it is shown on Fig. 3. Leading to a broader range of operability of the waveguide mode such as the upper cut-off wavelength has been shifted from 1605 nm to 1742 nm, for a mismatch of 0 nm and $a/2$, respectively.

4. Conclusions

In this paper, we presented a new design of PhC with atomically flat surface based on a pattern with edges aligned with Si[111] planes allowing a wet etching process acting to remarkably reduce the surface roughness. Therefore, increasing the accuracy and the reproducibility of the design, paving the way for on chip integration of high performant and reproducible Si PhC-based technology.

In addition, we discussed a unique way to engineer the line defect waveguide mode in PhCs to increase the frequency range operability by tailoring the band shape of the mode relying on a transition of the guiding mechanism and demonstrate the capability of our novel structure to demonstrate such surprising functions.

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