

Pi Stack PCB

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Description

The files contained within the archive are the gerber files for the production of the PiStack PCB, a 2 layer PCB. In order to view the gerber files, we suggest using [Gerbv](#) which is free/open source. The files contained within the archive are as follows. The boards are designed for manufacture by [seeed studio](#) using their Fusion assembly service.

Filename	Description
PiStack.dri	Drill station Info File
PiStack.GBL	Bottom Copper
PiStack.GBO	Bottom Silkscreen
PiStack.GBP	Bottom Paste Layer
PiStack.GBS	Bottom Solder Mask
PiStack.GML	Mechanical drawing
PiStack.gpi	Photoplotter Info File
PiStack.GTL	Top Copper
PiStack.GTO	Top Silkscreen
PiStack.GTP	Top Paste Layer
PiStack.GTS	Top Solder Mask
PiStack.TXT	Drill File (Excelon format)
top-placement.pdf	Part location identifiers for top of PCB
bottom-placement.pdf	Part location identifiers for bottom of PCB
bom.xlsx	Bill of Materials for PCB creation