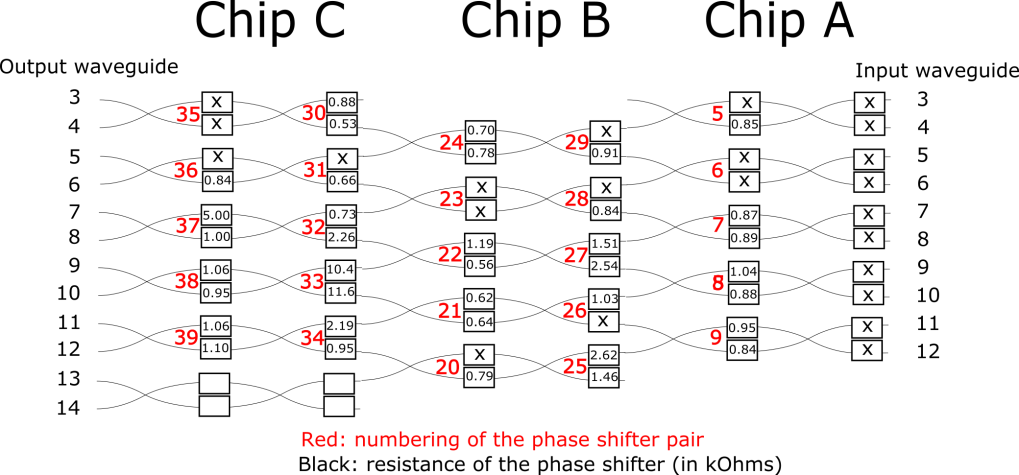
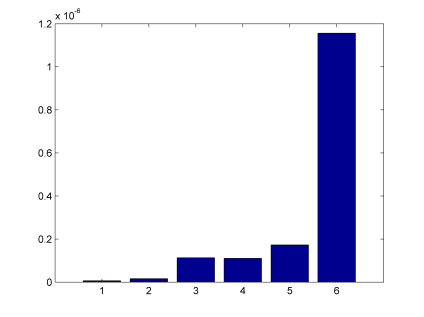
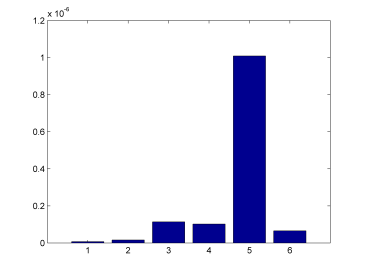
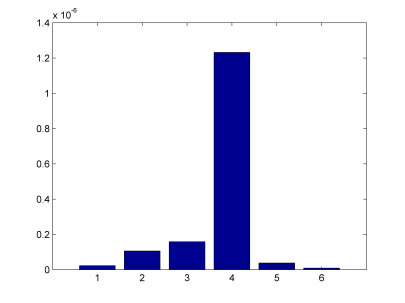
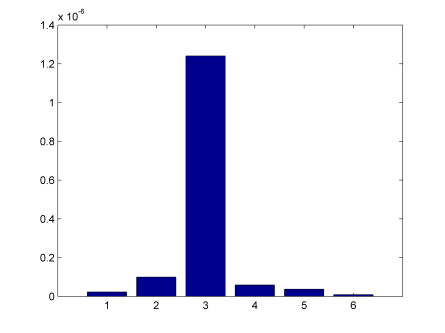
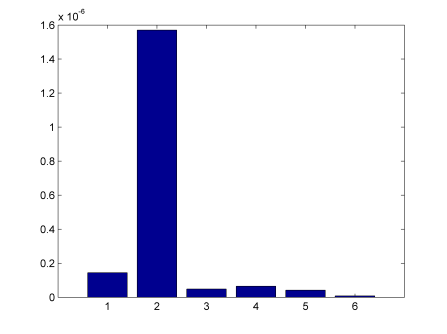
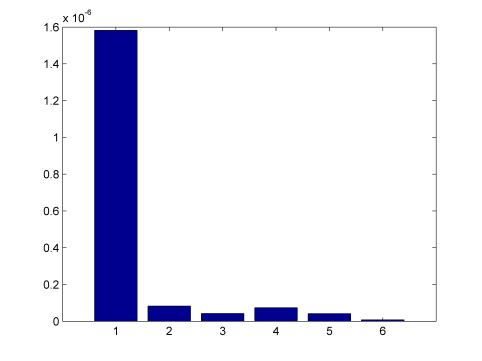
These files are intended to demonstrate the reconfigurability of the three chip assembly. For each of the 6 files, I sent light into input 10 and tried to maximize light going out of a specified output, for each of the 6 possible outputs (7-12). For reference, here is the sketch of the chips from 20170810\_resistancemap.png that is in the 20170809Data folder:



Each file contains 6 entries, corresponding to the measured power at each of the 6 possible outputs from 7 to 12. Here’s what the data looks like (see png files in this folder):



Note: for 10->9 and 10->10, I chose to route the light through MZI 22. Routing the light through MZI 21 would also have been possible.