

Silicon single-electron random number generator based on random telegraph signals at room temperature

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The need for hardware random number generators (HRNGs) that can be integrated in a Silicon (Si) complementary-metal-oxide-semiconductor (CMOS) platform has become increasingly important in the era of the Internet-of-Things (IoT). Si MOSFETs exhibiting random telegraph signals (RTSs) have been considered as such a candidate for HRNG, though its application has been hindered by RTS's variability and uncontrollable, unpredictable characteristics. In this paper, we report the generation and randomness evaluation of random numbers from RTSs in a Si single electron pump (SEP) device at room temperatures. SEP devices are known to consistently produce RTSs, due to a quantum dot electrically defined by multi-layer polycrystalline Si gates. Using RTSs observed in our devices, random numbers were extracted by a classifier supported by supervised learning, where part of data was used to train the classifier before it is applied to the rest to generate random numbers. The random numbers generated from RTSs were used as inputs for the Monte Carlo method to calculate the values of π , and the distribution was compared against the result obtained from Mersenne twister, a representative pseudo random number generator (PRNG), under the same condition. π was estimated more than 80000 times, and the distribution of the estimated values has a central value at 3.14 with a variance of 0.273, which is only twice larger than the result from PRNG. Our result paves a way to fully electronic CMOS compatible HRNG that can be integrated in a modern System-on-a-Chip in IoT devices.

I. INTRODUCTION

Password authentication is one of the most widely used protocols to protect one's private information^{1–3}. This protocol accepts input from a user in a form of a password, and from this password a function called 'hashing'^{4,5} (md5()⁶, for example) generates a 128bit output, which is stored in the server. From this generated sequence of characters, called hash, it is virtually impossible to guess the original password. When the server is attacked, the hackers will obtain hashes, but not original passwords. Therefore, the security of the user is still protected. However, it is critically important to use a password which is new, long enough and random enough to avoid a so-called 'dictionary attack'^{1–3}, where a hacker tries to guess the password from common candidates, such as those on personal details (for example, birthday, surname and forename), a password used in elsewhere and simple sequences of numbers and letters (1234567890, abc), that matches the obtained hash. To generate a password with enough strength, pseudo random number generators (PRNGs) are used to suggest strong passwords. PRNGs use a date and time, for example, as a seed, and an algorithm produces random numbers as a password.

Another way of generating a strong password is to use a hardware random number generator (HRNG)^{7–10}. HRNG uses the stochastic nature of certain physical phenomena or physical values, such as thermal noise of electronic devices⁷,

detection of single photons⁸, timing of Ovonic threshold switching⁹, chaotic behaviour of semiconductor lasers¹⁰ and so forth. As HRNG does not rely on algorithm, the security of a password that is generated by HRNG is fundamentally superior to the one generated by PRNG.

Recently, theoretical¹¹ and experimental^{12–14} studies have sought the possibility of utilising random telegraph signals (RTSs) for random number generation. RTSs are a stochastic shift of threshold voltage over time¹⁵, and were first found in 1985 in silicon (Si) metal-oxide-semiconductor field effect transistors (MOSFETs)¹⁶. The shift in V_{th} has been considered to be caused by trapping and de-trapping of a single carrier (an electron in a case of n-type MOSFET (n-MOSFET), and a hole in a case of a p-type MOSFET (p-MOSFET)) into a charge trap in a gate-oxide¹⁵. As a consequence of aggressive scaling of Si technology, RTSs started to become recognised as a reliability issue in complementary MOS (CMOS) technology in the 2010s^{17–23}. The merit of using RTSs as a source of randomness is primarily because of the compatibility of CMOS technology¹⁵. Also, it is well known that in many cases the probability to observe two discrete current states (high and low current states) can be well controlled by gate voltage^{15,24–27}, such that one can identify a bias condition where two current states are equally observed ('balance point')⁹. The difficulty in utilising RTSs as HRNG is that the presence and absence of RTSs cannot be controlled, and usually the probability to find a device that exhibit RTSs is less than 1%^{15,28}.

In this paper, we report the generation and evaluation of random numbers from RTSs in a Si single electron pump (SEP) device at room temperature. Our device is based on silicon-on-insulator (SOI) Fin-FET structure with multiple

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poly-crystalline Si gates, and the formation of an electrically defined quantum dot (QD) is achieved when negative bias is applied to the two gates^{25,26}. Our previous study reveals that RTSs can be caused by trapping and de-trapping of an electron in the QD, whose charge state is read by the net current flowing through the device^{25,26}. We selected a different device this time, and RTSs were observed again, showing the expected characteristics such as average lifetime of about 1s and tunability of the charge state by the top gate. Moreover, we observed anomalous three-level RTSs, which can be explained by the presence of multiple energy levels in a QD²⁵. The advantage of this mechanism is that we do not need to rely on fluctuant device features like oxide traps^{13,14} or incident potential pocket¹², whose presence can only be determined probabilistically. With the future possibility of real-time random number generation in sight, we considered the generation of random numbers from RTSs as a classification problem of time-series data²⁹. That is, the classifier was built from training data before it was applied to a new data to achieve random number generation. Finally, the quality of random numbers was evaluated by calculating values of π using Monte Carlo method with the generated random numbers as input. π was calculated more than 80000 times, whose distribution has a median at 3.14 with a deviation of 0.273. This variance was only twice larger than the distribution obtained from Mersenne twister (MT), a representative pseudo random number generator (PRNG), calculated under the same conditions. The reason for the wider variance was attributed to the misclassification in the test data.

II. DEVICE FABRICATION

Our device was fabricated on a 6-inch SOI wafer with the surface orientation being (100). The stack of the wafer was 100nm-thick SOI and 145nm-thick buried oxide (BOX) on a handle layer. After the thickness of the SOI was thinned down to 24nm, the nanowire and mesa for source and drain (S/D) were patterned by electron beam lithography (e-beam). This was followed by anisotropic wet etching using tetramethylammonium hydroxide (TMAH) as wet etchant. This resulted in the sidewall of the nanowire being atomically flat (111) crystalline surface^{30–32}. The nanowire width of e-beam mask was designed to be 100nm. After the oxidation and wet etching, the width of the nanowire was further reduced to about 60nm. 17.6nm-thick silicon dioxide (SiO_2) for gate dielectric was grown on SOI at 1000°C, followed by its partial removal for S/D contact. Then, polycrystalline Si (poly-Si) was deposited across the entire wafer, and phosphorous was heavily doped using spin-on-dopant (SOD) technique aiming for $5 \times 10^{19} \text{cm}^{-3}$. The dopants were activated by rapid-thermal-annealing (RTA) at 950°C for one minute. This layer was patterned by e-beam and inductively-coupled-plasma (ICP) etching for raised S/D as well as gates for electrically defining a QD in the channel, called left-gate (LG) and right-gate (RG). The length of L/RG ($L_{\text{L/RG}}$) was 100nm. In order to allow the formation of an inversion layers in the regions not covered by the L/RG, another layer of poly-Si was deposited and

patterned using the same technique used for L/RG. This gate is called top-gate (TG). TG and L/RG were electrically insulated by 9-nm thick thermal oxide. The length of the TG (L_{TG}) is defined as the separation between LG and RG, which is 150nm. Figure 1 (a) shows a schematic of cross-section of our device along the nanowire. Formation of the passivation oxide was followed by contact opening, aluminium deposition and its patterning for metal interconnect. Finally hydrogen termination was achieved by forming gas anneal at 450°C for 30 minutes²⁵.

Figure 1 (b) shows a scanning-electron-microscope (SEM) image of a similar device before the deposition of poly-Si for TG, in which the position of a QD is highlighted. Figure 1 (c) shows the potential profile along the nanowire when LG and RG were kept around V_{th} while TG inverted the entire channel. RTSs were consistently observed in devices from the same fabrication lot^{25,26}, and the physical mechanism of the RTSs were attributed to a trapping and detrapping of a single electron in the electrically defined QD. This kind of discretised signals were observed at room temperatures in devices fabricated using the similar technique, while the trapping and de-trapping of a single electron was detected by a nearby charge sensor^{33,34}. Therefore, the observation of RTSs in this particular device was also expected.

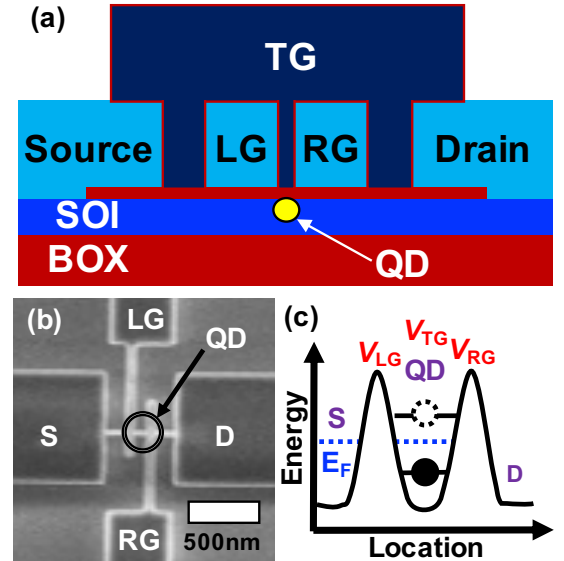


FIG. 1. Overview of the fabricated device. (a) A schematic of cross-section of device along the nanowire. (b) Scanning electron micrograph of the device. (c) Schematic of the potential profile realised in the device at room temperature when V_{LG} and V_{RG} are creating potential barriers in the channel.

III. CHARACTERISATION

A Keysight B1500 and Cascade M150 probe station were used for electrical characterisation. The detection limit of this experimental setup is 100fA. Figure 2 (a), (b) and (c) show the

transfer characteristics of the device when TG, LG and RG were swept, respectively, at two different drain voltage (V_d) values, 50mV and 1V. Leakage current was absent between the channel and the gates as well as between the gates. The device exhibited acceptable behaviour as a MOSFET, with the subthreshold swing of the $I_d - V_{TG}$ curve being 77mV/decade. The degradation in subthreshold swing in $I_d - V_{RG}$ and $I_d - V_{LG}$ curves was attributed to the bias condition of the measurement, where voltage on TG (V_{TG}) was set to 0V. Threshold voltage of TG (V_{th}) was 0V, and above V_{th} , current fluctuations were observed in the transfer characteristics. Similar current fluctuations were observed in a device from the same fabrication process, which has shorter gate lengths as well as narrower channel width^{25,26}. This prompted to perform time domain measurements to identify the nature of the current instability in the transfer characteristics.

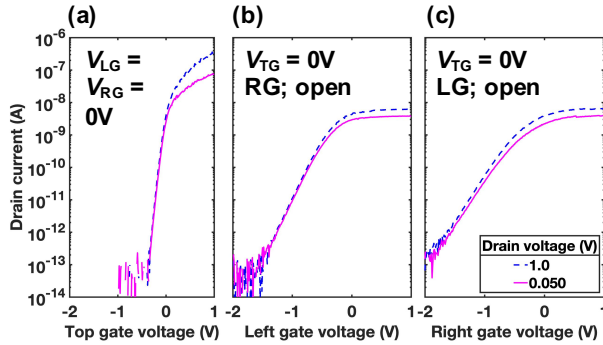


FIG. 2. Transfer characteristics of the device. (a) $I_d - V_{TG}$ curves with $V_d = 50\text{mV}$ and 1V. V_{LG} and V_{RG} were 0V. (b) $I_d - V_{RG}$ curve with $V_d = 50\text{mV}$ and 1V. V_{TG} was 0V and V_{LG} was floating. (c) $I_d - V_{LG}$ curve with $V_d = 50\text{mV}$ and 1V. V_{TG} was 0V and V_{RG} was floating.

Figure 3 (a) shows one example of the results of the time domain measurements, and (b) shows the histogram of the corresponding time trace, which is a probability distribution against the value of drain current (I_d). V_{TG} was set to be 0.2V, and V_{LG} and V_{RG} were -50mV in this case. The average lifetimes of $|0\rangle$, $|1\rangle$ were about 1s, while the lifetime of $|2\rangle$ was shorter than the other two current states. In Figure 3 (b), two major peaks and one minor peak can be seen. From this distribution, the probability to observe the highest current state ($|0\rangle$) and the middle state ($|1\rangle$) is almost the same, while the probability to observe the lowest current state ($|2\rangle$) is significantly lower than $|0\rangle$ and $|1\rangle$. Figure 3 (c) shows the histograms at different voltage conditions, where $V_{LG} = V_{RG}$ were varied from -200mV (blue dash line) to 0mV (green solid line) with 50mV increments. When $V_{LG} = V_{RG} = -200\text{mV}$, the dominant current state was $|0\rangle$. As $V_{LG} = V_{RG}$ increased, $|1\rangle$ started to observe more frequently, and when $V_{LG} = V_{RG} = 0\text{mV}$, the dominant current state was $|1\rangle$. Figure 3 (d) shows the occupancy of the current states as a function of $V_{LG} = V_{RG}$. The balance point was achieved at around $V_{LG} = V_{RG} = -50\text{mV}$. Also, as $V_{LG} = V_{RG}$ approaches to 0mV, the probability to observe $|2\rangle$ became negligible.

In this device, three current states were observed, which cannot be captured by a simple physical picture describing

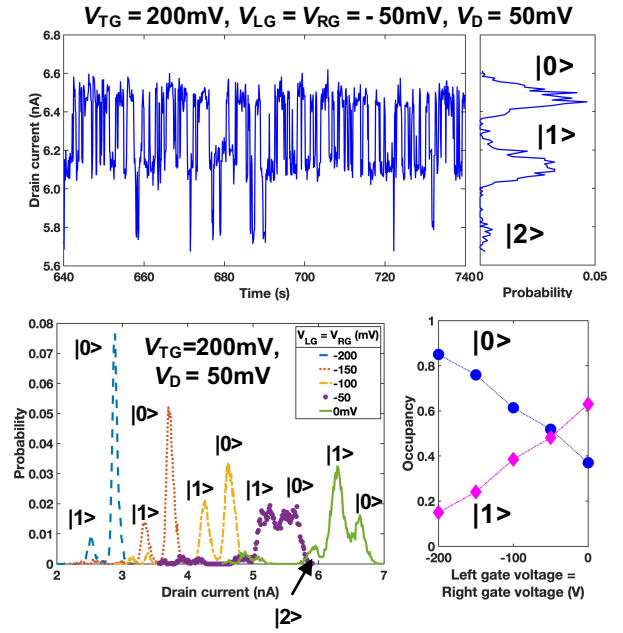


FIG. 3. Random telegraph signals observed in the device. (a) Part of a time domain measurement of the device with $V_{TG} = 200\text{mV}$, $V_{LG} = V_{RG} = -50\text{mV}$, $V_d = 50\text{mV}$. (b) Histogram of the corresponding time domain characteristics shown in (a). (c) Histograms of the several time domain characteristics with different $V_{LG} = V_{RG}$ values. (d) Occupancy of the current states as a function of $V_{LG} = V_{RG}$.

RTSs. In the most simple case of RTSs with two current states, this can be explained by a presence of a single isolated energy level with two different charge states, occupied or empty, corresponding to the two current levels¹⁵. If there are n independent energy levels were present, the number of current levels should be 2^n . In order to explain the existence of three current levels, a following physical model was constructed, which is shown as schematics in Figure 4.

Four diagrams that represent potential diagrams from source to drain are displayed in Figure 4. In this model, we assume the existence of two discrete energy levels in the QD. The separation between the energy levels are expected to be in the order of 100mV from our previous result²⁵, justifying the observation of single electron phenomena at room temperature. When the energy barriers created by LG and RG were high ($V_{LG} = V_{RG} = -200\text{mV}$, for example), both energy levels are above Fermi energy of source (E_F) and the ground state is occasionally occupied by an electron and it would be released soon after (Figure 4 (a)). This occasional occupation of the level in the QD and following release caused corresponding positive shift in V_{th} followed by negative V_{th} shifts at random. As the probability for the level to be occupied is still low, the probability distribution is highly asymmetric in favor of $|0\rangle$. When the barrier is lowered and the ground state aligned with E_F , the probability to observe $|0\rangle$ and $|1\rangle$ is almost equal (Figure 4 (b)). Finally, as the barriers were further lowered, both levels can interact with the electron reservoir. This means that the ground state can still capture and release an electron (Fig-

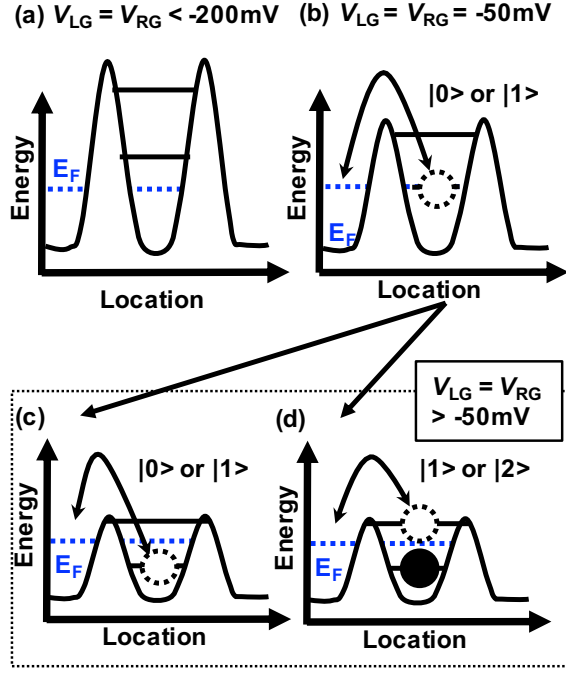


FIG. 4. Schematics of the physical model to explain the RTS shown in Figure 3. (a) The energy levels in the QD are above E_F when $V_{LG} = V_{RG}$ are less than -200mV . (b) As the potential barriers become low ($V_{LG} = V_{RG} = -150\text{mV}$ and -100mV), the ground state starts to be resonant with E_F . (c) The further decrease in potential barriers ($V_{LG} = V_{RG} = -50\text{mV}$ and 0mV) brings the second energy level in resonant with E_F , while electron can be trapped and de-trapped in the ground state. (d) However, when an electron occupies the QD, another electron can occupy the excited state. The occupation of the excited state is assumed to be only allowed when the ground state is occupied.

ure 4 (c)), and the excited states can also trap and de-trap an electron (Figure 4 (d)). However, the trapping and de-trapping of an electron in an excited state can be considered to be allowed only when the ground state is occupied. Here, we exclude a possibility where the ground state is empty and the excited state is occupied, which is a reasonable assumption based on this multi-level model in a single QD. Indeed, in the time domain measurement shown in Figure 3 (a), the transition to the current state $|2\rangle$ seems mostly occurred from $|1\rangle$. Overall, this physical model explains the characteristics of the observed RTSs reasonably well.

IV. THE PROCEDURE TO EXTRACT CHARACTERISTIC PARAMETERS OF RTS FOR RANDOM NUMBER GENERATION

Following on from discussing the physical model behind the RTS observed in our device, in the next two sections the procedure of random number generation from the RTS is introduced. Data for random number generation was prepared as follows. Nine time domain measurements were taken (called Data1,2,3...9) at the fixed bias condition of $V_{TG} =$

300mV and $V_{LG} = V_{RG} = -106\text{mV}$, which still realised the potential profile depicted in Figure 4 (b). V_{TG} was increased from 200mV (as shown in Figure 3) to 300mV , in order to increase the amplitude of RTS (difference in current value between high and low current states). The probability of observing high and low current states were almost equal at $V_{LG} = V_{RG} = -106\text{mV}$, determining the bias condition of the time domain measurements. RTSs were observed with similar average lifetime as the one shown in Figure 3 but with higher average current (about 9nA), which were used to generate random numbers (part of data shown in Figure 7 (a)). The sampling interval of each measurement (t_{int}) was set to be 24ms , which is shorter than the average RTS lifetimes. 100001 points were taken, resulting in the total measurement time of 2400s in a single time domain measurement. After one time domain measurement was completed, 20s of hold time, sufficiently longer than the average lifetime of RTSs, was provided such that the auto-correlation between two adjacent measurements were safely removed.

In order to generate a sequence of random numbers from RTS, high current state ($|0\rangle$) was attributed to a digital number 1, while the low current state ($|1\rangle$ and $|2\rangle$) was designated to be a digital number 0. This can be considered as a classification problem of time-series data²⁹, and to achieve this classification, a certain mathematical model (classifier) needed to be established. Several methods can be proposed. The most simple method is to create a model every time after a measurement was completed (Figure 5 (a)). That is, after a single time domain measurement was completed, a histogram of I_d was created, and the analog-to-digital conversion (ADC) was performed based on the threshold value, determined from the histogram, that segregates one current state to the other. The merit of this procedure is that the ADC from RTS to random numbers can be achieved with high accuracy, as the best classifier is produced for a given time domain measurement. However, the problem of this method is that real-time random number generation is not possible, as the random number is returned as a result of the post processing.

In order to overcome this issue, initialisation of a classifier is necessary. That is, by using several time domain measurements at the same bias condition as ‘training data’, a classifier (for example, allocate 1 if I_d exceeds a certain value determined from training data, else 0, and the sampling time twice longer than average lifetime of RTS in training data) was created, before the model was applied to a new RTS (‘test data’) to generate a sequence of random numbers (Figure 5 (b)). Dividing dataset into training and test data, building a mathematical model before applying it to the test data is a typical method used in the context to supervised learning. The clear advantage of this method over the aforementioned one is that the real time random number generation is possible. That is, after the classifier is established, a sequence of random numbers can be generated by applying the classifier to the measurement as the instrument records I_d over time. A disadvantage of this method is that as the classifier is optimised to the training data and not the test data, there may be a misclassification of current states. This misclassification is unavoidable in classification problems. However, the accuracy can be im-

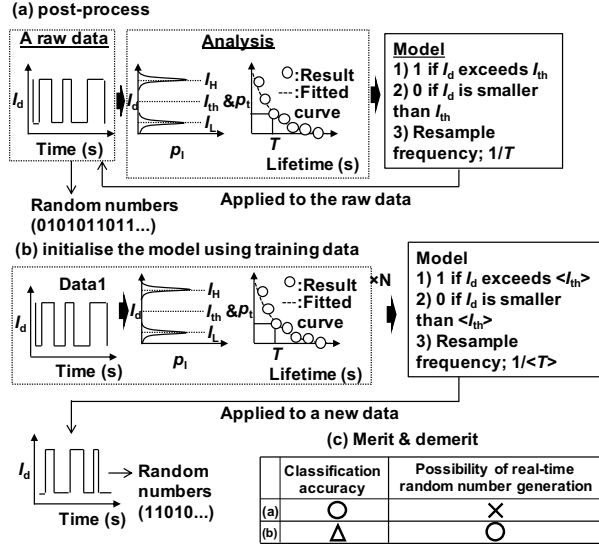


FIG. 5. Schematics for describing procedures to generate random number from random telegraph signals. (a) shows the protocol when random numbers are generated from a single measurement of random telegraph signal (post-processing). (b) shows the protocol when the classification model is generated from several measurements at the same bias condition before it is applied to a new result to generate random numbers (initialise the model using training data). (c) summarises the advantage and disadvantage of the two procedures described above.

proved by refining the model (taking into account a drift of average current due to bias temperature instability^{35–38}, for instance). The merit and demerit of both methods were summarised as a table in Figure 5 (c).

In the following, the procedure to generate a classifier is explained based on our data, whose flowchart is schematically shown in Figure 6. While the method itself can be used for real-time random number generation, in this paper real-time random number generation was emulated by using test data, rather than obtain more time domain measurements. Firstly, seven time domain measurements out of nine results (Data1,2...9) were used as training data (called trainingData1,2...7), and the other two measurements were reserved for emulating random number generation (called testData1,2). The selection of training and test data was not necessarily in the order of time, as all measurements were assumed to be independent due to the hold time of 20s. This means that there were $9C_2 = 36$ combinations of training and test data, that can be used to evaluate randomness of the generated random numbers in section VI. For simplicity, one particular combination was focused to explain the procedure in detail. Out of trainingData1,2...7, one result was selected (trainingData1, for example) and four parameters that characterise the time domain measurements were extracted, which are I_{th} , I_{max} , I_{min} and T . The first three parameters were used for ADC, while T determines the re-sampling frequency of test data. The definition of these parameters are explained in detail in the next two paragraphs. Extraction of these parameters was achieved by creating two histograms, which are

histogram of current values (calculated by a function called ‘histcount1’) and histogram of individual lifetimes (calculated by a function called ‘histcount2’).

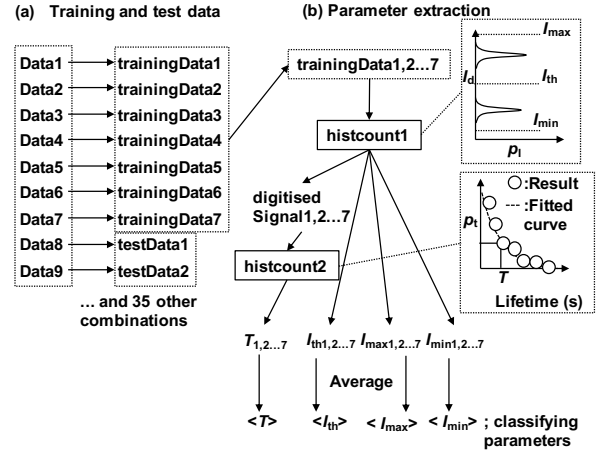


FIG. 6. Schematics to show how the training of the model was performed. (a) shows the division of the nine results (Data1,2...9) into training data (trainingData1,2...7) and test data (testData1 and 2). (b) shows the flowchart of parameter extraction from trainingData1,2...7. A function ‘histcount1’ generates a histogram of I_d value over time, outputting I_{th} , I_{max} , I_{min} and digitised signal (‘digitisedSignal’). The digitised signal was further analysed by a function ‘histcount2’, which generates a histogram of individual lifetimes, which resulted in the extraction of T . This process was iterated for all trainingData1,2...7 and average was taken, resulting in the final outputs $\langle I_{th} \rangle$, $\langle I_{max} \rangle$, $\langle I_{min} \rangle$ and $\langle T \rangle$

‘histcount1’ was defined to accept one time domain measurement (trainingData1, for example) as an input and firstly calculate a probability distribution to observe a certain current value of I_d over the measurement time. Due to the presence of RTS, two major peaks were observed in the histogram (Fig 3 (b)). These two peaks were then fitted by a double Gaussian function

$$f(I_d) = a_1 \exp[-\{(I_d - b_1)/c_1\}^2] + a_2 \exp[-\{(I_d - b_2)/c_2\}^2], \quad (1)$$

where a_1, b_1, c_1, a_2, b_2 and c_2 are all fitting parameters. In particular, b_1 and b_2 are the medians of the two Gaussian functions fitted to the probability distribution of current values, representing the average value for high and low current state. b_1, c_1, b_2 and c_2 have the dimension of Ampere, while a_1 and a_2 are dimensionless. Then, b_1 and b_2 were used to determine the threshold current value to classify the current state (called I_{th}), and maximum and minimum current values accepted in the given time domain measurement (called I_{max} and I_{min} , respectively) to remove outliers. I_{th} was simply defined as an average of b_1 and b_2

$$I_{th} = (b_1 + b_2)/2, \quad (2)$$

while I_{max} and I_{min} were defined as

$$I_{max} = I_{th} + 2|b_1 - b_2| \quad (3)$$

$$I_{min} = I_{th} - 2|b_1 - b_2|, \quad (4)$$

respectively. This means that when I_d exceeded I_{\max} , the digital number would not be allocated and NaN was returned instead of 1. Similarly, when I_d was lower than I_{\min} , NaN was given instead of 0. When I_d is bigger than I_{th} and smaller than I_{\max} , a digital number of 1 was designated, while when I_d is smaller than I_{th} and larger than I_{\min} , a digital number of 0 would be given. This digitisation was performed to obtain a sequence of digital numbers, called ‘digitisedSignal’, which constituted, together with I_{th} , I_{\max} and I_{\min} , the output of function ‘histcount1’. Figure 7 (a) shows part of Data8, I_{th} , I_{\max} , I_{\min} calculated from ‘histcount1’ with Data8 as an input, and ‘digitisedSignal’ plotted over the raw data. When an outlier was observed, like the points around $t=1910$ s, NaN was given instead of 0. This outlier might have been possibly caused by another RTS with lower probability to be observed compared to the one that has been discussed so far¹⁵.

‘histcount2’ was designed to accept ‘digitisedSignal’ as an input and firstly calculate a probability distribution of individual lifetime of one current state (high current state, for example) of a given training data (Data8, for example)¹⁵. As the capture and emission process of a single electron in a QD is considered to follow Poisson process, the distribution was expected to be fitted by an exponential function¹⁵. Figure 7 (b) shows a histogram of individual lifetime of high current state in Data8 as a training data, and indeed the probability to observe the same current state became exponentially smaller. The histogram was fitted by a simple exponential function

$$P_{\text{High}}(t) = \frac{1}{\langle \tau_{\text{High}} \rangle} \exp \left\{ -\frac{t}{\langle \tau_{\text{High}} \rangle} \right\}, \quad (5)$$

where $\langle \tau_{\text{High}} \rangle$ is a fitting parameter, whose physical meaning can be understood as follows; after $\langle \tau_{\text{High}} \rangle$, the probability for high current state not making a transit to low current state is $1/e \sim 37\%$ ¹⁵. In this sense, $\langle \tau_{\text{High}} \rangle$ represents the average lifetime of high current state in a given time domain measurement. Also, this value can be understood as a measure to evaluate the autocorrelation of the RTS signal. That is, if RTS was monitored longer than $\langle \tau_{\text{High}} \rangle$, the probability for I_d not making a transit from high current state to low current state can be considered as sufficiently small. The same histogram was calculated for low current state, and $\langle \tau_{\text{Low}} \rangle$, average lifetime of low current state, were obtained;

$$P_{\text{Low}}(t) = \frac{1}{\langle \tau_{\text{Low}} \rangle} \exp \left\{ -\frac{t}{\langle \tau_{\text{Low}} \rangle} \right\}. \quad (6)$$

$\langle \tau_{\text{High}} \rangle$ and $\langle \tau_{\text{Low}} \rangle$ were almost identical as the probability to observe high and low current states were nearly equal (see Figure 7 (a) and 10 (c)). However, it is virtually impossible to balance the probabilities to be exactly 50% with infinite precision, due to the limited resolution of voltage source ($1\mu\text{V}$). This means that either $\langle \tau_{\text{High}} \rangle$ or $\langle \tau_{\text{Low}} \rangle$ would be slightly longer than the other. For consistency, the longer one was defined as a typical lifetime of RTS;

$$\langle \tau \rangle = \max \{ \langle \tau_{\text{High}} \rangle, \langle \tau_{\text{Low}} \rangle \} \quad (7)$$

The time-scale parameter extracted from histcount2 would be used for re-sampling of test data for RNG, explained in the

next section V. For this purpose, the re-sampling interval should be sufficiently longer than a typical lifetime of RTS in order to reduce autocorrelation. Therefore, $\langle \tau \rangle$ was multiplied twice such that the probability for one current state did not make a transit to the other current state was less than $1/e^2 \sim 14\%$;

$$T = 2\langle \tau \rangle, \quad (8)$$

which is an output of ‘histcount2’.

After the four parameters were extracted from training-Data1 by ‘histcount1’ and ‘histcount2’, the same procedure was iterated for the remaining six time domain measurements (trainingData2,3...7). This resulted in seven sets of four parameters, and finally the average of each parameter was calculated, resulting in $\langle I_{\text{th}} \rangle$, $\langle I_{\max} \rangle$, $\langle I_{\min} \rangle$ and $\langle T \rangle$;

$$\langle I_{\text{th}} \rangle = \frac{1}{k} \sum_{i=1}^k I_{\text{th},i}, \quad (9)$$

$$\langle I_{\max} \rangle = \frac{1}{k} \sum_{i=1}^k I_{\max,i}, \quad (10)$$

$$\langle I_{\min} \rangle = \frac{1}{k} \sum_{i=1}^k I_{\min,i}, \quad (11)$$

$$\langle T \rangle = \frac{1}{k} \sum_{i=1}^k T_i, \quad (12)$$

where k is the number of training data (7 in this case), $I_{\text{th},i}$, $I_{\max,i}$, $I_{\min,i}$ and T_i are I_{th} , I_{\max} , I_{\min} and T of trainingData i , respectively. Note that ‘ $\langle T \rangle/2$ ’ is average of typical lifetimes of RTSs in trainingData1,2...7. These four parameters underpin the classifier for extracting random numbers from RTS.

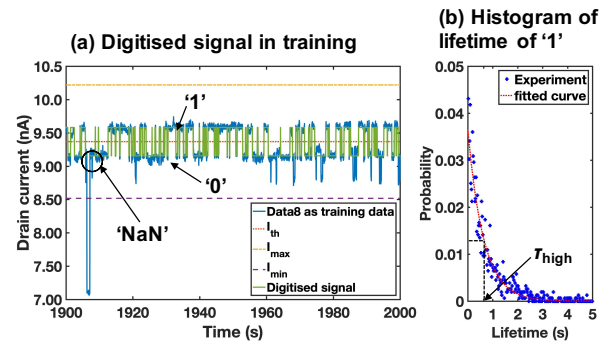


FIG. 7. Examples of the output of functions ‘histcount1’ and ‘histcount2’. (a) shows an example of output of histcount1 when Data8 was used as an input (training data). In (a), part of measurement result (Data8), I_{th} , I_{\max} , I_{\min} determined from Data8, and the digitised signal generated from Data 8 by the classification parameters (I_{th} , I_{\max} , I_{\min}) were shown. When outliers were observed, histcount1 returned NaN, as shown in the inset. (b) shows a histogram of individual lifetimes of high current state in Data8, generated by histcount2. Blue filled circles are the experimental results, while the red dotted line is the exponential function fitted to the results, allowing one to extract the average lifetime of high current states τ_{high} .

V. RANDOM NUMBER GENERATED FROM RANDOM TELEGRAPH SIGNALS OBSERVED IN THE SINGLE ELECTRON PUMP DEVICE

Using the classifier determined from the training data via histcounts1 and histcounts2, finally a sequence of random numbers was extracted from test data. A schematic of this procedure is shown in Figure 8. The extraction procedure is 1) allocate a digital number '1' if $\langle I_{\max} \rangle > I_d > \langle I_{\text{th}} \rangle$ was satisfied 2) allocate '0' if $\langle I_{\text{th}} \rangle > I_d > \langle I_{\min} \rangle$ was satisfied 3) allocate NaN if I_d exceeded $\langle I_{\max} \rangle$ or I_d was smaller than $\langle I_{\min} \rangle$. 4) re-sample 'digitisedSignal' with an interval of

$$t_{\text{re}} = \lfloor \langle T \rangle / t_{\text{int}} \rfloor t_{\text{int}}, \quad (13)$$

where t_{int} is a measurement interval (24ms), and $\lfloor \cdot \rfloor$ is a floor function, fixing the value of a real number $\langle T \rangle / t_{\text{int}}$ to the closest integer equal to or less than $\langle T \rangle / t_{\text{int}}$. The re-sampling can only be achieved with an interval of $\langle T \rangle$ when $\langle T \rangle$ happened to be an integer multiple of t_{int} . Since $\langle T \rangle$ is not necessarily an integer multiple of t_{int} , the floor function was practically necessary. The purpose of the re-sampling step was to remove autocorrelation between one digital number extracted from one point to the other¹⁴. When RTS is sampled with interval sufficiently shorter than the average lifetime of RTS, it is likely to observe the same current state at the next measurement point, which is obvious from the definition of lifetime of one current state. In order to extract a sequence of random numbers from RTS, it is required to be sampled with interval sufficiently longer than the average lifetime such that a digital number at one point is unpredictable from the previous digital number. Each T calculated from training data was defined to be twice of the typical lifetime of RTS, and therefore, on average, $\lfloor \langle T \rangle / t_{\text{int}} \rfloor t_{\text{int}}$ can be considered to be sufficiently longer than the lifetime of testData.

Note that in this paper the real-time random number generation is emulated and not actually performed, and the test data has the same number of measurement points as the training data. Depending on the initial point of re-sampling was taken, there were $\lfloor \langle T \rangle / t_{\text{int}} \rfloor$ ways of extracting a sequence of binary numbers from a test data (a circle, triangle and cross mark in Figure 8). This is because the selection of the initial point was purely arbitrary; as the test data is considered to be independent of the training data, the measurement could have started at time $t_0 = t_{\text{int}}$, $t_0 = 2t_{\text{int}}$, ... $t_0 = (\lfloor \langle T \rangle / t_{\text{int}} \rfloor - 1)t_{\text{int}}$. This means that sequences of random numbers starting at $t_0 = t_{\text{int}}$, $t_0 = 2t_{\text{int}}$, ... $t_0 = (\lfloor \langle T \rangle / t_{\text{int}} \rfloor - 1)t_{\text{int}}$ were all equally likely and should not be discarded. Therefore, all sequences were saved for further analysis. The number of sequences depended on which data (Data1,2...9) was used for test data, particularly the number of NaN in the digitised signal of the test data. In our case, $\lfloor \langle T \rangle / t_{\text{int}} \rfloor \sim 55$ sequences could be extracted from one time domain measurement. Finally, eight adjacent binary numbers were combined to form one decimal number (from 0 to 255), resulting in $\lfloor \langle T \rangle / t_{\text{int}} \rfloor$ sequences of 8-bit decimal random numbers, which was saved as a matrix called RN1 (one row of RN1 corresponds to a sequence of random numbers). To implement the real-time random number generation, time domain measurement should be taken with an interval

of $\lfloor \langle T \rangle / t_{\text{int}} \rfloor t_{\text{int}}$ in the first place, before the classifier was applied for ADC. This procedure was repeated for testData2, and a matrix of random numbers with the same size as the one generated from testData1 was obtained, called RN2.

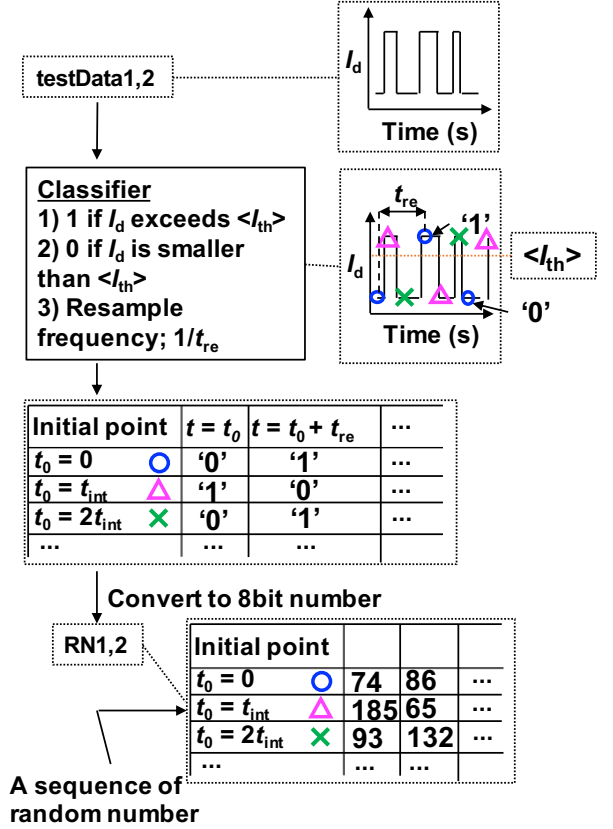


FIG. 8. A flow chart of extracting sequences of random number from testData1 and 2. The accurate description of the classifier is given in section V. testData1 and 2 were digitised by the classifier and also reorganised in a matrix depending on the initial sampling points. Binary random numbers were finally converted to 8bit decimal numbers, and two matrices called RN1 and RN2 were produced from testData1 and 2.

VI. EVALUATION OF RANDOMNESS GENERATED FROM RANDOM TELEGRAPH SIGNALS

In the section IV and V, the algorithm to generate random numbers from RTSs was introduced, and two matrices containing the sequences of random numbers (RN1 and RN2) were generated as an output. In this section, the randomness of the random numbers is evaluated by estimating the value of π using Monte Carlo method. This estimation of π is generally achieved as follows. Firstly, two sequences of N random numbers distributed in $[0,1]$ were prepared using any random number generator. These two sequences of random number can be used as a coordinate to designate a point in the x - y plane ($0 \leq x \leq 1, 0 \leq y \leq 1$). Then, a number of points that satisfy the condition $x^2 + y^2 < 1$ were counted, which is an

estimate of $\pi/4$. Because a random number generator should generate a different sequence of random numbers every time it runs, this estimate of π will also be different depending on the random number used as inputs. The important point is the distribution of a large number of estimated values of π , and the quality of random number can be considered as good when the distribution has a median around the true value (3.14...) with smaller deviation from the median. The accuracy of the median is determined by the randomness of the random number, while the precision of the estimation, gauged by the deviation from the median, is limited by the number of elements in a sequence (number of points in the x-y plane). This means that if a longer sequence was used as an input, it is more likely that the distribution of estimated values would be smaller.

Random numbers generated from RTS was then used as inputs of this procedure to calculate π (Figure 9 (a)). One sequence of random numbers was selected from both RN1 and RN2, and these random numbers were normalised such that all values would be fit in $[0,1]$, called rn1 and rn2. rn1 and rn2 were then used as a coordinate of a point in the x-y plane (rn1 for x, rn2 for y, for example), creating a scatter plot in the area $0 \leq x \leq 1, 0 \leq y \leq 1$. Finally π was estimated by counting number of points in $x^2 + y^2 < 1$. This estimation was repeated for all the combinations that can be taken from RN1 and RN2, which means that π can be calculated $\lfloor \langle T \rangle / t_{\text{int}} \rfloor^2$ times from RN1 and RN2. Figure 9 (b) shows one of the examples of a scatter plot generated from random numbers originating from Data3 and Data8. The estimated value of π from this particular distribution resulted in 3.15.

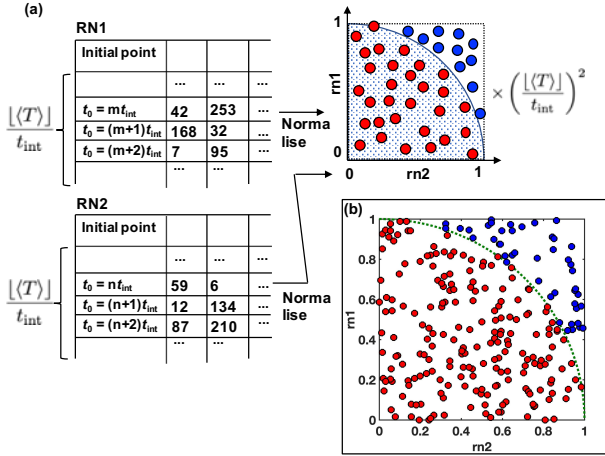


FIG. 9. (a) shows the procedure to estimate π from random numbers in RN1 and RN2. One sequence of random numbers were chosen from both RN1 and RN2, normalised in $[0,1]$, and used as a coordinate to designate a point in the x-y plane. Red filled circles are the points in the area $x^2 + y^2 < 1$, while blue filled circles were points outside. (b) shows one of the examples of rn1 and rn2 taken from RN1 and RN2, generated from Data3 and Data8 as test data.

Figure 10 (a) shows two examples of the distribution of estimated π , which are both compared with the values of π estimated from Monte Carlo method using uniform random number generated by Mersenne twister (MT). The blue points in Figure 10 (a) are probability distribution of calculated values

of π using Data1,2,4,5,6,7,9 to generate the classifier (trainingData) and Data3,8 to generate random numbers. When fitted by a Gaussian function, the median was 3.16 and the variance was 0.0750, which was comparable with the result obtained from uniformly generated random number by MT as input (plotted as orange dashed line in Figure 10 (a)). On the other hand, magenta points in Figure 10 (a) are the one using Data 3,4,5,6,7,8,9 for the classifier and Data1,2 for random numbers. As can be seen, the median of the distribution was off from the true value. This implies that the numbers generated from Data1,2 by the classifier based on Data3,4...9 were not uniform, which means that a number of smaller values (0,1,2...) was larger than that of bigger values (...253, 254, 255).

In order to seek the source of non-uniformity in random numbers generated from Data1,2 as test data, the validity of classification parameters were investigated. Figure 10 (b), (c), (d) and (e) show the histograms of Data3, Data8, Data1 and Data2 (light-blue lines) with corresponding I_{th} and $\langle I_{\text{th}} \rangle$ values. These values are highlighted by blue solid line and magenta dotted line, respectively. I_{th} is defined to be an average of two medians of double Gaussian functions fitted to a given histogram, meaning that the digitised signal generated using I_{th} can be used as a reference. On the other hand, as $\langle I_{\text{th}} \rangle$ is an average of I_{th} of multiple training data, which is not optimised to a test data, chances are that a current state can be misclassified to a different one due to fluctuation of measured current values around the median. Therefore, the difference between I_{th} and $\langle I_{\text{th}} \rangle$ can be used as a metric to characterise the extent of misclassification;

$$d_{ij} = \langle I_{\text{th}} \rangle_{ij} - I_{\text{th},i}, \quad (14)$$

where i and j are natural numbers that satisfy $1 \leq i \neq j \leq 9$, $I_{\text{th},i}$ is I_{th} for Data ' i ' and $\langle I_{\text{th}} \rangle_{ij}$ is $\langle I_{\text{th}} \rangle$ when Data ' i ' and Data ' j ' were used as test data. Positive d_{ij} means that misclassification of '1' as '0' frequently occurred when Data ' i ' was used as test data alongside with Data ' j '. Likewise, negative d_{ij} implies that '0' were often misclassified as '1' when Data ' i ' was used as test data alongside with Data ' j '. As shown in Figure 10 (b) and (c), d_{38} and d_{83} has a different sign, while d_{12} and d_{21} were both positive, seen in Figure 10 (d) and (e). This means that because more misclassification towards '0' occurred in both Data1 and Data2, RN1 and RN2 has a larger number of smaller values, leading to a larger number of points in the x-y plane that satisfy $x^2 + y^2 < 1$ and hence larger estimated values of π . In the case of Data3 and Data8, certainly there were misclassification in both cases, though the opposite sign of d_{38} and d_{83} helped to mask the non-uniformity in generated random number in the estimated values of π . This result suggests that even if the uniformity of a sequence of random numbers is not perfect, by knowing the deviation with its sign (d_{ij} , for example) and cancelling the deviations by combining two or three sequences of random numbers ($d_{\Sigma,ij}$), total uniformity of the points in higher dimension (2D,3D...) may be improved.

Combined with the distribution shown in Figure 10 (a), the randomness of points in the x-y plane can be evaluated by the

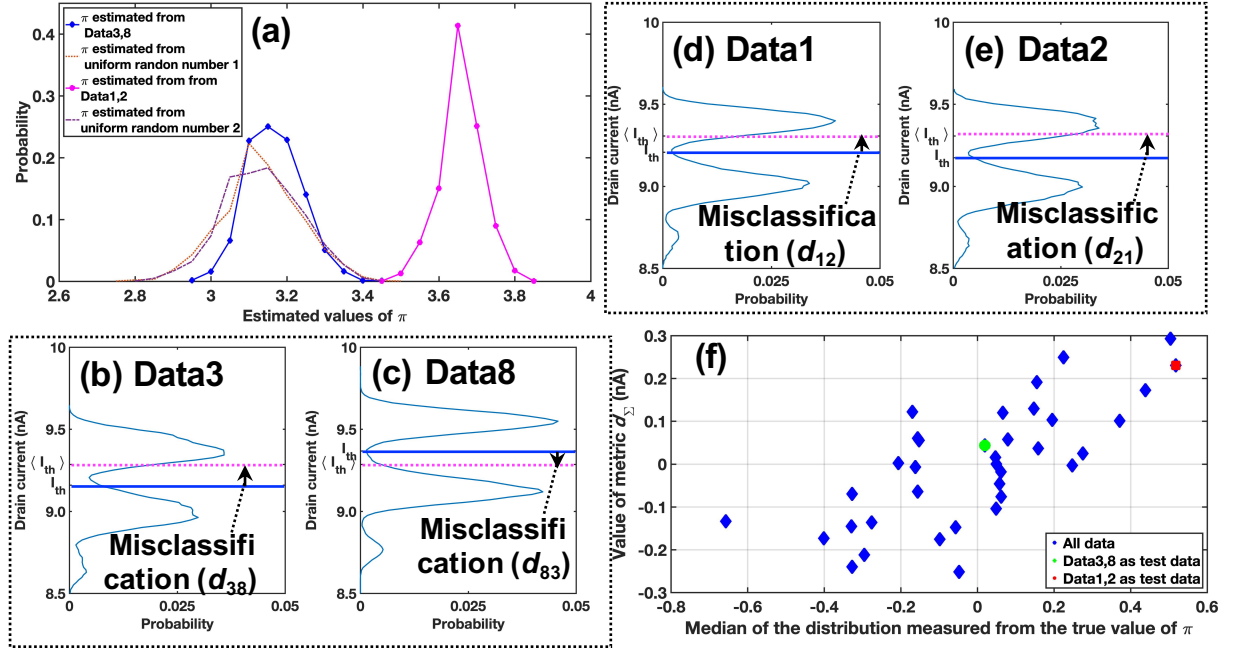


FIG. 10. Analysis on the estimated values of π . (a) shows the distributions of estimated values of π using two different test data. Blue solid diamonds are the one obtained from Data3 and Data8 as test data, while magenta filled circles are the one obtained from Data1 and Data2 as test data. Orange dotted line and purple dotted-dashed line are the ones calculated using uniform random number generated by Mersenne twister. (b) and (c) show the histograms of I_d of Data3 and Data8, respectively, (light-blue line). Blue solid lines in (b) and (c) denote threshold current (I_{th}) in each data, while magenta dotted lines in (b) and (c) show the average threshold current in training data ($\langle I_{th} \rangle$). (d) and (e) show the histogram of I_d of Data1 and Data2, respectively. (f) Values of metric d_{Σ} against the median of the distributions measured from the true value of π .

sum of d_{ij} and d_{ji} ;

$$d_{\Sigma,ij} = d_{ij} + d_{ji}; \quad (15)$$

Figure 10 (f) shows the values of this metric $d_{\Sigma,ij}$ against the median of the distribution of estimated values of π , measured from its true value. Blue diamonds denote the results from all the combinations of choosing two test data from nine measurement results, and a green filled circle highlights the case when Data3,8 were used as test data, while the red filled circle does the case when Data1,2 were used as test data. A clear pattern can be seen in this scatter plot, where the median of the distribution deviate from the true value when the value of $|d_{\Sigma,ij}|$ becomes larger. This suggests that the metric $d_{\Sigma,ij}$ can be used as a predictor of how good the estimation would be. In other words, if the value of $d_{\Sigma,ij}$ is small, the median of the distribution is expected to be closer to the true value than otherwise.

Finally, in order to evaluate the overall quality of the random number generated from our device, a histogram of the estimated values of π from all possible combinations was calculated, which is shown in Figure 11. In total, π was calculated 80855 times using the Monte Carlo method. The blue filled circles are the data points, while the blue dotted line is a Gaussian fit with the median of 3.14 and the variance of 0.273. This distribution can be broken down into individual 36 histograms, two of which are shown in Fig 10 (a). Therefore, the variance of the total distribution is determined by distribu-

tions like the one obtained from Data1,2, which miscalculate values of π due to misclassification. By reducing the misclassification (improving the learning algorithms, for example²⁹), the variance can also be smaller. In the same figure, the result from the uniform random number generated by MT is also shown, which has the median at 3.13 and the variance of 0.105, the latter of which is smaller than the one obtained from RTS, as expected.

VII. CONCLUSION

In this paper, random number generation based on random telegraph signals observed in a Si SEP device was discussed. RTSs was observed in one of the SEP devices, which showed a tunability of current states as expected^{25,26}. The observed RTS has three current levels, whose physical origin was attributed to smaller energy gap between the ground state and the first excited state. In order to achieve real-time random number generation, the classifier to perform analog-to-digital conversion should be learned from previous measurement results, rather than a result from which random numbers are to be extracted. Therefore, such an algorithm was developed and introduced in Sec.IV and V in detail. After the classifier was established using training data, random numbers were extracted from a new test data, whose uniformity was benchmarked by calculating values of π using Monte Carlo method

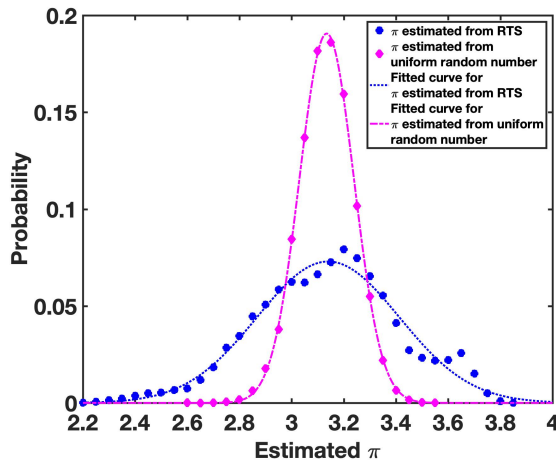


FIG. 11. Comparison between random number generated from RTS and from Mersenne twister (a PRNG). The blue solid circles are the distribution of calculated π from all data, while the blue dotted line show Gaussian fit to the distribution. The magenta filled circles are the distribution obtained from MT, while the magenta dash-dotted line show Gaussian fit to the distribution.

and compared with the result obtained from pseudo random numbers generated by MT. It turned out that the difference between the classifying parameters obtained from previous results and generated from the new result can serve as a predictor to evaluate the extent of misclassification and hence the randomness in the generated random numbers.

RTSs have been considered as a reliability issue in modern CMOS devices^{17–23} mainly because the variability of RTSs cannot be controlled across wafers or fabrication lots, and also parameters that characterise RTSs seem to be not correlated with each other^{15,28,39}. This certainly hinders the application of RTSs as HRNG, since the observation of RTSs is rare in the first place, and additionally the clock frequency at which the sampling would be made cannot be determined *a priori*. The device and algorithm shown in this paper addressed both difficulties, in a sense that SEP devices reliably exhibited RTSs^{25,26} and the unpredictability of average lifetimes was harnessed by implementing ‘supervised learning’.

While the bit rate of this proposed RNG turned out to be relatively slow, this was limited by the characteristics of RTSs used in this study. Traditionally, RTSs with average lifetime of less than μs were rarely reported in Si devices^{40–45}. However, several studies show that the trapping and de-trapping of an electron in a QD or in a trap at Si-Silicon dioxide (SiO_2) interface can be cycled at a frequency of GHz^{46,47}. This means that, combined with the CMOS compatibility, utilising RTSs as a source of randomness can be a promising candidate for future hardware security.

CONFLICT OF INTEREST STATEMENT

The authors declare that the research was conducted in the absence of any commercial or financial relationships that

could be construed as a potential conflict of interest.

AUTHOR CONTRIBUTIONS

M.K.H, Z.L, and S.S fabricated and prepared samples for measurement. F.L, K.I, Y.T, H.R and S.S established measurement setup. M.K.H, F.L, K.I, J.W.H and S.S performed measurements. K.I, I.T and S.S proposed the physical model of the observed random telegraph signals. K.I analysed the data, including the extraction of random numbers from random telegraph signals and evaluation of π using Monte Carlo method. K.I, M.S, F.L and S.S discussed the randomness of the random numbers. K.I prepared figures and drafted manuscript. All authors participated in discussion.

DATA AVAILABILITY STATEMENT

The data that supports the findings of this study are openly available in ePrints Soton, the University of Southampton Institutional Research Repository (<https://doi.org/10.5258/SOTON/D1488>)⁴⁸

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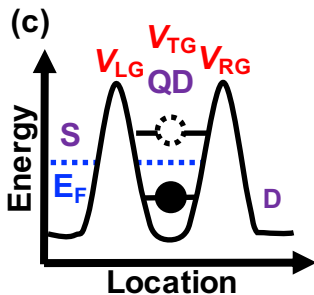
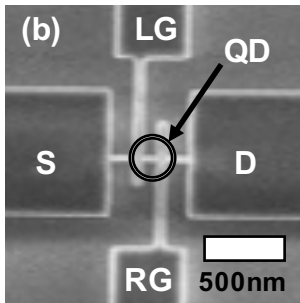
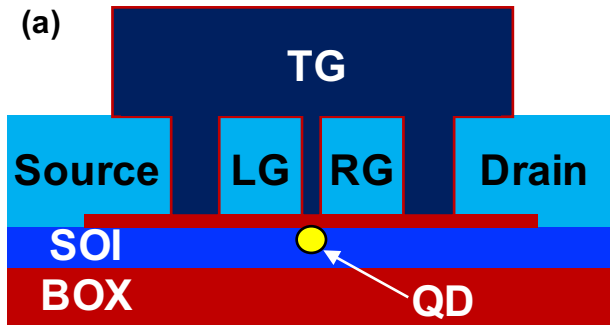
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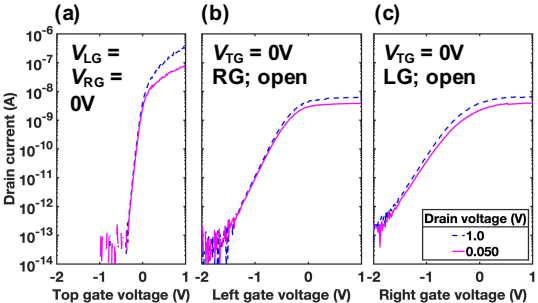
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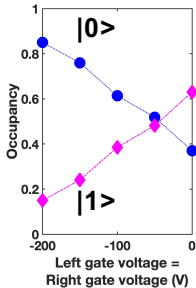
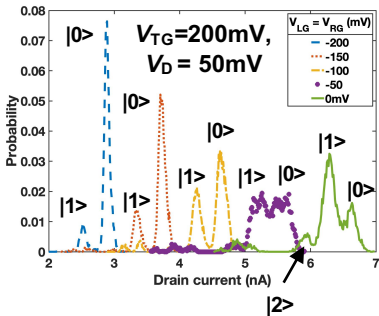
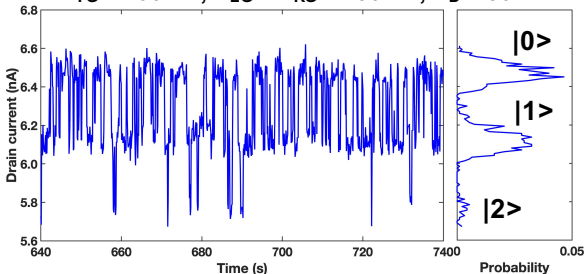
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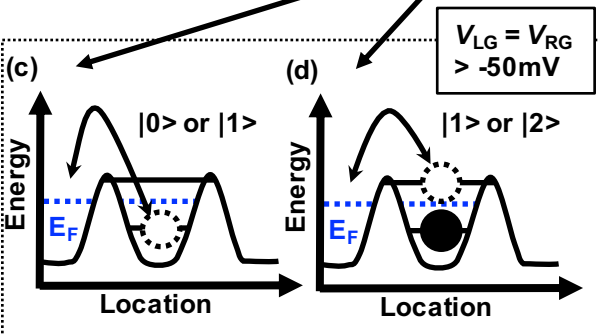
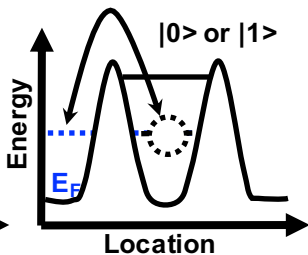
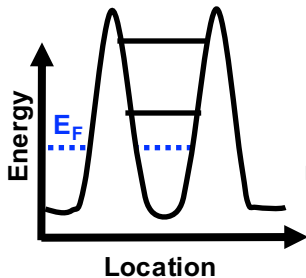




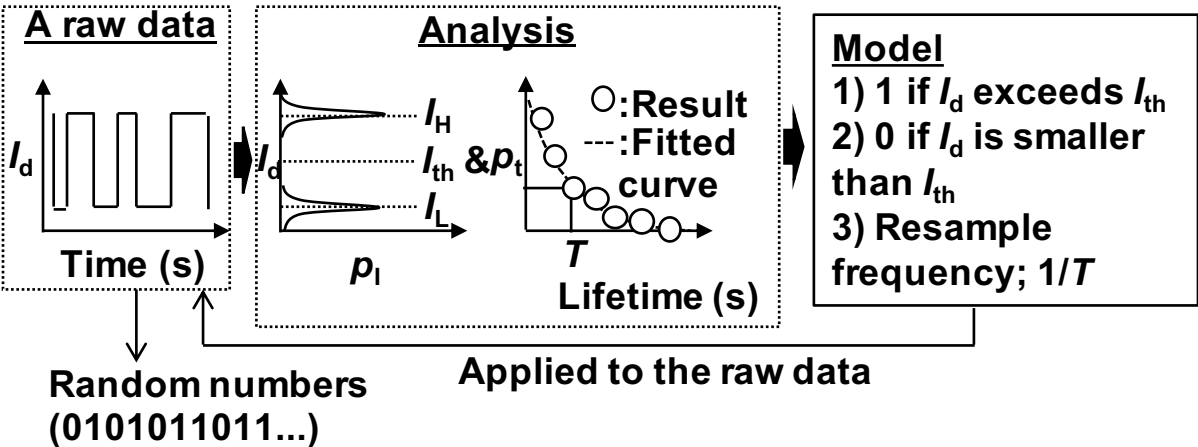
$V_{TG} = 200\text{mV}$, $V_{LG} = V_{RG} = -50\text{mV}$, $V_D = 50\text{mV}$



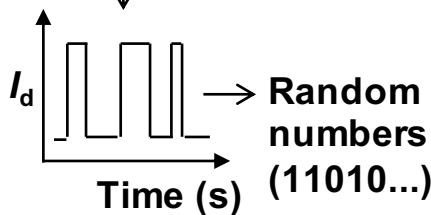
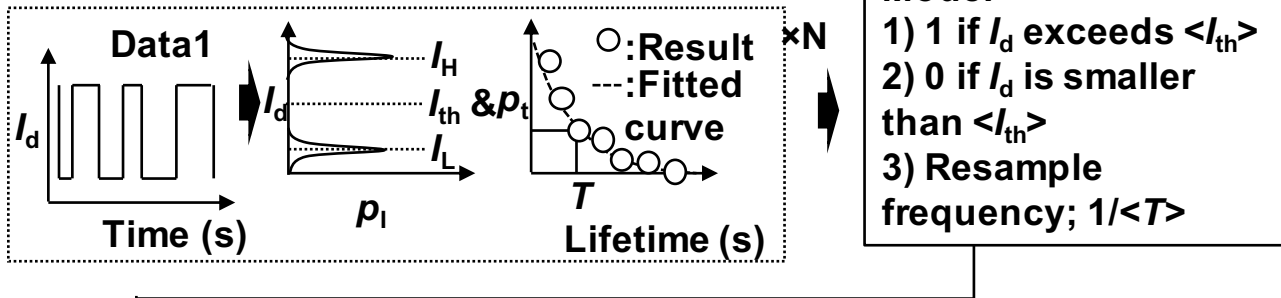
(a) $V_{\text{LG}} = V_{\text{RG}} < -200\text{mV}$ (b) $V_{\text{LG}} = V_{\text{RG}} = -50\text{mV}$



(a) post-process



(b) initialise the model using training data

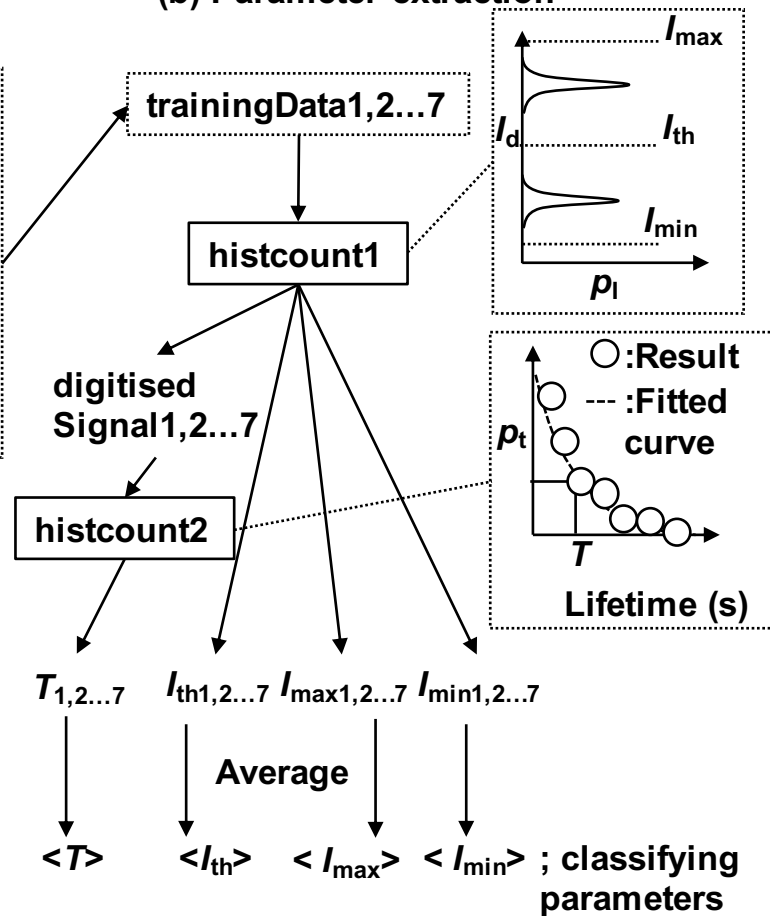


	Classification accuracy	Possibility of real-time random number generation
(a)	○	×
(b)	△	○

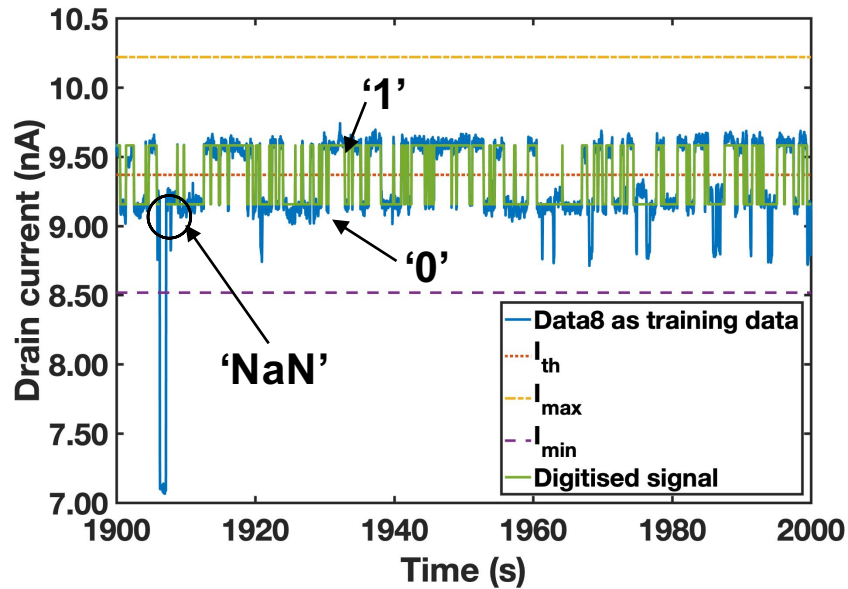
(a) Training and test data



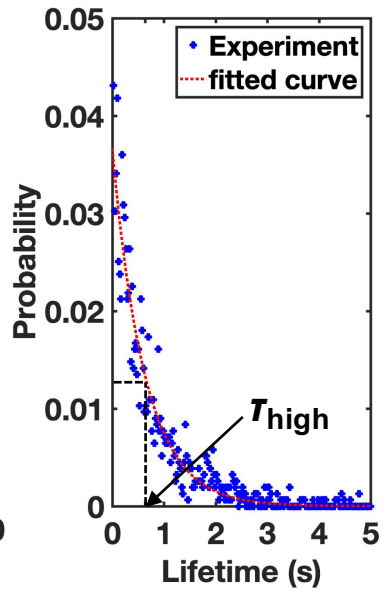
(b) Parameter extraction



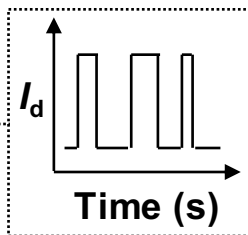
(a) Digitised signal in training



(b) Histogram of lifetime of '1'

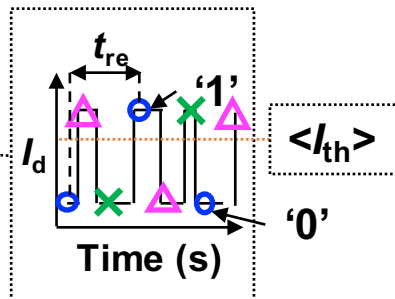


testData1,2



Classifier

- 1) 1 if I_d exceeds $\langle I_{th} \rangle$
- 2) 0 if I_d is smaller than $\langle I_{th} \rangle$
- 3) Resample frequency; $1/t_{re}$



Initial point		$t = t_0$	$t = t_0 + t_{re}$...
$t_0 = 0$	○	'0'	'1'	...
$t_0 = t_{int}$	△	'1'	'0'	...
$t_0 = 2t_{int}$	×	'0'	'1'	...
...		

Convert to 8bit number

RN1,2

Initial point				
$t_0 = 0$	○	74	86	...
$t_0 = t_{int}$	△	185	65	...
$t_0 = 2t_{int}$	×	93	132	...
...		

A sequence of random number

(a)

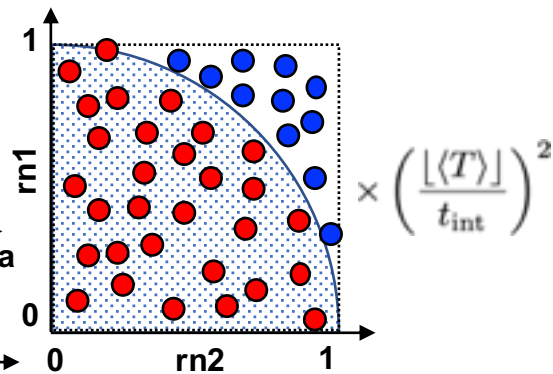
RN1

Initial point			

$t_0 = m t_{\text{int}}$	42	253	...
$t_0 = (m+1) t_{\text{int}}$	168	32	...
$t_0 = (m+2) t_{\text{int}}$	7	95	...
	

$$\frac{\lfloor \langle T \rangle \rfloor}{t_{\text{int}}}$$

Normalise



RN2

Initial point			

$t_0 = n t_{\text{int}}$	59	6	...
$t_0 = (n+1) t_{\text{int}}$	12	134	...
$t_0 = (n+2) t_{\text{int}}$	87	210	...
	

$$\frac{\lfloor \langle T \rangle \rfloor}{t_{\text{int}}}$$

Normalise

