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# Sampling Overhead Analysis of Quantum Error Mitigation: Uncoded vs. Coded Systems

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**ABSTRACT** Quantum error mitigation (QEM) is a promising technique of protecting hybrid quantum-classical computation from decoherence, but it suffers from sampling overhead which erodes the computational speed. In this treatise, we provide a comprehensive analysis of the sampling overhead imposed by QEM. In particular, we show that Pauli errors incur the lowest sampling overhead among a large class of realistic quantum channels having the same average fidelity. Furthermore, we show that depolarizing errors incur the lowest sampling overhead among all kinds of Pauli errors. Additionally, we conceive a scheme amalgamating QEM with quantum channel coding, and analyse its sampling overhead reduction compared to pure QEM. Especially, we observe that there exist a critical number of gates contained in quantum circuits, beyond which their amalgamation is preferable to pure QEM.

**INDEX TERMS** Quantum error mitigation, sampling overhead, quantum error correction codes, quantum error detection codes, hybrid quantum-classical computation.

### **ACRONYMS**

CPTnI Completely Positive Trace-nonIncreasing

CPTP Completely Positive Trace-Preserving

GEP Gate Error Probability

GGEP Generalized Gate Error Probability

QEM Quantum Error Mitigation

QECC Quantum Error Correction Code

OEDC Ouantum Error Detection Code

SOF Sampling Overhead Factor

### **NOTATIONS**

- Scalars, vectors and matrices are represented by x, x, and X, respectively. Sets and operators are denoted as  $\mathcal{X}$  and  $\mathcal{X}$ , respectively.
- The notations  $I_k$ ,  $1_n$ , and  $0_n$  denote the  $k \times k$  identity matrix, an n-dimensional all-one vector, and the n-dimensional all-zero vector, respectively. The subscripts are omitted when there is no confusion.

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- The notation  $||x||_p$  represents the  $\ell_p$ -norm of vector x. Then notation 1/x denotes the element-wise reciprocal of vector x.
- The notation  $[A]_{i,j}$  denotes the (i,j)-th entry of matrix A. For a vector x,  $[x]_i$  denotes its i-th component. The submatrix obtained by extracting the  $i_1$ -th to  $i_2$ -th rows and the  $j_1$ -th to  $j_2$ -th columns from A is denoted as  $[A]_{i_1:i_2,j_1:j_2}$ . The notation  $[A]_{:,i}$  denotes the i-th column of A, and  $[A]_{i,:}$  denotes the i-th row, respectively.
- The notation diag(·) denotes a diagonal matrix obtained by placing its argument on the main diagonal, and mdiag(A) denotes the matrix obtained by setting all entries in matrix A to zero apart from the main diagonal.
- The notation vec(A) denotes the vector obtained by vectorizing matrix A, and  $\text{vec}^{-1}(\cdot)$  denotes the inverse operation.
- The trace of matrix A is denoted as  $Tr\{A\}$ , and the complex conjugate of A is denoted as  $A^{\dagger}$ . Similarly, the complex adjoint of an operator  $\mathcal{X}$  is also denoted as  $\mathcal{X}^{\dagger}$ .
- The notation  $A \otimes B$  represents the Kronecker product between matrices A and B. The tensor product of operators A and B is also denoted as  $A \otimes B$ . Furthermore,



the Cartesian product between sets  $\mathscr{A}$  and  $\mathscr{B}$  is denoted as  $\mathscr{A} \otimes \mathscr{B}$  as well.

• The sign function sgn(x) is defined as

$$sgn(x) = \begin{cases} 1, & x > 0; \\ 0, & x = 0; \\ -1, & x < 0. \end{cases}$$

• The Bachmann-Landau notations [1] used in this treatise are given as follows:

$$\begin{aligned} a(n) &= O(b(n)) & \lim\sup_{n\to\infty} a(n)(b(n))^{-1} &< \infty \\ a(n) &= o(b(n)) & \lim\inf_{n\to\infty} a(n)(b(n))^{-1} &= 0 \end{aligned}$$

#### I. INTRODUCTION

Recent years have witnessed an astonishing development in the area of quantum computing. State-of-the-art quantum computers are typically equipped with qubits scaling from fifty to a few hundreds, and have been shown difficult to be simulated on classical supercomputers [2]. This marks the beginning of the era of noisy intermediate-scale quantum computing [3]. The word "noisy" indicates that noisy intermediate-scale quantum computers would suffer from the notorious quantum decoherence effects, which impose a perturbation on each and every quantum operation carried out by a "quantum gate".

In principle, noisy quantum gates do not necessarily prevent quantum computation from being sufficiently accurate. A classical result, namely the threshold theorem [4], states that quantum computation may be carried out in the presence of decoherence with the help of quantum error correction codes (QECCs) [5]–[8], given that the error rate of each quantum gate is below a certain threshold. Generally speaking, QECCs protect a logical quantum bit (qubit) by mapping it to a larger set of physical qubits. The redundancy of the physical qubits ensures that errors perturbing a small fraction of the qubits can be detected and corrected with the help of some ancillary qubits (ancillas). Moreover, the error-correction capability can be further enhanced by concatenating several QECCs, albeit naturally, at the expense of higher qubit overhead [9]–[11].

However, compared to the idealized quantum computing models considered in the threshold theorem, practical noisy intermediate-scale quantum computers may not be capable of supporting fully fault-tolerant operations, due to their limited number of qubits. Consequently, they may not be able to execute algorithms that require relatively long processing time, such as Shor's factorization algorithm [12] and Grover's quantum search algorithm [13]. Fortunately, there is evidence that algorithms tailored for noisy intermediate-scale quantum computers may yield superior performance compared to those of classical computers [3], [14]. Most of these algorithms belong to the category of hybrid quantum-classical algorithms, which exploit the power of classical computation to compensate for the short coherence time of quantum processors. As portrayed in Fig. 1, a typical hybrid quantum-classical algorithm would be

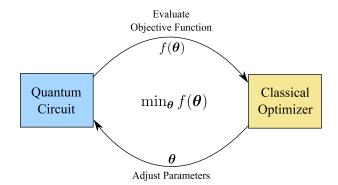


FIGURE 1. Illustration of the iterations in hybrid quantum-classical algorithms.

performed in an iterative fashion. The quantum circuit, which will be referred to as the function-evaluation circuit in this treatise, is designed to evaluate an objective function, given a set of input parameters [15]. The value of the objective function is then utilized in a classical optimizer, which computes an adjusted set of parameters for the next iteration. In general, the design of the function-evaluation circuit determines the application of the algorithm. Popular designs include the alternating operator circuit used in the quantum approximate optimization algorithm [14], the "unitary coupled-cluster ansatz" circuit applied in the computation of molecular energy based on variational eigensolver [16]–[18], as well as other heuristic designs aiming for quantum machine learning [19], [20]. Remarkably, the quantum approximate optimization algorithm has been applied to communication-related problems as well, such as channel decoding [21].

Although the quantum circuits in hybrid quantum-classical algorithms have short depth, their decoherence may still inflict non-negligible computational errors [22], [23]. This necessitates the design of low-qubit-overhead techniques for protecting quantum gates. Recently, quantum error mitigation (QEM) has been proposed, which may correct the computational result without using any ancilla [24]–[26]. Without loss of generality, we may decompose a realistic imperfect quantum gate into a perfect gate followed by a quantum channel. QEM mitigates the deleterious effect of the channel by applying an "inverse channel" right after the imperfect gate, which is implemented using a probabilistic mixture of gates [24].

In contrast to the qubit overhead of QECCs, QEM introduces another type of computational overhead, namely the sampling overhead [24]. This overhead originates from the fact that the "inverse channel" is typically not completely positive trace-preserving (CPTP) (unless the original channel is of unitary nature, and hence it does not impose decoherence) [24], [25]. Consequently, QEM leads to an increased variance in the final computational result, hence additional measurements are required at the output quantum state for achieving a satisfactory accuracy. In effect, increasing the number of measurements will slow down the computation process. As the depth of the quantum circuit grows,



the sampling overhead may accumulate dramatically. Ultimately, the benefit of quantum speedup will be neutralized for computation tasks that require extremely long coherence time.<sup>1</sup>

In general, the sampling overhead required depends on the channel characteristics. Naturally, a fundamental question concerning the practicality of QEM is: "Can we predict and control the sampling overhead given a limited number of channel parameters?" In this treatise, we investigate this deep-rooted research question from both theoretical and practical perspectives. We first introduce the notion of the so-called sampling overhead factor (SOF) for characterizing the sampling overhead incurred by a quantum channel, and then provide a comprehensive analysis of the SOF of general CPTP channels. Finally, we discuss potential techniques of reducing the SOF of quantum channels.

Our main contributions are summarized as follows.

- We present the general design philosophy of QEM from a communication theoretical perspective, emphasizing its application in hybrid quantum-classical computation, and introduce the notion of SOF to quantify its sampling overhead. Specifically, we highlight that by invoking a "quantum channel precoder" in the quantum circuit, the SOF required by QEM may be reduced.
- We formulate a coherent-triangular error decomposition of memoryless quantum channels, which are tensor products of single-qubit channels, with the aid of their Pauli transfer matrix [27] representation. Specifically, the coherent component of a quantum channel has a zero SOF, meaning that it can be mitigated without any sampling overhead when compensated using QEM in the ideal case, while the triangular component always has non-zero SOF.
- We prove that Pauli channels have the lowest SOF among all triangular channels, and show that depolarizing channels have the lowest SOF among Pauli channels.
   Furthermore, we provide upper and lower bounds on the SOF of Pauli channels.
- We conceive an amalgam of QEM and quantum codes, including QECCs and quantum error detection codes (QEDCs), aiming for reducing the overall SOF. In particular, we show that there exist critical sizes of quantum circuits, beyond which the amalgamation is preferable to pure QEM.

The structure of the treatise is demonstrated in Fig. 2, and the rest of this treatise is organized as follows. In Section II, we present preliminary concepts that will be used extensively thereafter, such as quantum states, channels and hybrid quantum-classical computation. Then, in Section III we present the general design and formulation of QEM. Based on

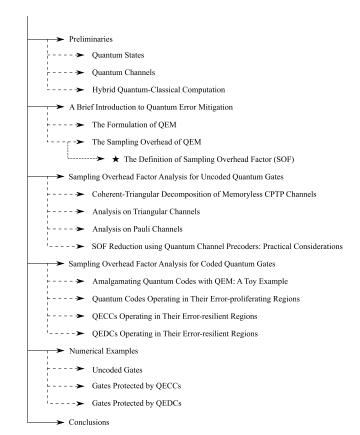


FIGURE 2. The structure of this treatise.

this formulation, we analyse on the SOF of uncoded quantum gates in Section IV. In Section V, we conceive and analyse the amalgam of QEM and QECCs as well as QEDCs, which will be referred to as the QECC-QEM and QEDC-QEM schemes, respectively. The analytical results are then demonstrated via numerical simulations in Section VI. Finally, we conclude in Section VII.

### **II. PRELIMINARIES**

### A. QUANTUM STATES

The basic carrier of quantum information is a qubit, namely a two-level quantum system. Ideally, the state of a qubit can be characterized by a vector as

$$|\psi\rangle = \alpha|0\rangle + \beta|1\rangle,\tag{1}$$

satisfying the normalization property of  $|\alpha|^2 + |\beta|^2 = 1$ . Under the conventional computational basis, the basis vectors  $|0\rangle$  and  $|1\rangle$  can be expressed as

$$|0\rangle = [1\ 0]^{\mathrm{T}}, \ |1\rangle = [0\ 1]^{\mathrm{T}}.$$

In practice, the interaction between the qubits and the environment will cause decoherence, namely turning a deterministic quantum state into a probabilistic mixture of states. Especially, the deterministic states are termed as pure states, while the probabilistic mixtures are termed as mixed states. A mixed state of a qubit can be fully characterized by a  $2 \times 2$ 

<sup>&</sup>lt;sup>1</sup>Another issue limiting the performance of QEM is that there may be residual error channels, when the gates used by QEM are themselves imperfect and/or the channel estimation is imperfect. This issue may be partially alleviated via quantum gate set tomography, which enables us to implement QEM using imperfect gates [25]. Yet in this treatise, we will focus on the issue of sampling overhead.



matrix termed as the density matrix  $\rho$  given by

$$\rho = \sum_{i} p_i |\psi_i\rangle\langle\psi_i|, \qquad (2)$$

satisfying  $p_i \ge 0$  for all i and  $\sum_i p_i = 1$ . Hence the density matrix is positive semi-definite and has unit trace. The pure states  $|\psi_i\rangle$  are the components of the probabilistic mixture. Additionally, under the computational basis, it can be expressed as the linear combination of the following matrices:

$$S_{\mathcal{I}} = \begin{bmatrix} 1 & 0 \\ 0 & 1 \end{bmatrix}, \quad S_{\mathcal{X}} = \begin{bmatrix} 0 & 1 \\ 1 & 0 \end{bmatrix},$$
  
 $S_{\mathcal{Y}} = \begin{bmatrix} 0 & -i \\ i & 0 \end{bmatrix}, \quad S_{\mathcal{Z}} = \begin{bmatrix} 1 & 0 \\ 0 & -1 \end{bmatrix}.$ 

In general, a mixed state of an n-qubit system can be characterized by a  $2^n \times 2^n$  density matrix. Similar to the single qubit case, the n-fold tensor products of  $S_{\mathcal{I}}$ ,  $S_{\mathcal{X}}$ ,  $S_{\mathcal{Y}}$  and  $S_{\mathcal{Z}}$  form a basis for the space of  $2^n \times 2^n$  density matrices, termed as the n-qubit Pauli group. To facilitate further discussion, we denote  $S_i^{(n)}$  as the i-th operator in the n-qubit Pauli group. The superscript (n) is omitted when it is unambiguous from the context.

The difference between two quantum states  $\rho_1$  and  $\rho_2$  is typically quantified using fidelity, defined as

$$F(\rho_1, \rho_2) = \text{Tr}\left\{ \left( \rho_1^{\frac{1}{2}} \rho_2 \rho_1^{\frac{1}{2}} \right)^{\frac{1}{2}} \right\}^2, \tag{3}$$

and can be simplified as

$$F(\rho_1, \rho_2) = \operatorname{Tr}\{\rho_1^{\dagger} \rho_2\}$$

if either  $\rho_1$  or  $\rho_2$  represents a pure state.

### **B. QUANTUM CHANNELS**

Formally speaking, quantum channels are typically modelled by linear operators acting on quantum states. Naturally, the output of a quantum channel has to be a legitimate quantum state, which is positive semi-definite, and has trace one. Therefore, quantum channels are required to be completely positive, trace preserving (CPTP) operators [28, Sec. 8.2.4]. Any CPTP operator  $\mathcal C$  admits an operator-sum representation formulated as follows:

$$C(\rho) = \sum_{i} \mathbf{K}_{i} \rho \mathbf{K}_{i}^{\dagger}, \tag{4}$$

where the operation elements  $K_i$  satisfy the completeness condition given by

$$\sum_{i} K_{i}^{\dagger} K_{i} = I. \tag{5}$$

Alternatively, quantum channels can also be expressed in a matrix form. To elaborate, if we represent a quantum state having a density matrix of  $\rho$  as a vector  $\mathbf{x}$ , a quantum channel  $\mathcal{C}$  acting on  $\mathbf{x}$  can be written as a matrix  $\mathbf{C}$  satisfying

$$Cx = T^{\dagger} \left[ \sum_{i} (K_{i}^{*} \otimes K_{i}) \right] Tx, \tag{6}$$

where T is a basis transition matrix which determines the specific matrix form of the quantum channel. In general, the specific matrix form of a quantum channel depends on the set of bases we choose. In this treatise, we use the Pauli transfer matrix representation [27] of quantum channels, given by

$$[C]_{i,j} = \frac{1}{2^n} \operatorname{Tr} \{ S_i C(S_j) \}, \tag{7}$$

for which the basis transition matrix T is given by

$$T = \frac{1}{\sqrt{2^n}} \left[ \text{vec}\{S_1\} \text{ vec}\{S_2\} \dots \text{vec}\{S_{4^n}\} \right].$$

In this sense, the vector representation x of a density matrix  $\rho$  can be expressed as

$$[\mathbf{x}]_i = \frac{1}{\sqrt{2^n}} \text{Tr}\{\mathbf{S}_i \rho\}. \tag{8}$$

A key quality indicator of a quantum channel is its average fidelity. As the terminology "average fidelity" suggests, it is the fidelity between the input and the output states, integrated over the space of all legitimate input states. Formally, the average fidelity of a quantum channel  $\mathcal C$  is defined as [29]

$$\bar{F}(\mathcal{C}) = \int F(|\psi\rangle\langle\psi|, \mathcal{C}(|\psi\rangle\langle\psi|)) \,\mathrm{d}\psi\rangle. \tag{9}$$

Using the Pauli transfer matrix representation, the average fidelity of  $\mathcal C$  can be written in closed form as

$$\bar{F}(C) = \frac{\text{Tr}\{C\} + 2^n}{4^n + 2^n},\tag{10}$$

where n is the number of qubits that  $\mathcal{C}$  acts upon. In general,  $\bar{F}(\mathcal{C})$  satisfies  $0 \leq \bar{F}(\mathcal{C}) \leq 1$ , and  $1 - \bar{F}(\mathcal{C})$  is often referred to as the "average infidelity" of  $\mathcal{C}$  [29].

For Pauli channels, another important quality metric is the gate error probability (GEP), namely the probability that the output state does not coincide with the input state. For example, for the following channel

$$C(\rho) = (1 - p)\rho + \frac{p}{3}S_{\mathcal{X}}\rho S_{\mathcal{X}} + \frac{p}{3}S_{\mathcal{Y}}\rho S_{\mathcal{Y}} + \frac{p}{3}S_{\mathcal{Z}}\rho S_{\mathcal{Z}},$$
(11)

the GEP is *p*. Many important results on quantum coding, including the threshold theorem, are based on GEP.

A somewhat perplexing issue is that the GEP is inconsistent with the average fidelity. More precisely, for a Pauli channel  $\mathcal{C}$ , we have  $\bar{F}(\mathcal{C}) \neq 1$  – GEP. To avoid the difficulty of using two different metrics for Pauli and non-Pauli channels, in this treatise we introduce a generalization of the GEP, which will be referred to as the generalized gate error probability (GGEP) hereafter. Specifically, we define the GGEP of channel  $\mathcal{C}$  as

$$\epsilon(\mathcal{C}) = 1 - \frac{1}{4^n} \text{Tr}\{C\}. \tag{12}$$

As a channel quality metric, GGEP has the following advantages.

1) When C is a Pauli channel, the GGEP degenerates to the conventional GEP, which is p in (11).



2) For a general channel C, which is not necessarily a Pauli channel, the GGEP is proportional to the average infidelity of  $\mathcal{C}$ , in the sense that

$$\epsilon(\mathcal{C}) = \left(1 + \frac{2^n}{4^n}\right) [1 - \bar{F}(\mathcal{C})]. \tag{13}$$

Thus an operation preserving the average fidelity would also preserve the GGEP.

### C. HYBRID QUANTUM-CLASSICAL COMPUTATION

The evaluation of eigenvalues and eigenvectors is a fundamental subroutine in many existing quantum algorithms, such as Shor's algorithm, the Harrow-Hassidim-Lloyd (HHL) algorithm, and in Hamiltonian simulation [12], [30]–[33]. In early contributions, quantum phase estimation [28, Sec. 5.2] was the default algorithm for eigenvalue evaluation, which requires a long coherence time. To enable eigenvalue evaluation on noisy intermediate-scale quantum computers, hybrid quantum-classical algorithms based on variational optimization have been proposed in [34]–[36].

Mathematically, for a Hermitian matrix H, the eigenvector  $\psi_0$  corresponding to the smallest eigenvalue can be calculated as follows

$$\psi_{g} = \underset{\psi}{\operatorname{arg \, min}} \psi^{\dagger} \boldsymbol{H} \psi,$$
 (14a)  
subject to  $\|\psi\|_{2} = 1.$  (14b)

subject to 
$$\|\boldsymbol{\psi}\|_2 = 1$$
. (14b)

This problem is referred to as the variational formulation of eigenvalue evaluation in the literature [16], [34], [37]. If the eigenvectors are reparametrized using a vector  $\boldsymbol{\theta}$ , yielding  $\psi = \psi(\theta)$ , the task of finding the smallest eigenvalue or the corresponding eigenvector may be achieved by searching for the minimum of the objective function

$$J(\theta) = \psi^{\dagger}(\theta)H\psi(\theta) \tag{15}$$

in the space of  $\theta$ , while satisfying the normalization constraint (14b). The formulation (15) has been applied to the electronic structure computation of the hydrogen molecule<sup>2</sup> in [38]. In general, this would be a non-convex problem with respect to  $\theta$ , which may be solved using iterative non-convex optimization solvers, such as the classic gradient descent and the Nelder-Mead simplex method [39]. In each iteration, the objective function or another function (e.g. the gradient) is first evaluated at a specific point in the parameter space, and then the parameters are updated according to the function

Under the framework of quantum computation, the problem (14) can be recast as

$$|\psi_{g}(\boldsymbol{\theta})\rangle = \underset{|\psi(\boldsymbol{\theta})\rangle}{\arg\min} \langle \psi(\boldsymbol{\theta})|\mathcal{H}|\psi(\boldsymbol{\theta})\rangle,$$
 (16)

<sup>2</sup>The problem of hydrogen molecular electronic structure computation aims to find the bond length corresponding to the ground state of the hydrogen molecule. In this case, the objective function  $J(\theta)$  is the ground state energy, while the parameter  $\theta$  (which is a scalar) corresponds to the inter-atom distance.

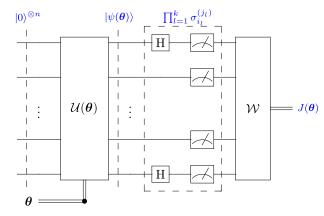


FIGURE 3. Portrayal of a quantum circuit evaluating the objective function J( heta) in a hybrid quantum-classical algorithm. The operator  ${\cal W}$ represents the weighted averaging operation. The gates in the dashed box are chosen according to the observable decomposition (18).

where  $\mathcal{H}$  is a quantum observable [28, Sec. 2.2.5] representing the matrix H, and the state  $|\psi_g(\theta)\rangle$  here corresponds to the ground state of  $\mathcal{H}$ . The quadratic form  $J(\theta) =$  $\langle \psi(\boldsymbol{\theta})|\mathcal{H}|\psi(\boldsymbol{\theta})\rangle$  can be viewed as the expectation value of the observable  $\mathcal{H}$ . The constraint (14b) is automatically satisfied due to the normalization property of quantum states. Taking decoherence into account, (16) can be reformulated equivalently for mixed states as

$$\rho_{\mathbf{g}}(\boldsymbol{\theta}) = \underset{\rho(\boldsymbol{\theta})}{\operatorname{argmin}} \operatorname{Tr}\{\mathcal{H}\rho(\boldsymbol{\theta})\}. \tag{17}$$

For conciseness of our discussion, we shall use the pure-state formulation (16) hereafter, whenever there is no confusion.

The essence of hybrid quantum-classical computation is to evaluate the functions using a quantum circuit, whereas the parameter values are updated using a classical computer, as illustrated in Fig. 1. To be more specific, a schematic of the quantum circuit evaluating the objective function  $J(\theta)$  is portrayed in Fig. 3. The input state of the circuit is typically the all-zero state  $|0\rangle^{\otimes n}$ . The function-evaluation circuit  $\mathcal{U}(\boldsymbol{\theta})$ encodes the parameter vector  $\boldsymbol{\theta}$ , and transforms the input state to the state  $|\psi(\theta)\rangle$ . The expectation value  $J(\theta)$  is then computed based on the result of multiple measurements. This is achieved by decomposing the observable  ${\mathcal H}$  (involving at most K-qubit interactions) as

$$\mathcal{H} = \sum_{k=1}^{K} \left\{ \sum_{i_1, j_1, \dots, i_k, j_k} h_{i_1, i_2, \dots, i_k}^{(j_1, j_2, \dots, j_k)} \prod_{l=1}^{k} \sigma_{i_l}^{(j_l)} \right\},$$
(18)

where  $\sigma_{i_l}^{(j_l)}$  denotes a Pauli- $j_l$  operator (i.e.,  $j_l$  may be  $\mathcal{X}$ ,  $\mathcal{Y}$  or  $\mathcal{Z}$ ) acting on the  $i_l$ -th qubit. In light of this, the term  $\prod_{l=1}^k \sigma_{i_l}^{(j_l)}$ can be implemented using a simple quantum circuit consisting of k single-qubit gates followed by measurements, as shown in the dashed box of Fig. 3. For example, a Pauli-Z operator in (18) corresponds to a direct measurement, whereas a Pauli- $\mathcal{X}$  operator corresponds to a Hadamard gate followed by measurement. Thus, the expectation value can be obtained by measuring the outputs of these simple



circuits, and evaluating a weighted sum over them using the weights  $h_{i_1,i_2,...,i_k}$ .

In contrast to "fully quantum" algorithms (e.g. Shor's algorithm and Grover's search algorithm [40]) aiming to arrive at one of the computational basis states at the very end of computation, hybrid quantum-classical algorithms aim to compute the expectation values. Therefore, the measurement results have to be averaged over a number of independent circuit executions. In this treatise, we will refer to this process as "circuit sampling".

To portray the potential advantage of hybrid quantum-classical computation, we provide a sketchy complexity comparison between classical computation and hybrid quantum-classical computation. Using classical computation, evaluating  $J(\theta)$  for  $\psi \in \mathbb{C}^{2^n}$  requires on the order of  $O(2^{2n})$  operations. By contrast, the complexity of the hybrid scheme depends both on the complexity of the function-evaluation circuit as well as on the structure of  $\mathcal{H}$ . More precisely, denoting the complexity of the function-evaluation circuit in terms of quantum gates as T, the total complexity of evaluating  $J(\theta)$  would be  $O(T\prod_{k=1}^K N_k)$ , where  $N_k$  denotes the number of weight-k Pauli strings in  $\mathcal{H}$ . Therefore, for application scenarios where the observable  $\mathcal{H}$  is "sparse" in the sense that the number of terms  $\prod_{k=1}^K N_k$  is small (e.g. polynomial in n), a substantial speedup over classical computation may be achieved, when using the hybrid approach.

### III. A BRIEF INTRODUCTION TO QUANTUM ERROR MITIGATION

When contaminated by decoherence, the quantum circuits of hybrid quantum-classical computation would produce erroneous expectation values. Fortunately, the weighted-averaging nature of hybrid quantum-classical computation facilitates the conception of a qubit-overhead-free method that mitigates the deviation from the true expectation value, namely the QEM [24]. In this section, we introduce the formulation of QEM and the computational overhead it incurs – namely the sampling overhead.

### A. THE BASIC FORMULATION OF QEM

The philosophy of QEM is to insert a probabilistic quantum circuit right after every quantum gate, which reverts the effect of the quantum channel modelling the imperfection inflicted by the gate. Conceptually, a QEM-protected gate can be portrayed as in Fig. 4. The imperfect gate (in this case an imperfect CNOT gate) can be decomposed into a perfect gate and a quantum channel  $\mathcal{C}$ . Given an input state having the density matrix  $\rho_{\rm in}$ , according to (4), the output state after the imperfect gate is given by

$$\rho_{\text{out}} = \sum_{i} \mathbf{K}_{i} U_{g} \rho_{\text{in}} U_{g}^{\dagger} \mathbf{K}_{i}^{\dagger}, \tag{19}$$

where the matrix  $U_{\rm g}$  corresponds to the effect of the perfect gate. In a vectorized form, the output state can be expressed as

$$x_{\text{out}} = CGx_{\text{in}}, \tag{20}$$

where we have  $G = U_g^* \otimes U_g$ , and the Pauli transfer matrix C is given in (6). If we have an estimate  $\hat{C}$  of C, potentially obtained using methods such as quantum process tomography [41], an estimate of the output of the decoherence-free gate G can be obtained as

$$\hat{\mathbf{x}} = \mathbf{M}\mathbf{x}_{\text{out}} = \hat{\mathbf{C}}^{-1}\mathbf{C}\mathbf{G}\mathbf{x}_{\text{in}},\tag{21}$$

where  $M = \hat{C}^{-1}$  is the Pauli transfer matrix representation of the probabilistic quantum circuit  $\mathcal{M}$  constructed for inverting the channel.

To elaborate further, if a gate is followed directly by measurement,  $\mathcal{M}$  is implemented by applying different circuits according to a probability distribution in different circuit executions and performing a weighted averaging over the measurement outcomes. This can be formulated as

$$\sum_{k=1}^{K} \boldsymbol{m}_{k}^{\mathrm{T}} \boldsymbol{M} \boldsymbol{x}_{\mathrm{out}} = \sum_{k=1}^{K} w_{k} p_{k} \boldsymbol{m}_{k}^{\mathrm{T}} \boldsymbol{M}_{k} \boldsymbol{x}_{\mathrm{out}},$$
 (22)

where  $M_k$  is the k-th candidate circuit applied at a probability of  $p_k$ ,  $w_k$  is the weight of the measurement outcome of the k-th candidate circuit, and  $m_k$  is the Pauli transfer matrix representation of the measurement operator corresponding to the outcome. For a circuit constructed by multiple gates, the weights and probability distributions follow directly by linearity. For example, for a simple circuit containing two consecutive imperfect gates  $\tilde{\boldsymbol{G}}^{(1)}$  and  $\tilde{\boldsymbol{G}}^{(2)}$ , we have

$$\sum_{j=1}^{J} \sum_{k=1}^{K} \mathbf{m}_{jk}^{T} \mathbf{M}^{(2)} \tilde{\mathbf{G}}^{(2)} \mathbf{M}^{(1)} \tilde{\mathbf{G}}^{(1)} \mathbf{x}_{in} 
= \sum_{j=1}^{J} \sum_{k=1}^{K} w_{j}^{(1)} w_{k}^{(2)} p_{j}^{(1)} p_{k}^{(2)} \mathbf{m}_{jk}^{T} \mathbf{M}_{k}^{(2)} \tilde{\mathbf{G}}^{(2)} \mathbf{M}_{j}^{(1)} \tilde{\mathbf{G}}^{(1)} \mathbf{x}_{in},$$
(23)

where the superscripts "(1)" and "(2)" indicate the first and the second gate, respectively.

Since the expectation evaluation in hybrid quantumclassical computation is implemented by applying a linear transformation (weighted averaging) to the measurement outcomes, it fits nicely with QEM. By contrast, "fully quantum" algorithms such as Shor's algorithm and Grover's algorithm operate in different ways, hence they might not be protectable by OEM.

Optionally, one may apply a quantum channel precoder to the imperfect gate, yielding

$$\hat{x}_{P} = M_{P} P_{2} C G P_{1} x_{in}$$

$$= (P_{2} \hat{C} G P_{1} G^{-1})^{-1} P_{2} C G P_{1} x_{in}, \qquad (24)$$

as shown in Fig. 4. The quantum channel precoder turns the channel C into another (possibly more preferable) channel  $P_2CGP_1G^{-1}$ . For example, the so-called Pauli twirling of [25], [26], [42] may be viewed as a quantum channel precoder turning an arbitrary channel into a Pauli channel. Similarly, Clifford twirling [43] turns an arbitrary channel into a depolarizing channel. According to the operator-sum representation [28, Sec. 8.2.4], a quantum channel precoder can be



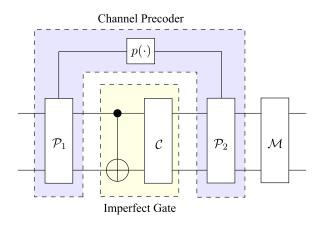


FIGURE 4. Schematic of a QEM-protected imperfect CNOT gate equipped with a quantum channel precoder.

implemented by a probabilistic mixture of gates applied both before and after the imperfect gate to be protected.<sup>3</sup>

In order to obtain M, we may first choose a basis matrix B, in which each column is the vectorized Pauli transfer matrix of a candidate circuit [25]. For example, if the first column of B represents an operator  $\mathcal{O}$ , we have  $[B]_{:,1} = \text{vec}(O)$ , where O is the Pauli transfer matrix of O. Next, we determine the coefficients as follows:

$$\boldsymbol{\mu}_{\mathcal{C}} = \boldsymbol{B}^{-1} \operatorname{vec}\{\boldsymbol{C}^{-1}\}.s \tag{25}$$

Given the coefficients  $\mu_{\mathcal{C}}$ , we can now express M as

$$\mathbf{M} = \sum_{i} [\boldsymbol{\mu}_{\mathcal{C}}]_{i} \text{vec}^{-1} \{ [\boldsymbol{B}]_{:,i} \}.$$
 (26)

This can be realized by applying the candidate circuit corresponding to  $[\boldsymbol{B}]_{:,i}$  with probability  $|[\boldsymbol{\mu}_{\mathcal{C}}]_i| \cdot ||\boldsymbol{\mu}_{\mathcal{C}}||_1^{-1}$  and assigning a weight  $\operatorname{sgn}([\boldsymbol{\mu}_{\mathcal{C}}]_i) \cdot ||\boldsymbol{\mu}_{\mathcal{C}}||_1$  to the measurement outcome. In light of this,  $\boldsymbol{\mu}_{\mathcal{C}}$  is referred to as the quasi-probability representation [24] of channel  $\mathcal{C}$ .

#### B. THE SAMPLING OVERHEAD OF OEM

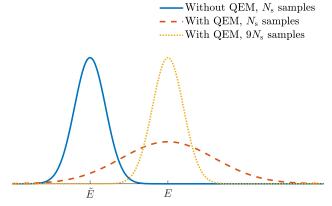
In general, the probabilistic implementation of M will incur a sampling overhead. To elaborate, if we wish to compute the expectation value  $J(\theta)$  to a certain accuracy, we have to operate the circuit a certain number of times (sampling from the output state vector). When the gates are perfect, the number of samples required is determined by the variance of the observable  $\mathcal{H}$  given by

$$Var\{\mathcal{H}\} = \langle \psi(\boldsymbol{\theta})|\mathcal{H}^2|\psi(\boldsymbol{\theta})\rangle - (\langle \psi(\boldsymbol{\theta})|\mathcal{H}|\psi(\boldsymbol{\theta})\rangle)^2. \quad (27)$$

In practice, the observable  $\mathcal{H}$  cannot be measured directly, but has to be estimated using the measurement outcome of several operators according to the decomposition (18). Therefore, if we have  $\mathcal{H} = \sum_i \mathcal{H}_i$  where each  $\mathcal{H}_i$  can be measured directly, we have

$$Var\{\mathcal{H}\} = \sum_{i} Var\{\mathcal{H}_i\}.$$

<sup>3</sup>For the moment, we assume that the gates used to implement the quantum channel precoder are decoherence-free. The practical case of erroneous gates will be considered in Section IV-D.



**FIGURE 5.** An illustration of the sampling overhead of QEM. The curves represent the distribution of the computational results with/without QEM.

and

$$Var\{\mathcal{H}_i\} = \langle \psi(\boldsymbol{\theta}) | \mathcal{H}_i^2 | \psi(\boldsymbol{\theta}) \rangle - (\langle \psi(\boldsymbol{\theta}) | \mathcal{H}_i | \psi(\boldsymbol{\theta}) \rangle)^2.$$

Upon assuming that the required accuracy is quantified in terms of its variance  $\sigma^2$ , this may be achieved using  $N_{\rm s}$  samples in the perfect gate scenario. After QEM, the expected value remains unchanged. However, if the number of samples is kept fixed, QEM will lead to a variance increase, since the entries in  $\mu_{\mathcal{C}}$  are not necessarily positive. Explicitly, the variance after QEM is given by

$$\sigma_{\text{OEM}}^2 = \|\boldsymbol{\mu}_{\mathcal{C}}\|_1^2 \sigma^2, \tag{28}$$

where  $\|\boldsymbol{\mu}_{\mathcal{C}}\|_1 \geq 1$ . Therefore, in order to achieve the same accuracy, we have to sample every quantum gate  $N_s(\|\boldsymbol{\mu}_{\mathcal{C}}\|_1^2 -$ 1) times additionally. To elaborate a little futher, we consider the 'toy' example portrayed in Fig. 5. In this example, we assume that the error-free expectation value is E, and we assume that  $\|\boldsymbol{\mu}_{\mathcal{C}}\|_1 = 3$ . Observe from the figure that when the circuit is sampled  $N_s$  times, the computational result without QEM is randomly distributed around its mean value E, which deviates from the true value E. Having been corrected by QEM, the mean value of the computational result equals to E. However, the variance of the result is increased by a factor of  $\|\boldsymbol{\mu}_{\mathcal{C}}\|_1^2 = 9$ . To ensure that the accuracy meets our requirement, we have to sample the circuit  $N_s \|\boldsymbol{\mu}_{\mathcal{C}}\|_1^2 = 9N_s$ times, as illustrated by the dotted curve of Fig. 5. Empirical evidence has shown that applying quantum channel precoders is capable of reducing the sampling overhead for certain types of channels [25].

In the previous example, we have considered the case of a single channel  $\mathcal{C}$ . In general, a quantum circuit consists of  $N_{\rm g}>1$  gates, which may be viewed as the cascade of  $N_{\rm g}$  channels, denoted by  $\mathcal{C}_1,\ldots,\mathcal{C}_{N_{\rm g}}$ . To achieve the required computational accuracy, we have to sample the circuit

$$\widetilde{N}_{s} = N_{s} \prod_{i=1}^{N_{g}} \| \boldsymbol{\mu}_{C_{i}} \|_{1}^{2}$$
(29)

times. To facilitate our analysis, we define the *sampling* overhead of a circuit as the additional number of samples



imposed by QEM, which equals to  $\widetilde{N}_s - N_s$  in our previous example. We note that the sampling overhead of a circuit increases exponentially with the number of gates.

According to the previous discussions, we may use the following notion to characterize the sampling overhead incurred by a single channel  $\mathcal C$  when compensated by QEM.

Definition 1 (Sampling Overhead Factor): We define the sampling overhead factor (SOF) of a quantum channel C as

$$\gamma_{\mathcal{C}} \triangleq \|\boldsymbol{\mu}_{\mathcal{C}}\|_{1}^{2} - 1. \tag{30}$$

Remark 1: When there is only a single gate modelled by the associated channel  $\mathcal{C}$  in the circuit, we can see from (29) that the sampling overhead of the circuit may be represented in terms of the SOF  $\gamma_{\mathcal{C}}$  as  $N_s\gamma_{\mathcal{C}}$ . When there are several gates in the circuit, the sampling overhead of the circuit can be computed using the SOFs of the channels as follows:

$$\widetilde{N}_{s} - N_{s} = N_{s} \left[ \prod_{i=1}^{N_{g}} (1 + \gamma_{\mathcal{C}_{i}}) - 1 \right]. \tag{31}$$

To provide further intuitions about the SOF, let us consider the 'toy example' of a single-qubit depolarizing channel  $\mathcal{C}$ , having the following operator-sum representation

$$C(\rho) = 0.97\rho + 0.01\mathbf{S}_{\mathcal{X}}\rho\mathbf{S}_{\mathcal{X}} + 0.01\mathbf{S}_{\mathcal{Y}}\rho\mathbf{S}_{\mathcal{Y}} + 0.01\mathbf{S}_{\mathcal{Z}}\rho\mathbf{S}_{\mathcal{Z}},$$

which can be observed to have GGEP  $\epsilon = 0.03$ . The corresponding Pauli transfer matrix takes the following form

$$C = \text{diag}\{1, 0.96, 0.96, 0.96\},\$$

and the inverse Pauli transfer matrix is given by

$$C^{-1} = \text{diag} \{1, 1.0417, 1.0417, 1.0417\}.$$

Geometrically, the channel  $\mathcal{C}$  corresponds to a homogeneous shrinking of the Bloch sphere, making its radius 0.96 times the original radius, while the inverse channel  $\mathcal{C}^{-1}$  corresponds to a homogeneous dilation extending the radius to 1/0.96 = 1.0417 times the original radius, as portrayed in Fig. 6.

To perform QEM on the channel  $\mathcal{C}$ , we should first choose a basis. Note that the Pauli operators have the following Pauli transfer matrix representations

$$P_{\mathcal{I}} = \operatorname{diag} \{1, 1, 1, 1\},\$$

$$P_{\mathcal{X}} = \operatorname{diag} \{1, 1, -1, -1\},\$$

$$P_{\mathcal{Y}} = \operatorname{diag} \{1, -1, 1, -1\},\$$

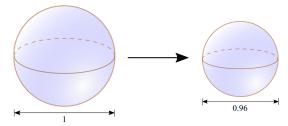
$$P_{\mathcal{Z}} = \operatorname{diag} \{1, -1, -1, 1\},\$$
(32)

which constitutes a complete basis of diagonal Pauli transfer matrices. In light of this, we may choose the Pauli operators as the basis. The corresponding quasi-probability representation can then be computed as

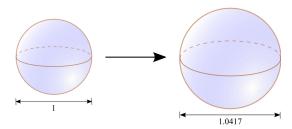
$$\mu_{\mathcal{C}} = [1.03125 - 0.01042 - 0.01042 - 0.01042]^{\mathrm{T}},$$

yielding  $\|\mu_{\mathcal{C}}\|_1 = 1.0625$ . Therefore, given the basis we chose, the SOF of the channel  $\mathcal{C}$  is given by

$$\gamma_{\mathcal{C}} = \|\boldsymbol{\mu}_{\mathcal{C}}\|_{1}^{2} - 1 = 0.1289.$$



(a) The depolarizing channel shrinks the Bloch sphere.



(b) The inverse of the depolarizing channel dilates the Bloch sphere.

FIGURE 6. Geometric illustration of a single-qubit depolarizing channel as well as the corresponding inverse channel.

### IV. SAMPLING OVERHEAD FACTOR ANALYSIS FOR UNCODED QUANTUM GATES

In this section, we investigate the SOF of quantum gates that are not protected by quantum codes. This will also lay the foundation for the analysis of coded quantum gates in Sec. V.

The SOF, in essence, may also be viewed as a specific characteristic of the representation of a quantum channel under a specific basis, as implied by (25) and (30). Hence, the particular choice of basis will certainly have an impact on it. Considering realistic restrictions and aiming for simplifying our analysis, we make the following assumptions concerning the choices of basis.

1) The basis vectors should correspond to legitimate quantum operations in order to be implementable. Formally, we assume that the basis vectors are the vectorized Pauli transfer matrices of completely positive tracenonincreasing (CPTnI) operators, for which the operation elements of (4) satisfy

$$\sum_{i} \mathbf{K}_{i}^{\dagger} \mathbf{K}_{i} \leq \mathbf{I}. \tag{33}$$

This includes perfect gates (unitary operators), imperfect gates (CPTP operators), and measurements (trace-decreasing operators).

2) We assume that the basis always includes all vectorized Pauli operators. This is not very restrictive for most existing quantum computers, since Pauli gates are one of their most fundamental building blocks.

The other (potentially more important) factor influencing the SOF is the quantum channel itself. Various channel models have been proposed in the literature, such as depolarizing channels, phase damping channels, amplitude damping channels, etc. [25], [44], [45]. To maintain the generality of our treatment, we do not explicitly consider a specific channel model, but rather a general CPTP channel.



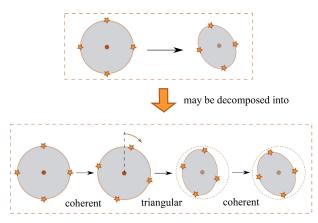


FIGURE 7. Geometric interpretation of the coherent-triangular decomposition of a signle-qubit CPTP channel on the Bloch sphere, detailed in Lemma 1. For better illustration, we only plot a cross-section of the Bloch sphere.

### A. Coherent-triangular DECOMPOSITION OF Memoryless CPTP CHANNELS

Without loss of generality, we assume that

Assumption 1: The first row and the first column in a Pauli transfer matrix corresponds to the identity operator  $\mathcal{I}^{\otimes n}$  in the n-qubit Pauli group.

Note that for a valid density matrix  $\rho$ , the condition  $\text{Tr}\{\rho\} = 1$  is always satisfied. According to Assumption 1, this implies that for the corresponding vector representation x, we have

$$x = \frac{1}{\sqrt{2^n}} [1 \,\tilde{x}^{\mathrm{T}}]^{\mathrm{T}},\tag{34}$$

where  $\tilde{x} \in \mathbb{R}^{4^n-1}$ , according to (8). The dimensionality of x is  $4^n$ , because the number of Pauli operators over n qubits is  $4^n$ . Thus for any trace-preserving channel, we have

$$C = \begin{bmatrix} 1 & \mathbf{0}^{\mathrm{T}} \\ \mathbf{b} & \tilde{C} \end{bmatrix}, \tag{35}$$

which amounts to the following result

$$Cx = \frac{1}{\sqrt{2^n}} [1 \ \tilde{C}\tilde{x} + \boldsymbol{b}]^{\mathrm{T}}.$$
 (36)

It can now be seen from (34), (35) and (36) that a CPTP channel can be viewed as an *affine transformation* in the  $(4^n - 1)$ -dimensional space spanned by the Pauli operators excluding the identity. In this regard, we have the following result for single-qubit channels, whose geometric interpretation is demonstrated in Fig. 7.

Lemma 1: Any single-qubit CPTP channel can be expressed as the composition of (up to) two coherent channels<sup>4</sup> and a triangular channel, meaning that

$$C = UDV^{T}$$

$$= \begin{bmatrix} 1 & \mathbf{0}^{T} \\ \mathbf{0} & \tilde{U} \end{bmatrix} \begin{bmatrix} 1 & \mathbf{0}^{T} \\ \tilde{U}^{T}b & \tilde{D} \end{bmatrix} \begin{bmatrix} 1 & \mathbf{0}^{T} \\ \mathbf{0} & \tilde{V} \end{bmatrix}^{T}, \quad (37)$$

where  $\tilde{\boldsymbol{U}}$  and  $\tilde{\boldsymbol{V}}$  are a pair of unitary matrices, and  $\tilde{\boldsymbol{D}}$  is a diagonal matrix. The matrix  $\boldsymbol{D}$  corresponds to the triangular channel, while the matrices  $\boldsymbol{U}$  and  $\boldsymbol{V}$  represent the coherent channels.

*Proof:* Consider the singular value decomposition of  $\tilde{C}$  given by

$$\tilde{\boldsymbol{C}} = \tilde{\boldsymbol{U}}\tilde{\boldsymbol{D}}\tilde{\boldsymbol{V}}^{\mathrm{T}}.\tag{38}$$

From (35) we obtain directly that D is a triangular channel, and hence it now suffices to show that both U and V can be implemented by unitary gates. Since the entries of Pauli transfer matrices are all real numbers [46], the matrices  $\tilde{U}$  and  $\tilde{V}$  are both  $3 \times 3$  orthogonal matrices, corresponding to the three-dimensional rotations around the Bloch sphere belonging to the special orthogonal group SO(3). They can be implemented by single-qubit unitary gates belonging to SU(2) due to the SO(3)-SU(2) homomorphism.

Compared to the triangular component, the coherent component of a CPTP channel might be easier to deal with, since their effect may be compensated by using unitary gates. This implies that if the unitary gates designed for the compensation are error-free, the effect of coherent channels may be reversed without any sampling overhead. By contrast, the triangular component may have to be compensated by using probabilistic gates, hence imposes overhead.

It is known that Lemma 1 does not hold for general multi-qubit channels [47]. Nevertheless, it is applicable to the case where the channel C is memoryless, hence it can be described by the tensor product of single-qubit channels. To see this, we may rewrite a memoryless channel as  $C = C_1 \otimes C_2 \otimes \ldots \otimes C_n$ , and for each  $C_i$  we have  $C_i = U_i D_i V_i^T$ . This further implies that

$$C = \left(\bigotimes_{i=1}^{n} U_{i}\right) \left(\bigotimes_{i=1}^{n} D_{i}\right) \left(\bigotimes_{i=1}^{n} V_{i}\right)^{\mathrm{T}}.$$

Observe that both  $\bigotimes_{i=1}^n U_i$  and  $\bigotimes_{i=1}^n V_i$  correspond to practically implementable single-qubit gates. Since the Kronecker product preserves the triangular structure, we see that  $\bigotimes_{i=1}^n D_i$  also represents a triangular channel.

#### **B.** ANALYSIS ON triangular CHANNELS

According to the discussions in Section IV-A, we are particularly interested in the quasi-probability representation of triangular channels, whose Pauli transfer matrices take the same form as the matrix  $\boldsymbol{D}$  in (37). More precisely, we define triangular channels as CPTP quantum channels whose Pauli transfer matrix can be written as follows

$$\boldsymbol{D} = \begin{bmatrix} 1 & \mathbf{0}^{\mathrm{T}} \\ \boldsymbol{b} & \boldsymbol{L} \end{bmatrix}, \tag{39}$$

where L is a lower triangular matrix. This includes both amplitude damping channels and Pauli channels as representative examples. For example, a single-qubit amplitude damping channel having decay probability p has the following

<sup>&</sup>lt;sup>4</sup>The term "coherent channels" refers to the channels having unitary matrix representations and themselves can be implemented using (error-free) unitary gates.



Pauli transfer matrix

$$\begin{bmatrix} 1 & 0 & 0 & 0 \\ 0 & \sqrt{1-p} & 0 & 0 \\ 0 & 0 & \sqrt{1-p} & 0 \\ p & 0 & 0 & 1-p \end{bmatrix},$$

where the rows/columns are ordered for ensuring that they correspond to the Pauli-I, X, Y, and Z operators, respectively. Observe that the matrix does have a triangular structure, which is preserved under the permutation of the Pauli-X, Y, and Z operators. As a direct corollary, for a multi-qubit channel inflicting amplitude damping independently on each qubit, the Pauli transfer matrix is triangular, since the triangular structure is preserved under the Kronecker product.

For a fair comparison, we consider channels having the same GGEP  $\epsilon$ , meaning that  $D \in \mathcal{D}_n(\epsilon)$ , where we have:

$$\mathcal{D}_n(\epsilon) = \{ \mathbf{D} \in \mathcal{C}_n | 4^{-n} \operatorname{Tr} \{ \mathbf{D} \} = 1 - \epsilon \}, \tag{40}$$

and  $\mathcal{D}_n$  denotes the set of all CPTP triangular channels over n qubits.

In the following proposition, we will show that regardless of the specific choice of the basis  $\boldsymbol{B}$ , Pauli channels have the lowest SOF among all triangular CPTP channels. The Pauli channels are defined as channels that transform one Pauli matrix into another. Based on (7), this implies that the corresponding Pauli transfer matrices are diagonal matrices.

Proposition 1 Pauli channels Have the Lowest SOF: Given a fixed GGEP  $\epsilon$ , for any full-rank basis matrix **B** consisting of vectorized Pauli transfer matrix representation of CPTnI operators, among all CPTP triangular channels over n qubits, Pauli channels have the lowest SOF.

Remark 2: We note that there is some empirical evidence supporting that projecting a quantum channel onto the set of Pauli channels might help in reducing the sampling overhead [25]. Here we formally show that this is indeed true, and it is true for the entire family of triangular channels.

Next, we consider some notational simplifications for a further investigation in the context of Pauli channels. First of all, since the Pauli transfer matrices of Pauli channels are diagonal (as exemplified by (32)), we may rewrite the vector representation of a Pauli transfer matrix (or that of its inverse) in a reduced-dimensional manner. Specifically, we could represent the Pauli transfer matrix of a Pauli channel using merely the vector on its main diagonal, i.e.  $d = \text{vec}\{\text{mdiag}\{D\}\}$ . Hence we have  $Dx = d \circ x$ .

Additionally, the  $16^n \times 16^n$  basis matrix  $\boldsymbol{B}$  can be reduced to  $\tilde{\boldsymbol{B}} \in \mathbb{R}^{4^n \times 4^n}$  for Pauli channels. In light of this, we have

$$\boldsymbol{\mu}_{\mathcal{D}} = \text{vec}\{\text{mdiag}\{\tilde{\boldsymbol{B}}^{-1}(1/\boldsymbol{d})\}\}. \tag{41}$$

For simplicity, we introduce the further notation of

$$\tilde{\boldsymbol{\mu}}_{\mathcal{D}} = \tilde{\boldsymbol{B}}^{-1}(1/\boldsymbol{d}). \tag{42}$$

Note that each column in  $\tilde{\boldsymbol{B}}$  represents the vectorized Pauli transfer matrix of a specific Pauli operator (as a quantum

channel). According to the definitions of the Pauli operators and (7), under the computational basis, the vectorized Pauli transfer matrix representation of single-qubit Pauli operators can be expressed as

$$s_{\mathcal{I}} = [1 \ 1 \ 1 \ 1]^{\mathrm{T}}, \ s_{\mathcal{X}} = [1 \ 1 \ -1 \ -1]^{\mathrm{T}},$$
  
 $s_{\mathcal{Y}} = [1 \ -1 \ 1 \ -1]^{\mathrm{T}}, \ s_{\mathcal{Z}} = [1 \ -1 \ -1 \ 1]^{\mathrm{T}},$  (43)

respectively. In this case, it can be seen that the corresponding simplified basis matrix of

$$\tilde{\mathbf{B}}_1 = [\mathbf{s}_{\mathcal{T}} \, \mathbf{s}_{\mathcal{X}} \, \mathbf{s}_{\mathcal{V}} \, \mathbf{s}_{\mathcal{Z}}]$$

has the form of the Hadamard transform matrix. In general, Pauli operators over n qubits can be expressed as the tensor product of n single-qubit Pauli operators, hence the corresponding  $\tilde{\boldsymbol{B}}$  takes the form of

$$\tilde{\mathbf{B}} = (\tilde{\mathbf{B}}_1)^{\otimes n}$$

which is simply a Hadamard transform matrix of higher dimensionality, where  $\tilde{\mathbf{B}}_1$  denotes the matrix  $\tilde{\mathbf{B}}$  for single-qubit systems.

By exploiting the properties of the Hadamard transform, we are now able to obtain the following result.

Proposition 2 Depolarizing Channels Have the Lowest SOF: Among all Pauli channels over n qubits having the GGEP  $\epsilon$ , depolarizing channels have the lowest SOF.

According to Proposition 2, depolarizing channels lend themselves most readily to be compensated by QEM. This means that the QEM method has a strikingly different nature compared to the family of quantum error correction schemes in terms of the overhead imposed, since depolarizing channels may be viewed as the channels most impervious for QECCs. To elaborate, depolarizing channels exhibit the lowest hashing bound among all Pauli channels, hence they would require the highest qubit overhead<sup>5</sup> for QECCs [28].

### C. BOUNDING THE SOF OF PAULI CHANNELS

In Sec. IV-B we have shown that Pauli channels are preferable for QEM in the sense that they have the lowest SOF. In this subsection, we proceed by further investigating Pauli channels and bound their SOF for a given GGEP  $\epsilon$ . First of all, by explicitly calculating the SOF of depolarizing channels, we can readily obtain a lower bound on the SOF of triangular channels (hence also on Pauli channels), as stated below.

Corollary 1 (SOF lower bound): For an triangular channel  $\mathcal C$  having the GGEP  $\epsilon$ , the SOF incurred by QEM is bounded from below as

$$\gamma_{\mathcal{C}} \ge 4\epsilon \cdot \frac{1}{(1-\epsilon)^2}.$$
(44)

The lower bound is attained, when  $\mathcal{C}$  is a depolarizing channel.

*Proof:* According to Proposition 2, it is clear that the channel having the lowest SOF among all *N*-qubit triangular

<sup>&</sup>lt;sup>5</sup>Given a fixed GEP.



channels is the *N*-qubit depolarizing channel. The SOF of this channel is given by

$$\gamma = \left\| \mathbf{H}_{N}^{-1} \left( 1 / \left( \mathbf{H}_{N} \mathbf{d}_{N} \right) \right) \right\|_{1}^{2} - 1 \tag{45a}$$

$$= \left(\frac{(4^N - 1)(1 - 2\epsilon) - \epsilon}{4^N(1 - \epsilon) - 1}\right)^2 - 1. \tag{45b}$$

where  $H_N$  and  $H_N^{-1}$  are the Hadamard transform matrix and the inverse Hadamard transform matrix having dimensionality of  $16^N \times 16^N$ , respectively, and we have  $d_N = [1 - \epsilon \epsilon (4^N - 1)^{-1} \mathbf{1}^T]$ . It is straightforward to verify that the right hand side of (45b) is a monotonically decreasing function of N, thus

$$\gamma \le \lim_{N \to \infty} \left( \frac{(4^N - 1)(1 - 2\epsilon) - \epsilon}{4^N (1 - \epsilon) - 1} \right)^2 - 1 \tag{46a}$$

$$=\frac{4\epsilon}{(1-\epsilon)^2}.\tag{46b}$$

Hence the proof is completed.

Let us now derive an upper bound on the SOF of Pauli channels. For this purpose, we consider a matrix representation specifically designed for Pauli channels, which will be referred to as the Pauli random walk (PRW) representation hereafter. More precisely, a Pauli channel  $\mathcal{C}$  over n qubits can be represented by a matrix  $C_{\text{PRW}}$  having the following form:

$$[C_{\text{PRW}}]_{i,j} = \frac{1}{2^n} \text{Tr} \{ \mathcal{P}_i \mathcal{C} \mathcal{P}_j \}. \tag{47}$$

Conventionally, an *n*-qubit Pauli channel C can be characterized using a vector  $\eta_C$  satisfying

$$C(\rho) = \sum_{i=1}^{4^n} [\eta_C]_i S_i^{(n)} \rho \left( S_i^{(n)} \right)^{\dagger}. \tag{48}$$

We will refer to  $\eta_{\mathcal{C}}$  as the probability vector of  $\mathcal{C}$  in the rest of this paper. For examples of  $\eta_{\mathcal{C}}$  values corresponding to some simple channel models, please refer to Appendix C. In light of (48), the PRW representation can be expressed as a function of  $\eta_{\mathcal{C}}$  as follows

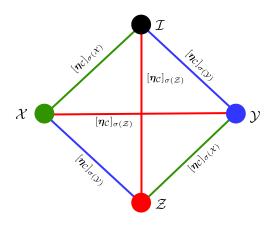
$$[\boldsymbol{C}_{PRW}(\boldsymbol{\eta}_{\mathcal{C}})]_{i,i} = [\boldsymbol{\eta}_{\mathcal{C}}]_{l}, \ \mathcal{P}_{i}\mathcal{P}_{i} = \mathcal{P}_{l}.$$

To gain further insights into the PRW representation, we will rely on a weighted Cayley graph [48]  $\mathscr{G}$  of Pauli groups, in which the *i*-th vertex represents the *i*-th operator in  $\mathscr{P}^n$ . For a specific channel  $\mathscr{C}$ , a pair of nodes i and j in the Cayley graph are connected with an edge having a weight of  $[\eta_{\mathscr{C}}]_l$ , if we have  $\mathscr{P}_i\mathscr{P}_j=\mathscr{P}_l$ . As a tangible example, the graph  $\mathscr{G}$  corresponding to the single-qubit Pauli group is portrayed in Fig. 8. The function  $\sigma(\mathscr{O})$  denotes the index of the operator  $\mathscr{O}$  in  $\mathscr{P}$ , where we have  $\sigma(\mathscr{X})=2$ ,  $\sigma(\mathscr{Y})=3$ , and  $\sigma(\mathscr{Z})=4$ . For a fixed GGEP  $\epsilon$ , we can rewrite  $C_{\mathrm{PRW}}(\eta_{\mathscr{C}})$  as

$$C_{\text{PRW}}(\eta_{\mathcal{C}}) = (1 - \epsilon)I + A(\mathcal{G}, \eta_{\mathcal{C}}), \tag{49}$$

where  $A(\mathcal{G}, \eta_{\mathcal{C}})$  is the weighted adjacency matrix of the graph  $\mathcal{G}$  corresponding to the channel  $\mathcal{C}$ , which satisfies

$$[\mathbf{A}(\mathcal{G}, \boldsymbol{\eta}_{\mathcal{C}})]_{i,j} = \begin{cases} [\boldsymbol{\eta}_{\mathcal{C}}]_l, & i \neq j, \ \mathcal{P}_i \mathcal{P}_j = \mathcal{P}_l; \\ 0, & i = j. \end{cases}$$
 (50)



**FIGURE 8.** The weighted Cayley graph  $\mathscr G$  of the single-qubit Pauli group  $\mathscr P$  constructed based on channel  $\mathcal C$ . The vertices represent the four operators in  $\mathscr P$ . The quantities next to the edges represent the corresponding edge weights.

Whenever there is no confusion, we will simply denote  $C_{PRW}(\eta_C)$  as C, and  $A(\mathcal{G}, \eta_C)$  as A.

It can be observed from (50) that the channel  $\mathcal{C}$  may be interpreted as a random walk over the graph  $\mathcal{G}$ , which maps an input state  $|\psi\rangle\langle\psi|$  to  $\mathcal{P}_i|\psi\rangle\langle\psi|$  with probability  $\eta_i$ . The goal of the quasi-probability representation method is to find another operator that reverses the random walk process. Specifically, (25) can be simplified as follows

$$\tilde{\boldsymbol{\mu}}_{\mathcal{C}} = \boldsymbol{C}^{-1} \boldsymbol{\alpha},\tag{51}$$

where  $\alpha = [1 \ \mathbf{0}_{4^n-1}^T]^T$ , and  $\tilde{\mu}_{\mathcal{C}}$  is obtained by extracting the  $4^n$  entries corresponding to Pauli operators from  $\mu_{\mathcal{C}}$  in (25).

With the aid of PRW representation, we are now ready to present the following SOF upper bound for Pauli channels.

Proposition 3 (SOF upper bound): For an n-qubit Pauli channel C, given a GGEP  $\epsilon$ , the SOF can be upper bounded as

$$\gamma_{\mathcal{C}} \le 4\epsilon \cdot \frac{1-\epsilon}{(1-2\epsilon)^2}.$$
(52)

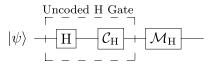
The equality is attained when there is only a single type of error, namely there is only one non-zero entry in  $\eta_{\mathcal{C}}$ .

Note that Pauli channels having only a single type of error correspond to the highest hashing bound, when mitigated by QECCs. Therefore, by considering both Corollary 1 and Proposition 3, one may intuitively conjecture that for Pauli channels having the same GGEP, the SOF increases as the hashing bound increases. The hashing bound of a Pauli channel  $\mathcal{C}$  can be expressed as [49]–[51]

$$R_{\text{hashing}} = 1 - H(\eta_{\mathcal{C}}), \tag{53}$$

where  $R_{\text{hashing}}$  is the highest affordable coding rate capable of satisfying the hashing bound, and  $H(\eta_C)$  denotes the entropy of  $\eta_C$  viewed as a probability distribution. Mathematically, the entropy  $H(\eta_C)$  is a Schur-concave function [52, Sec. 2.1]

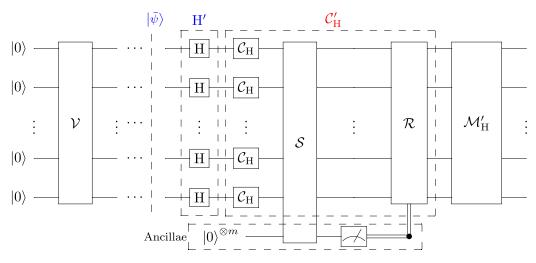




 $|\psi\rangle$  Coded H Gate  $|\psi\rangle$   $|\psi\rangle$ 

(a) Conceptual schematic of a QEM-protected uncoded Hadamard gate.

(b) Conceptual schematic of a QEM-protected coded Hadamard gate.



(c) Detailed schematic of a QEM-protected coded Hadamard gate relying on the transversal gate configuration.

FIGURE 9. Comparison between an uncoded Hadamard gate and a coded Hadamard gate, both protected by QEM.

with respect to the probability distribution  $\eta_C$ . To elaborate, a Schur-concave function f(x) is characterized by

$$f(x) \le f(Qx),$$

for any doubly stochastic matrix Q. This implies that doubly stochastic transformations on  $\eta_C$  would lead to the increase of entropy [53]. The term  $R_{\text{hashing}}$  in (53) can be seen to have the exactly opposite property termed as Schur-convex [52, Sec. 2.1], hence the aforementioned conjecture can be formulated as follows: "the SOF is a Schur-convex function with respect to the probability vector  $\eta_C$ ".

Next we show that the conjecture is correct, when the channels under consideration are memoryless channels.

Proposition 4: For any *n*-qubit memoryless Pauli channel  $C = \bigotimes_{i=1}^{n} C_i$ , given a fixed GGEP  $\epsilon$ , the SOF is a Schur-convex function of  $\eta_{C}$ , meaning that

$$\|\boldsymbol{\mu}(\boldsymbol{\eta}_{\mathcal{C}})\|_{1} \geq \left\|\boldsymbol{\mu}\left(\bigotimes_{i=1}^{n} \boldsymbol{Q}_{i} \boldsymbol{\eta}_{\mathcal{C}_{i}}\right)\right\|_{1}$$
 (54)

holds for all doubly-stochastic matrices  $Q_i$  preserving the GGEP, where  $\mu(x)$  denotes the quasi-probability representation vector of the Pauli channel having the probability vector of x.

# D. SOF REDUCTION USING QUANTUM CHANNEL PRECODERS: PRACTICAL CONSIDERATIONS

Our previous analysis indicates that depolarizing channels are the most preferable channels in terms of having the lowest SOF. This implies that Clifford twirling [43], [54], [55], a technique that turns an arbitrary channel into a depolarizing channel while preserving the original average fidelity, might be a quantum channel precoder enabling effective SOF reduction. Specifically, given a quantum channel  $\mathcal{C}$  over n qubits, the Clifford twirling  $\mathcal{T}_{\mathscr{C}}$  transforms the channel such that the output state satisfies

$$\mathcal{T}_{\mathscr{C}}[\mathcal{C}(\rho)] = \sum U^{\dagger} \mathcal{C}(U \rho U^{\dagger}) U dU, \qquad (55)$$

where the summation is carried out over the Clifford group on n qubits. Conceptually, the Clifford twirling over two-qubit channels can be implemented as demonstrated in Fig. 10a, where the gates comprising the circuits  $\mathcal{U}$  and  $\mathcal{U}^{\dagger}$  are chosen according to a uniform distribution over the set of Clifford gates.

In practice, however, the gates used for implementing Clifford twirling might be imperfect themselves. In light of this, a real-world Clifford twirling would in general impose an average fidelity reduction, and thus lead to additional SOF. For certain channels, the theoretical SOF reduction of Clifford twirling may be outweighed by this additional overhead. A representative example is constituted by the family of Pauli channels, whose SOF is rather close to that of depolarizing channels, according to Proposition 3.

The observation that the Pauli channels have similar SOFs implies that Pauli twirling  $\mathcal{T}_{\mathscr{P}}$  might be a more practical quantum channel precoder, which turns an arbitrary channel



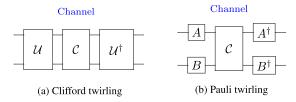


FIGURE 10. Schematics of Clifford twirling and Pauli twirling over two-qubit channels.

into a Pauli channel in the following manner [42]

$$\mathcal{T}_{\mathscr{P}}[\mathcal{C}(\rho)] = \frac{1}{4^n} \sum_{i=1}^{4^n} S_i^{\dagger} \mathcal{C}(S_i \rho S_i^{\dagger}) S_i.$$
 (56)

The implementation of Pauli twirling is portrayed in Fig. 10b, where the gates *A* and *B* are chosen according to a uniform distribution on the set of Pauli gates. In state-of-the-art quantum computers, two-qubit gates, as used in the Clifford twirling shown in Fig. 10a, would result in much more error than single-qubit gates (typically by a factor of 10 or even higher [56]), hence Pauli twirling using single-qubit gates may introduce much lower additional SOF than Clifford twirling.

In practise, we cannot directly implement twirling at both sides of the channel. Instead, we have to twirl simultaneously both the perfect gate and the channel. Therefore, the techniques should be slightly modified in order to effectively apply the twirling to the channel. Specifically, if we wish to apply Pauli twirling to a channel  $\mathcal C$  associated with an imperfect gate  $\tilde{\mathcal G}=\mathcal C\circ\mathcal G$  where  $\mathcal G$  denotes the perfect gate, we may apply the following modified twirling to  $\tilde{\mathcal G}$ 

$$\begin{split} \widetilde{\mathcal{T}}_{\mathscr{P}}[\widetilde{\mathcal{G}}(\rho)] &= \frac{1}{4^n} \sum_{i=1}^{4^n} S_i^{\dagger} \widetilde{\mathcal{G}}[(\boldsymbol{G}^{\dagger} \boldsymbol{S}_i \boldsymbol{G}) \rho (\boldsymbol{G}^{\dagger} \boldsymbol{S}_i \boldsymbol{G})^{\dagger}] S_i \\ &= \frac{1}{4^n} \sum_{i=1}^{4^n} S_i^{\dagger} \mathcal{C}[\boldsymbol{S}_i \boldsymbol{G} \rho \boldsymbol{G}^{\dagger} \boldsymbol{S}_i^{\dagger}] S_i \\ &= [\mathcal{T}_{\mathscr{P}}(\mathcal{C}) \circ \mathcal{G}](\rho). \end{split}$$

A similar procedure can also be applied to Clifford twirling. We note that the operation  $(G^{\dagger}S_iG)\rho(G^{\dagger}S_iG)^{\dagger}$  can be simplified to  $S_m\rho S_m^{\dagger}$  for some m, if the perfect gate  $\mathcal{G}$  is a Clifford gate, since the Clifford group  $\mathscr{C}_n$  is the normalizer of the Pauli group  $\mathscr{P}_n$  satisfying  $\mathscr{C}_n = \{U \in \mathbb{U}(2^n) | U \mathscr{P}_n U^{\dagger} = \mathscr{P}_n\}$ .

## V. SAMPLING OVERHEAD FACTOR ANALYSIS FOR CODED QUANTUM GATES

In this section, we investigate the SOF of gates protected by quantum channel codes, including QECCs and QEDCs. These codes are designed to convert the original channel corresponding to the unprotected gate into an reduced-error-rate channel over more qubits, with the objective of having lower GGEP. Under the framework of QEM, they can also be viewed as channel precoders. Naturally, it is of great interest to us, whether an amalgam of quantum codes and QEM can benefit each other. Specifically, we consider the scenario where every set of k logical qubits is protected using n physical qubits. Using the terminology of quantum coding, this means that we consider [[n, k, d]] codes, where d is the minimum distance of the code [7]. Furthermore, if not otherwise stated, we assume that Clifford gates are considered using the transversal gate scheme of [44], while non-Clifford gates are implemented via the magic state distillation process of [57]. These are conventional assumptions in the quantum fault-tolerant computing literature [28, Sec. 10.6]. Furthermore, for the conciseness of discussion, we assume that the quantum channels encountered in this section are all Pauli channels, or had been turned into Pauli channels by means of Pauli twirling.

### A. AMALGAMATING QUANTUM CODES WITH QEM: A TOY EXAMPLE

To elaborate further on how QEM may be amalgamated with quantum codes, we consider the simple example of protecting a single Hadamard gate. As shown in Fig. 9a, an uncoded imperfect Hadamard gate can be decomposed into a perfect Hadamard gate H and a quantum channel  $C_H$ . Given that the channel  $C_H$  is known, we can apply the QEM circuit  $\mathcal{M}_H$ to invert it. By contrast, in the coded scheme, the logical qubit is protected using an encoder V exploiting n physical qubits at the input of the circuit, as portrayed in Fig. 9b. In the code space, the original input state  $|\psi\rangle$  is expressed as the coded state  $|\psi\rangle$ , while the coded Hadamard gate may be decomposed into an equivalent perfect Hadamard gate H' and another quantum channel  $C'_H$ . Consequently, the QEM circuit  $\mathcal{M}_H'$  has to be designed for the transformed channel  $\mathcal{C}_{H}^{\prime}.$  More specifically, the Hadamard gate protected using the transversal gate configuration is depicted in more detail in Fig. 9b. The equivalent Hadamard gate is implemented simply by *n* transversal Hadamard gates. As a result, each physical qubit experiences the same channel  $\mathcal{C}_H$ . Right after the transversal gates, with the help of m ancillae, the integrity of the output state is examined by the stabilizer check S. The subsequent recovery circuit R is capable of correcting a fixed number of Pauli errors, depending on the minimum distance of the code. For example, if Steane's codes is applied,  $\mathcal{R}$  can correct any single Pauli error that appeared within the circuit. The transversal gates along with the stabilizer check and the recovery circuit constitute the transformed channel  $\mathcal{C}'_{\mathbf{H}}$ .

Ideally, since S and R are able to correct errors, the transformed channel  $\mathcal{C}'_H$  might have a lower GGEP than the original channel  $\mathcal{C}_H$ . However, this might not be true in practice, because S and R themselves are also prone to errors. Intuitively, assuming that the GGEP of each gate in the circuit is at most  $\epsilon$ , as  $\epsilon$  tends to zero, the GGEP of  $\mathcal{C}'_H$  is at most on the order of  $O(\epsilon^2)$ , since all single errors are corrected. Therefore, quantum codes are capable of reducing the channel GGEP, provided that  $\epsilon$  is sufficiently small. Specifically, the value of physical gate GGEP  $\epsilon_{th}$ , below which quantum codes become beneficial, is referred to as the fault-tolerance threshold [58].

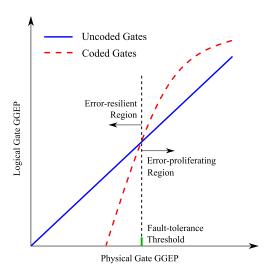


FIGURE 11. Illustration of the fault-tolerance threshold, the error-proliferating region and the error-resilient region of quantum codes.

In general, given a quantum code, the logical gate GGEP would be higher than the physical gate GGEP, when the physical gate GGEP is relatively high. As the physical gate GGEP decreases, it gradually becomes higher than the logical gate GGEP, as sketched in Fig. 11 and detailed in [59, Sec. VI-B]. The physical gate GGEP corresponding to the cross-over point of the two curves is the fault-tolerance threshold of the quantum code. We will refer to the region where the quantum code is beneficial, namely where the logical gate GGEP is lower than the physical gate GGEP, as the error-resilient region. By contrast, the opposite region will be referred to as the 'error-proliferation' region.

In the following subsections, we will first analyse the SOF of coded gates when the code is operating in its error-proliferation region, followed by the opposite scenario.

### B. QUANTUM CODES OPERATING IN THEIR ERROR-PROLIFERATING REGIONS

Using our previous results on uncoded gates in Section IV-C, it may be readily shown that quantum codes operating in their error-proliferating regions may not lead to substantial SOF reduction. Formally, we have the following result.

Corollary 2: For an uncoded gate having a GGEP of  $\epsilon$  and SOF  $\gamma$ , the SOF is lower-bounded by  $\gamma \cdot \frac{(1-2\epsilon)^2}{(1-\epsilon)^3}$ , when the gate is protected by some quantum code operating in its error-proliferating region. Furthermore, provided that the channel corresponding to the uncoded gate is a depolarizing channel, the lower bound can be further refined to  $\gamma \cdot \frac{(3-4\epsilon)^2}{(3-4\epsilon)^2}$ 

 $\overline{3(1-\epsilon)^2(3-\epsilon)}$ . Proof: This is a direct corollary following from Corollary 1 and Proposition 3. To elaborate a little further, considering the extreme case where the threshold is met exactly so that the output GGEP of the quantum code is equal to  $\epsilon$ , the generic lower bound is obtained in the form of:

$$\gamma \cdot \frac{1}{(1-\epsilon)^2} \cdot \frac{(1-2\epsilon)^2}{1-\epsilon} = \gamma \cdot \frac{(1-2\epsilon)^2}{(1-\epsilon)^3}$$
 (57)

using (44) and (52). The lower-bound valid for depolarizing channels is obtained as

$$\gamma \cdot \frac{4\epsilon \frac{1}{(1-\epsilon)^2}}{\left(\frac{(4-1)(1-2\epsilon)-\epsilon}{4(1-\epsilon)-1}\right)^2 - 1} = \gamma \cdot \frac{(3-4\epsilon)^2}{3(1-\epsilon)^2(3-\epsilon)}$$
(58)

using (44) and (45b), and by further exploiting the fact that single-qubit depolarizing channels have the highest SOF among all depolarizing channels sharing the same GGEP.  $\Box$ 

To demonstrate the implications of Corollary 2 more explicitly, we plot the lower bounds in Fig. 12. Since the fault-tolerance thresholds of most QECCs are as low as  $10^{-2} \sim 10^{-3}$ , as it can be observed from Fig. 12, even when the GGEP meets the threshold exactly, the quantum codes can only offer an overhead reduction of at most 1%. Therefore, amalgamating QEM with codes operating in their error-proliferating regions may not be mutually beneficial.

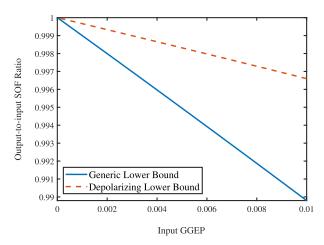


FIGURE 12. Lower bounds of the output-to-input SOF ratio for quantum codes operating in their error-proliferating regions.

### C. QECCs OPERATING IN THEIR ERROR-RESILIENT REGIONS

In light of our previous discussions, it becomes plausible that QECCs operating in their error-resilient regions may contribute to SOF reduction by reducing the gate error probability. As stated in [28, Sec. 10.6.1] for example, their error-correcting capability can be further improved via concatenation. However, the price of concatenating codes is a drastic increase in the qubit overhead. It is thus interesting to investigate whether the amalgamation of QECC and QEM would outperform pure concatenated QECCs, and if so, in what scenarios.

To elaborate, we consider the simple example of transversal Hadamard gates protected by a rate 1/3 repetition code, as portrayed in Fig. 13 and detailed in [59]. By concatenating the repetition code twice, the number of physical qubits protecting a single logical qubit become three times that of the non-concatenated code. By contrast, if we amalgamate the rate 1/3 code with QEM, the additional qubits can be used to parallelize the computation, leading to a computational



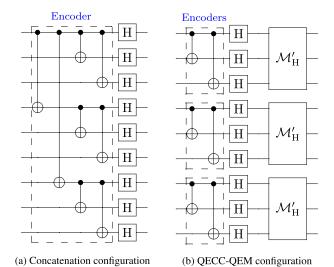


FIGURE 13. Comparison between the concatenated QECC scheme and the QECC-QEM scheme protecting transversal Hadamard gates using the rate 1/3 repetition code. In the example, the QECC-QEM scheme runs three times as fast as the concatenation scheme.

acceleration by a factor of three. In this sense, the QECC-QEM scheme outperform the concatenated scheme, when the SOF of QEM obeys  $\gamma_C \leq 2$ .

As the number of gates in a circuit increases, the QEM sampling overhead grows exponentially (as indicated by (31)), while the qubit overhead of the concatenated scheme remains constant. Therefore, there would be a critical point where the overall QEM sampling overhead escalation starts to outweigh the parallelization speedup benefits. This may be interpreted as a limitation imposed on the circuit size, beyond which full fault-tolerance becomes necessary. Next we provide an estimate of the critical point, given that the gate error probability is sufficiently low.

Proposition 5 Lower Bound on the Critical Point: Consider a quantum circuit in which each gate has a GGEP at most  $\epsilon$ . If the gates are protected using l-stage concatenated (i.e., concatenated l times) [[n, k, d]] QECC operating in its error-resilient region via the transversal gate configuration, amalgamating the code with QEM is more preferable than applying the (l+1)-stage concatenated code, when the number of gates  $N_l$  satisfies

$$N_{l} \leq \frac{\ln n}{\ln(1 + 4f^{(l)}(\epsilon)) - \ln[1 + 4f^{(l+1)}(\epsilon)]}$$

$$\approx \frac{1}{f^{(l)}(\epsilon) - f^{(l+1)}(\epsilon)} \cdot \frac{\ln n}{4}$$
(59a)
(59b)

$$\approx \frac{1}{f^{(l)}(\epsilon) - f^{(l+1)}(\epsilon)} \cdot \frac{\ln n}{4}$$
 (59b)

when  $\epsilon \ll 1$ , and  $f(\epsilon)$  is the output GGEP of the single-stage [[n, k, d]] code given the input GGEP  $\epsilon$ , and  $f^{(l)}(\epsilon)$  denotes the *l*-times self-composition of function  $f(\epsilon)$ , as exemplified by  $f^{(2)}(\epsilon) = f[f(\epsilon)]$ .

Proof: For a circuit in which every gate is protected using l-stage concatenated [[n, k, d]] QECCs, the total computational overhead (including both the QEM overhead and qubit overhead)  $\tilde{\gamma}_l$  can be bounded as

$$\tilde{\gamma}_l < n^l (1 + \gamma_l)^N, \tag{60}$$

where  $\gamma_l$  denotes the highest SOF of a single logical gate in the circuit, and N is the total number of gates. Hence the critical point  $N_l$  between l-stage concatenation and (l + 1)stage concatenation satisfies

$$(1 + \gamma_l)^{N_l} = n(1 + \gamma_{l+1})^{N_l}. (61)$$

According to Corollary 1 and Propostion 3, when  $\epsilon \ll 1$ , the upper and lower bounds for the SOF of a single gate tend to be equal. Thus the SOF of each uncoded gate can be upper bounded by  $\gamma \leq 4\epsilon$ . For *l*-stage concatenated codes, we have  $\gamma_l \leq 4 f^{\circ l}(\epsilon)$ . Therefore we obtain

$$N_l \le \frac{\ln n}{\ln(1 + 4f^{(l)}(\epsilon)) - \ln[1 + 4f^{(l+1)}(\epsilon)]}.$$
 (62)

Additionally, using the Maclaurin approximation of ln(1 +  $(x) \approx x$  when x > 0 is sufficiently small, we obtain (59b). Hence the proof is completed.

Remark 3: As a special case, the pure QEM (i.e., l = 0) is more preferable than amalgamating a single-stage [[n, k, d]]code with it, when the number of gates satisfies

$$N_0 \le \frac{\ln n}{\ln(1+4\epsilon) - \ln[1+4f(\epsilon)]}$$

$$\approx \frac{1}{\epsilon - f(\epsilon)} \cdot \frac{\ln n}{4}.$$
(63a)

$$\approx \frac{1}{\epsilon - f(\epsilon)} \cdot \frac{\ln n}{4}.$$
 (63b)

Note that when (l + 1)-stage QECC concatenation cannot be implemented due to the associated physical limitations (e.g. total number of physical qubits), the amalgam of l-stage QECC and QEM may be applied even beyond the critical point.

### D. QEDCs OPERATING IN THEIR ERROR-RESILIENT **REGIONS**

Due to their smaller minimum distance than that of QECCs, QEDCs are not capable of correcting any error. Nonetheless, they can be used as important building blocks in the scheme of post-selection fault-tolerance [60], [61]. To expound a little further, post-selection fault-tolerance differs from its conventional counterpart in that it is implemented by detecting potential errors, and only accepting the results if no error is detected. Typically, QEDCs have a shorter codeword length compared to QECCs, hence they often also possess a higher threshold. For instance, the [[4, 2, 2]] QEDC can detect an error at the price of protecting a logical gate using four physical gates, while Steane's [[7, 1, 3]] QECC requires seven gates. This makes post-selection fault-tolerance a preferable scheme, when the gates are relatively noisy [60]–[62].

In the context of QEM, the high threshold of QEDCs appears to make their amalgamation with OEM more beneficial. However, a subtle issue is that QEDCs suffer from their own sampling overhead. To elaborate, if for every gate the



probability of successful error detection is p, similar to that of QEM, the SOF of the QEDC may be defined as

$$\gamma_{\text{QEDC}} = \frac{1}{1-p} - 1 = \frac{p}{1-p},$$
(64)

which will be referred to as the QEDC-SOF in the following disucssions. Additionally, performing QEM on the post-selected channel (where single errors are eliminated) also incurs an SOF, which in this context will be referred to as the QEM-SOF. Thus, the total SOF can be calculated as follows:

$$\gamma_{\text{total}} = (1 + \gamma_{\text{QEDC}})(1 + \gamma_{\text{QEM}}) - 1$$

$$= \gamma_{\text{OEDC}} + \gamma_{\text{OEM}} + \gamma_{\text{OEDC}} \cdot \gamma_{\text{OEM}},$$
(65)

where  $\gamma_{QEDC}$  and  $\gamma_{QEM}$  represent the QEDC-SOF and the QEM-SOF, respectively. In this regard, the amalgamation of QEDC and QEM is only beneficial when the total SOF is lower than that of QEM applied directly to uncoded gates.

To compute the QEDC-SOF of each logical gate for a specific QEDC, it suffices to compute the probability that a single Pauli error occurs in the entire physical circuit corresponding to the logical gate. Let us assume that every single-qubit gate incurs a Pauli channel having a GGEP of  $\epsilon_1$ , and every two-qubit gate incurs a Pauli channel having GGEP  $\epsilon_2$ . For any logical gate implemented using the transversal gate configuration, it is clear that the occurrence probability of a single Pauli error in single-qubit and two-qubit gates, namely  $p_1$  and  $p_2$ , can be expressed as

$$p_1 = n\epsilon_1 + O(\epsilon_1^2), \ p_2 = n\epsilon_2 + O(\epsilon_2^2),$$
 (66)

for any [[n, k, d]] QEDC, since every logical gate is implemented using n physical gates. The terms having the orders of  $O(\epsilon_1^2)$  and  $O(\epsilon_2^2)$  are negligible when the GEPs are sufficiently small. According to (64), we also have the following result for the corresponding QEDC-SOF

$$\gamma_1 = n\epsilon_1 + O(\epsilon_1^2), \quad \gamma_2 = n\epsilon_2 + O(\epsilon_2^2).$$
 (67)

Among QEDCs capable of detecting a single arbitrary Pauli error, the one having the lowest n is the [[4, 2, 2]] code. In this case, we have  $\gamma_1 \approx 4\epsilon_1$  and  $\gamma_2 \approx 4\epsilon_2$ . The actual QEDC-SOF would be even higher due to the inevitable imperfections in the stabilizer measurements. On the other hand, for small  $\epsilon_1$  and  $\epsilon_2$ , we can see from Corollary 1 and Proposition 3 that when no QEDC is applied, the QEM-SOF is also approximately four times the GGEP. Therefore, we have the following remark.

*Remark 4:* Amalgamating QEDCs and QEM might not be beneficial in the sense of SOF reduction, given that the logical gates are implemented using the transversal gate configuration.

This result may not be applicable to some logical gates, namely to those that are not implemented transversally. For example, some two-qubit gates processing the two logical qubits within a single block of the [[4, 2, 2]] QEDC may be implemented using simple physical gates. As illustrated

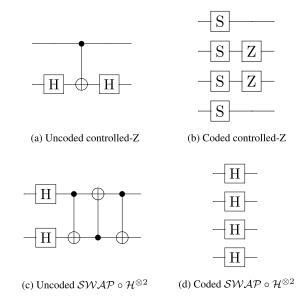


FIGURE 14. Illustration of logical gates on the two logical qubits within a [[4, 2, 2]] QEDC code block. They are not implemented in a transversal manner.

in Fig. 14, a logical controlled-Z gate can be implemented using six single-qubit gates  $\mathcal{S}\otimes(\mathcal{Z}\circ\mathcal{S})\otimes(\mathcal{Z}\circ\mathcal{S})\otimes\mathcal{S}$ . Since two-qubit gates typically have much higher GGEP compared to single-qubit gates, the QEDC-SOF may be even lower than the GGEP of a single controlled-Z gate. Similarly, the logical gate  $\mathcal{SWAP}\circ\mathcal{H}^{\otimes 2}$  can be implemented by simply using four physical Hadamard gates, hence also has a low QEDC-SOF. Here, the operator  $\mathcal{SWAP}$  refers to the SWAP gate exchanging a pair of qubits [28, Sec. 1.3.4].

Unfortunately, non-transversal logical gates can only be designed in a case-by-case manner. Moreover, not all of them admit the nice and simple implementation as those shown in Fig. 14. For example, a CNOT gate between the two qubits in a [[4, 2, 2]] code block has to be implemented via a SWAP gate, which has a high QEDC-SOF. By contrast, the transversal gate configuration is a general design paradigm that can be applied to all logical Clifford gates [44]. In this regard, we may draw the conclusion that QEDC-QEM is only beneficial for certain specific non-transversal gate designs.

### **VI. NUMERICAL RESULTS**

In this section, we augment our discussions throughout previous sections by numerical results. Throughout this section, for single-qubit channels, the basis matrix  ${\pmb B}$  used for QEM is constituted by the conventional set of quantum operations listed in Table 1 [25]. The geometric interpretation of these operations is portrayed in Fig. 15. The basis operators of QEM for two-qubit channels are constituted by the tensor product of these operators. The operators  ${\cal R}_x$ ,  ${\cal R}_y$ , and  ${\cal R}_z$  represent  $\pi/2$  rotations around the x-, y-, and z-axes of the Bloch sphere, respectively, while  ${\cal R}_{yz}$ ,  ${\cal R}_{xz}$ , and  ${\cal R}_{xy}$  represent  $\pi$  rotations around the axes determined by the equations  $\begin{cases} y=z\\ x=0 \end{cases}$ ,  $\begin{cases} x=z\\ y=0 \end{cases}$ , and  $\begin{cases} x=y\\ z=0 \end{cases}$ , respectively. Similarly,



TARLE 1	Sixteen hasis	operators used	d in OFM for	r single-auhit	channels
IADLE I.	JIXLEEII VASIS	uperatura user	a ili Ocivi idi	i Silikie-uudil	CHAIIIIEIS.

	Operator	Output state
1	$\mathcal{I}$	$\mathcal{I}( ho)= ho$
2	$\mathcal{X}$	$\mathcal{X}( ho) = oldsymbol{S}_{\mathcal{X}}  ho oldsymbol{S}_{\mathcal{X}}^{\dagger}$
3	$\mathcal{Y}$	$\mathcal{Y}( ho) = oldsymbol{S}_{\mathcal{Y}}  ho oldsymbol{S}_{\mathcal{Y}}^{\dagger}$
4	$\mathcal{Z}$	$\mathcal{Z}( ho) = oldsymbol{S}_{\mathcal{Z}}  ho oldsymbol{S}_{\mathcal{Z}}^{\dagger}$
5	$\mathcal{R}_{\mathrm{x}}$	$\mathcal{R}_{ ext{x}}( ho) = rac{1}{2}(oldsymbol{I} + \imath oldsymbol{S}_{\mathcal{X}})  ho (oldsymbol{I} + \imath oldsymbol{S}_{\mathcal{X}})^{\dagger}$
6	$\mathcal{R}_{\mathrm{y}}$	$\mathcal{R}_{\mathrm{y}}( ho) = rac{1}{2}(oldsymbol{I} + \imath oldsymbol{S}_{\mathcal{Y}})  ho (oldsymbol{I} + \imath oldsymbol{S}_{\mathcal{Y}})^{\dagger}$
7	$\mathcal{R}_{\mathrm{z}}$	$\mathcal{R}_{\mathbf{z}}( ho) = rac{1}{2}(oldsymbol{I} + \imath oldsymbol{S}_{\mathcal{Z}}) ho(oldsymbol{I} + \imath oldsymbol{S}_{\mathcal{Z}})^{\dagger}$
8	$\mathcal{R}_{\mathrm{yz}}$	$\mathcal{R}_{ ext{yz}}( ho) = rac{1}{2}(oldsymbol{S}_{\mathcal{Y}} + oldsymbol{S}_{\mathcal{Z}}) ho(oldsymbol{S}_{\mathcal{Y}} + oldsymbol{S}_{\mathcal{Z}})^{\dagger}$
9	$\mathcal{R}_{ ext{xz}}$	$\mathcal{R}_{ ext{xz}}( ho) = rac{1}{2}(oldsymbol{S}_{\mathcal{X}} + oldsymbol{S}_{\mathcal{Z}}) ho(oldsymbol{S}_{\mathcal{X}} + oldsymbol{S}_{\mathcal{Z}})^{\dagger}$
10	$\mathcal{R}_{\mathrm{xy}}$	$\mathcal{R}_{ ext{xy}}( ho) = rac{1}{2}(oldsymbol{S}_{\mathcal{X}} + oldsymbol{S}_{\mathcal{Y}}) ho(oldsymbol{S}_{\mathcal{X}} + oldsymbol{S}_{\mathcal{Y}})^{\dagger}$
11	$\pi_{\mathrm{x}}$	$\pi_{ ext{x}}( ho) = rac{1}{4}(oldsymbol{I} + oldsymbol{S}_{\mathcal{X}}) ho(oldsymbol{I} + oldsymbol{S}_{\mathcal{X}})^{\dagger}$
12	$\pi_{ m y}$	$\pi_{ ext{y}}( ho) = rac{1}{4}(oldsymbol{I} + oldsymbol{S}_{\mathcal{Y}}) ho(oldsymbol{I} + oldsymbol{S}_{\mathcal{Y}})^{\dagger}$
13	$\pi_{\mathrm{z}}$	$\pi_{\mathrm{z}}( ho) = rac{1}{4}(oldsymbol{I} + oldsymbol{S}_{\mathcal{Z}}) ho(oldsymbol{I} + oldsymbol{S}_{\mathcal{Z}})^{\dagger}$
14	$\pi_{ m yz}$	$\pi_{ ext{yz}}( ho) = rac{1}{4}(oldsymbol{S}_{\mathcal{Y}} + \imath oldsymbol{S}_{\mathcal{Z}}) ho(oldsymbol{S}_{\mathcal{Y}} + \imath oldsymbol{S}_{\mathcal{Z}})^{\dagger}$
15	$\pi_{ ext{xz}}$	$\pi_{ ext{xz}}( ho) = rac{1}{4}(oldsymbol{S}_{\mathcal{X}} + \imath oldsymbol{S}_{\mathcal{Z}}) ho(oldsymbol{S}_{\mathcal{X}} + \imath oldsymbol{S}_{\mathcal{Z}})^{\dagger}$
16	$\pi_{\mathrm{xy}}$	$\pi_{ ext{xy}}( ho) = rac{1}{4}(oldsymbol{S}_{\mathcal{X}} + \imath oldsymbol{S}_{\mathcal{Y}}) ho(oldsymbol{S}_{\mathcal{X}} + \imath oldsymbol{S}_{\mathcal{Y}})^{\dagger}$

the operators  $\pi_x$ ,  $\pi_y$ ,  $\pi_z$ ,  $\pi_{yz}$ ,  $\pi_{xz}$ , and  $\pi_{xy}$  represent the measurement operations on the corresponding axes.

#### A. UNCODED GATES

We first characterize Proposition 1 and Proposition 2 via numerical examples. In Fig. 16, the SOF vs. the GGEP is plotted for both single-qubit and two-qubit gates inflicted by coherent errors, amplitude damping and depolarizing channels, as detailed below. Here, the two-qubit channels are restricted to product channels, namely those constructed by the tensor product of two single-qubit channels. Specifically, a single-qubit amplitude damping channel  $\mathcal{C}_{damp}$  is characterized by [28, Sec. 8.3.5]

$$C_{\text{damp}}(\rho) = \mathbf{E}_0 \rho \mathbf{E}_0^{\dagger} + \mathbf{E}_1 \rho \mathbf{E}_1^{\dagger}, \tag{68}$$

where the operation elements are given by [28, Sec. 8.3.5]

$$E_0 = \begin{bmatrix} 1 & 0 \\ 0 & \sqrt{1-\delta} \end{bmatrix}, E_1 = \begin{bmatrix} 0 & \sqrt{\delta} \\ 0 & 0 \end{bmatrix},$$

and the parameter  $\delta$  is the amplitude damping probability of the channel, namely the probability that the channel turns an excited state  $|1\rangle$  into the ground state  $|0\rangle$ . Notably, the amplitude damping channel is a non-Pauli triangular channel. The single-qubit coherent channel we consider here is the over-rotation channel, which takes the form of [25]

$$C_{\text{over}}(\rho) = U_{x} \rho U_{x}^{\dagger}, \tag{69}$$

where

$$U_{X} = \begin{bmatrix} \cos(\frac{4\phi}{\pi}) & \iota \sin(\frac{4\phi}{\pi}) \\ \iota \sin(\frac{4\phi}{\pi}) & \cos(\frac{4\phi}{\pi}) \end{bmatrix}.$$

The parameter  $\phi$  controls the over-rotation angle of the channel, and  $\iota = \sqrt{-1}$  denotes the imaginary unit.

Observe from Fig. 16 that the amplitude damping channels affecting both a single and two qubits have higher SOFs

than depolarizing channels. This corroborates Proposition 1 and Proposition 2, which imply that depolarizing channels have the lowest SOF among all triangular channels. The over-rotation channels are not triangular channels, yet they exhibit the highest SOF. In general, their SOFs would depend on the specific set of basis operators comprised by the matrix **B**. In fact, coherent channels are represented by unitary transformations. In light of this, in the ideal case that "unitary rotation" gates can be implemented without decoherence, they can be compensated in an overhead-free manner by simply applying its complex conjugate.

In Fig. 16, we can also see the effect of quantum channel precoders, especially that of Pauli twirling. To elaborate, observe in both Fig. 16a and 16b, that the Pauli-twirled versions both of the coherent error and of the amplitude damping channels have almost the same SOF as depolarizing channels, provided that the gates used in the implementation of Pauli twirling are free from decoherence. By contrast, when Pauli twirling is implemented using realistic imperfect gates, the twirled channels have higher SOF, which is still lower than that of the amplitude damping channels. Another noteworthy fact is that imperfect Pauli twirling of two-qubit gates incurs relatively low overheads, compared to single-qubit gates. This is because two-qubit gates are more prone to decoherence than their single-qubit counterparts. Specifically, in this example we assume that every single-qubit gate (resp. two-qubit gate) has the same GGEP, and follow the convention that two-qubits gates have 10 times higher GGEP than single-qubit gates [56]. In light of these results, Pauli twirling may be a preferable quantum channel precoder, especially for two-qubit gates.

Next we illustrate the bounds of the SOF of Pauli channels presented in Section IV-C. As portrayed in Fig. 17, given a fixed GEP  $\epsilon$ , all points representing the SOFs of randomly produced Pauli channels fall between the upper bound (52) and the lower bound (44). Moreover, it can be seen that when the GEP is less than  $5 \times 10^{-3}$ , the upper and lower bounds are nearly identical, and a linear approximation of the SOF (i.e.,  $4\epsilon$ ) becomes rather accurate.

### B. TRANSVERSAL GATES PROTECTED BY QECC

In this subsection, we investigate the SOF when QEM is applied to transversal logical gates protected by QECCs operating in their error-resilient regions. For the simplicity of presentation, we assume that all physical gates are subjected to the deleterious effect of depolarizing channels. Furthermore, we also assume that they can be decomposed into the tensor products of single-qubit depolarizing channels. Finally, we assume that all single-qubit gates have an identical GEP, and so do two-qubit gates.

As presented in Section V-C, QEM and QECCs operating in their error-resilient regions can be beneficially amalgamated to reduce the SOF. Here we consider the amalgam of QEM and an *l*-stage concatenated Steane [[7, 1, 3]] code. According to Proposition 5, for every QECC operating in its error-resilient region, there would be several critical circuit

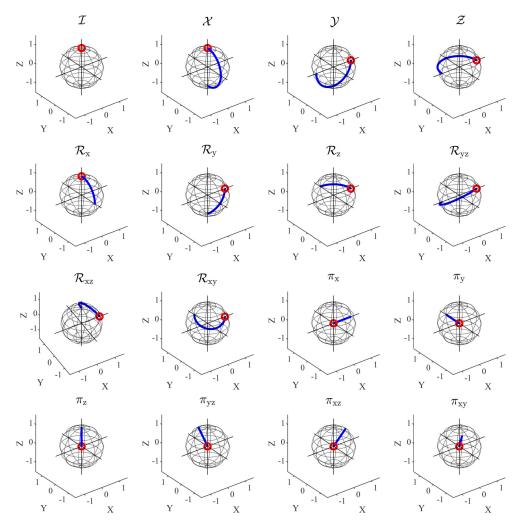


FIGURE 15. The trajectories of quantum states under the corresponding basis operations on the Bloch sphere. The circles represent the initial states, while the bold solid lines represent the trajectories.

sizes. To elaborate, if a quantum circuit contains gates that exceeds the (l+1)-th critical point, amalgamating QEM with the l-stage concatenated code will be more beneficial than relying on the (l+1)-stage concatenated code, and vice versa.

In Fig. 18, we compare the performance of three QECC-QEM schemes for quantum circuits containing various number of logical gates. In Fig. 18a, we demonstrate the aforementioned critical points of circuit size. In Fig. 18b the areas having different shading represent the circuit configurations for which the corresponding QECC-QEM scheme is the most preferable among the three candidates. As portrayed in the figures, for the case where the GEP of physical gates equals to  $10^{-4}$ , pure QEM is preferable when the circuit contains less than about  $6 \times 10^3$  gates. BY contrast, the single-stage QECC-QEM combination may be a good choice for circuits containing between  $6 \times 10^3$  and  $8 \times 10^4$ gates. An interesting issue is that the pure QEM becomes the most preferable option when the GEP is higher than about  $10^{-3}$ , which is somewhat counter-intuitive. This may be attributed to the fact that the fault-tolerance threshold of the [[7, 1, 3]] code under our assumptions used in this treatise is around  $1.5 \times 10^{-3}$ . When the GEP of physical gates is close to their threshold, the error-correction capability of QECCs is not fully exploited. To elaborate a little further, the term  $[f^{(l)}(\epsilon) - f^{(l+1)}(\epsilon)]$  in (59b) would typically be a non-monotonic function of  $\epsilon$ , with its maximum located close to the threshold. Hence, the critical circuit size increases as the GEP of physical gates decreases, provided that the GEP is sufficiently low.

### C. GATES PROTECTED BY QEDC

In this subsection, we consider a combined QEDC-QEM scheme, for which we make the same assumptions concerning the quantum gates as those stated in Section VI-B. Additionally, we assume that the GEP of two-qubit gates is 10 times as high as that of single-qubit gates.

When logical gates are implemented transversally, according to the discussion in Section V-D, the total SOF of the QEDC-QEM scheme would typically be even higher than that of pure QEM. This is demonstrated in Fig. 19, where



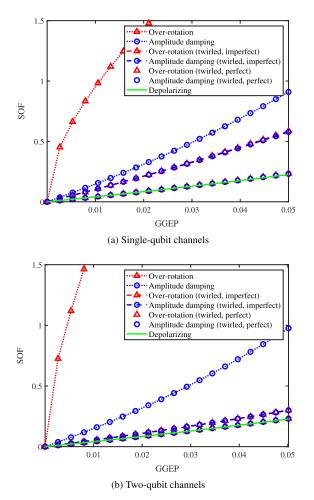
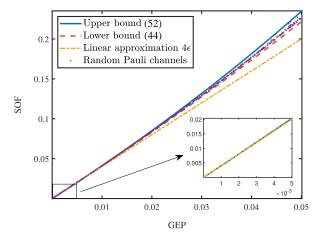
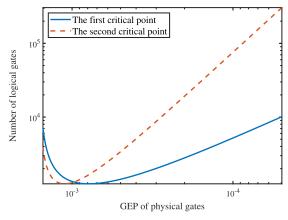


FIGURE 16. SOF as a function of GGEP for uncoded gates inflicted by coherent error, amplitude damping and depolarizing channels having the same GGEP. The Pauli-twirled versions of coherent error and amplitude damping channels are also plotted for comparison.

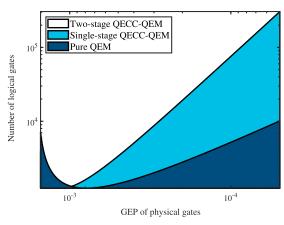


**FIGURE 17.** Upper and lower bounds of the SOF as a function of GEP  $\epsilon$  for Pauli channels over a single uncoded qubit.

we consider the total SOF of a single transversal logical CNOT gate protected by the [[4, 2, 2]] QEDC. It can be seen that most of the overhead is attributed to the QEDC-SOF, which is much higher than the overhead of pure QEM.



(a) The critical points of circuit sizes (discussed in Proposition 5)



(b) The most preferable QECC-QEM scheme

FIGURE 18. Performance comparison between QECC-QEM schemes for quantum circuits containing various numbers of logical gates. Here the QECC is chosen as Steane's [[7, 1, 3]] code.

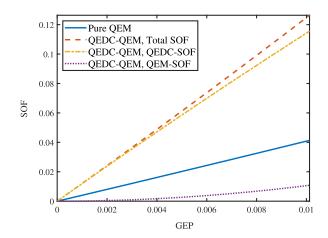
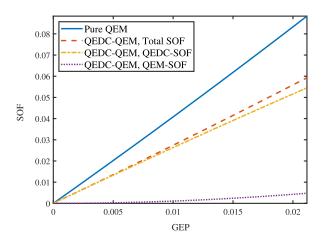


FIGURE 19. SOF comparison between pure QEM and amalgamated [[4, 2, 2]] QEDC-QEM for a CNOT gate. The logical CNOT gates is implemented using the transversal gate configuration.

By contrast, the QEM overhead in the QEDC-QEM scheme is significantly lower than that of pure QEM, implying that the post-selection fault-tolerance threshold of the [[4, 2, 2]] code is higher than 0.01.



**FIGURE 20.** SOF comparison between pure QEM and amalgamated [[4, 2, 2]] QEDC-QEM for a  $\mathcal{SWAP} \circ \mathcal{H}^{\otimes 2}$  gate. The logical  $\mathcal{SWAP} \circ \mathcal{H}^{\otimes 2}$  gate is implemented as illustrated in Fig. 14d instead of transversally.

As suggested by Fig. 14, some non-transversal logical gates may even outperform transversal gates in terms of requiring lower QEDC-QEM SOF. In particular, we consider the specific logical gate of  $\mathcal{SWAP} \circ \mathcal{H}^{\otimes 2}$  implemented in the manner illustrated in Fig. 14d. Since this implementation only involves single-qubit gates, the QEDC-SOF is significantly lower compared to the transversal implementation. Consequently, as portrayed in Fig. 20, the total QEDC-QEM overhead is lower than that of pure QEM. However, it is still not clear, whether this fact can justify the practical value of the QEDC-QEM scheme, since designing a low-sampling-overhead non-transversal implementation of all Clifford gates would require substantial effort. This may be an interesting topic deserving further investigation.

### VII. CONCLUSIONS

We have presented a comprehensive analysis on the SOF of QEM under various channel conditions. For uncoded gates affected by errors modelled by general CPTP channels, we have shown that Pauli channels have the lowest SOF among all triangular channels (which includes the amplitude damping channels) having the same GGEP. Following this line of reasoning, we have shown furthermore that depolarizing channels have the lowest SOF in the family of all Pauli channels.

We have also conceived the QECC-QEM as well as the QEDC-QEM schemes, and have shown that there exist several critical quantum circuits sizes, beyond which sophisticated codes having more concatenation stages is more preferable, and vice versa. Specifically, for QEDC-QEM, we have demonstrated that it may not be compatible with the popular transversal gate configuration, but they may still have beneficial applications, when the logical gates are appropriately designed.

### APPENDIX A PROOF OF PROPOSITION 1

*Proof:* To prove our claim, it suffices to show that Pauli twirling does not increase the SOF. Without loss of generality, we assume that the specific columns corresponding to Pauli operators in  $\boldsymbol{B}$  are the first  $4^n$  columns. First we note that the quasi-probability representation corresponding to any CPTP channel having Pauli transfer matrix representation  $\boldsymbol{C}$  may be expressed as:

$$\boldsymbol{\mu}_{\boldsymbol{C}} = \boldsymbol{B}^{-1} \operatorname{vec}\{\boldsymbol{C}^{-1}\},\tag{70}$$

while the quasi-probability representation corresponding to the Pauli-twirled channel is given by

$$\boldsymbol{\mu}_{\mathcal{T}_{\mathscr{D}}\boldsymbol{C}} = \boldsymbol{B}^{-1}\operatorname{vec}\{[\mathcal{T}_{\mathscr{D}}\boldsymbol{C}]^{-1}\},\tag{71}$$

where the super-operator  $\mathcal{T}_{\mathscr{D}}$  represents the Pauli twirling operation. We can rewrite (71) in a matrix form as

$$\boldsymbol{\mu}_{\mathcal{T}_{\mathscr{D}}\boldsymbol{C}} = \boldsymbol{B}^{-1}\operatorname{vec}\{[\operatorname{vec}^{-1}\{\boldsymbol{T}_{\mathscr{D}}\operatorname{vec}\{\boldsymbol{C}\}\}]^{-1}\}, \qquad (72)$$

where  $T_{\mathscr{P}}$  denotes the matrix representation of the Pauli twirling operator. Recall that the Pauli-twirled channel is given by

$$\mathcal{T}_{\mathscr{P}}C = \frac{1}{4^n} \sum_{i=1}^{4^n} \mathcal{P}_i C. \tag{73}$$

Upon introducing  $\mathbf{B} = [\mathbf{b}_1 \ \mathbf{b}_2 \ \dots \ \mathbf{b}_{16^n}]$ , the Pauli operator  $\mathcal{P}_i$  can be expressed in a matrix form as  $\mathbf{P}_i = \mathbf{b}_i \mathbf{b}_i^{\mathrm{T}}$ . Thus the Pauli twirling operator can be represented as

$$T_{\mathscr{P}} = \frac{1}{4^n} (BI_{\mathscr{T}}) (BI_{\mathscr{T}})^{\mathrm{T}}, \tag{74}$$

with  $I_{\mathscr{P}}$  being the following matrix

$$I_{\mathscr{P}} = \begin{bmatrix} I_{4^n} & 0_{4^n \times (16^n - 4^n)} \\ 0_{(16^n - 4^n) \times 4^n} & 0_{(16^n - 4^n) \times (16^n - 4^n)} \end{bmatrix}.$$

Thus we have

$$T \mathscr{P} \operatorname{vec}\{C\} = \operatorname{vec}\{\operatorname{mdiag}\{C\}\}.$$

Since the Pauli transfer matrix C is triangular, we have

$$(\operatorname{mdiag} \{C\})^{-1} = (\operatorname{mdiag} \{C^{-1}\}),$$

hence (72) can be simplified as

$$\mu_{\mathcal{T}_{\mathscr{D}}C} = B^{-1}T_{\mathscr{D}}\operatorname{vec}\{C^{-1}\}. \tag{75}$$

Using (70) and (75), we can show that if the statement of

$$\mu_{\mathcal{T}_{\infty}C} = T_{\perp}B^{-1}\operatorname{vec}\{C^{-1}\} = T_{\perp}\mu_{C}$$

holds for a certain matrix  $T_L$ , the proof can be completed by showing that  $||T_L||_1 \le 1$ , since we have:

$$||T_{L}||_{1} = \sup_{x} \frac{||T_{L}x||_{1}}{||x||_{1}} \ge \frac{||\mu_{T_{\mathscr{D}}C}||_{1}}{||\mu_{C}||_{1}}.$$



Next we construct the matrix  $T_L$  explicitly. Let us consider the QR decomposition [63] of the matrix B

$$\mathbf{B} = \mathbf{Q}\mathbf{R},\tag{76}$$

where Q is an orthogonal matrix and R is an upper triangular matrix. Substituting (74) and (76) into (75), we have

$$\mu_{\mathcal{T}_{\mathscr{P}}C} = \frac{1}{4^n} I_{\mathscr{P}} R^{\mathrm{T}} Q^{\mathrm{T}} \operatorname{vec}\{C^{-1}\}.$$
 (77)

Similarly, we can obtain

$$\boldsymbol{\mu}_{\boldsymbol{C}} = \boldsymbol{R}^{-1} \boldsymbol{Q}^{\mathrm{T}} \operatorname{vec} \{ \boldsymbol{C}^{-1} \}. \tag{78}$$

Having compared (77) and (78), we may observe that

$$T_{\mathrm{L}} = \frac{1}{4^{n}} I_{\mathscr{P}} R^{\mathrm{T}} R.$$

Upon introducing  $Q = [q_1 \ q_2 \ \dots \ q_{16^n}]$ , the matrix R can be represented by

$$\mathbf{R} = \begin{bmatrix} \mathbf{q}_{1}^{\mathrm{T}} \mathbf{b}_{1} & \mathbf{q}_{1}^{\mathrm{T}} \mathbf{b}_{2} & \mathbf{q}_{1}^{\mathrm{T}} \mathbf{b}_{3} & \dots \\ 0 & \mathbf{q}_{2}^{\mathrm{T}} \mathbf{b}_{2} & \mathbf{q}_{2}^{\mathrm{T}} \mathbf{b}_{3} & \dots \\ 0 & 0 & \mathbf{q}_{3}^{\mathrm{T}} \mathbf{b}_{3} & \dots \\ \vdots & \vdots & \vdots & \ddots \end{bmatrix}.$$
(79)

Since Pauli operators are orthogonal to each other, the first  $4^n$  columns in  $\mathbf{B}$  (i.e.,  $\mathbf{b}_1$  through  $\mathbf{b}_{4^n}$ ) are also mutually orthogonal, meaning that

$$\boldsymbol{q}_i^{\mathrm{T}} \boldsymbol{b}_j = \begin{cases} \|\boldsymbol{b}_i\|_2, & i = j; \\ 0, & i \neq j, \end{cases}$$

when  $i = 1, 2, \dots, 4^n$  and  $j = 1, 2, \dots, 4^n$ . Therefore we have

$$I_{\mathscr{P}}R^{\mathrm{T}} = [\operatorname{diag}\{\|\boldsymbol{b}_1\|_2, \|\boldsymbol{b}_2\|_2, \dots, \|\boldsymbol{b}_{4^n}\|_2\} \ \mathbf{0}_{4^n \times 16^n}].$$

Upon introducing  $m_{\mathbf{B}} = \max_{i=1,...,16^n} \|\mathbf{b}_i\|_2$ , from (79) we have

$$\|\boldsymbol{T}_{L}\|_{1} \leq \frac{m_{\boldsymbol{B}}}{4^{n}} \max_{i=1,\dots,16^{n}} \sum_{j=1}^{\min\{4^{n},i\}} |\boldsymbol{q}_{j}^{T}\boldsymbol{b}_{i}|$$
 (80a)

$$\leq \frac{m_{\mathbf{B}}}{4^n} \max_{i=1,\dots,16^n} \|\mathbf{w}_i\|_1 \max_{j=1,\dots,4^n} \|\mathbf{b}_j\|_2 \quad (80b)$$

$$\leq \frac{m_B^2}{4^n} \max_{i=1,\dots,16^n} \|\mathbf{w}_i\|_1, \tag{80c}$$

where  $\mathbf{w}_i \in \mathbb{R}^{\min\{4^n, i\}}$ , and

$$[\mathbf{w}_i]_j = \frac{\mathbf{b}_j^{\mathrm{T}} \mathbf{b}_i}{\|\mathbf{b}_i\|_2^2}.$$
 (81)

Using (81), the projection of  $b_i$  onto the space spanned by Pauli operators can be expressed as

$$T \mathscr{D} \boldsymbol{b}_i = [\boldsymbol{Q}]_{:,1:4^n} [\boldsymbol{Q}]_{:,1:4^n}^1 \boldsymbol{b}_i$$

$$= \sum_{j=1}^{\min\{4^n,i\}} (\boldsymbol{q}_j^{\mathrm{T}} \boldsymbol{b}_i) \boldsymbol{q}_j$$

$$=\sum_{j=1}^{\min\{4^n,i\}} [\boldsymbol{w}_i]_j \boldsymbol{b}_j.$$

Since  $B_i = \text{vec}^{-1}\{b_i\}$  corresponds to a CPTnI operator, the complete positiveness<sup>6</sup> implies  $[w_i]_j \ge 0$  for all i and j, and the "trace–non-increasing" property implies  $\sum_j [w_i]_j \le 1$ . Therefore we have  $||w_i||_1 \le 1$  for all i, and hence

$$\|T_{\mathbf{L}}\|_{1} \le 4^{-n} m_{\mathbf{R}}^{2}. \tag{82}$$

Note that  $m_B^2 = \max_i \|\mathbf{B}_i\|_F^2$ . By consider the operator-sum decomposition of  $\mathbf{B}_i$ , we see that

$$\|\mathbf{B}_i\|_{\mathrm{F}}^2 = \|\sum_i (\mathbf{K}_i^* \otimes \mathbf{K}_i)\|_{\mathrm{F}}^2$$
 (83a)

$$\leq \left(\sum_{i} \|\boldsymbol{K}_{i}^{*} \otimes \boldsymbol{K}_{i}\|_{\mathrm{F}}\right)^{2} \tag{83b}$$

$$= \left(\sum_{i} \operatorname{Tr}\{\boldsymbol{K}_{i}^{\dagger}\boldsymbol{K}_{i}\}\right)^{2} \tag{83c}$$

$$\leq 4^n,$$
 (83d)

where (83d) follows from (33).

Combining (82) and (83), we can see that  $\|T_L\|_1 \le 1$ , hence the proof is completed.

### APPENDIX B

### **PROOF OF PROPOSITION 2**

To facilitate our further analysis, we denote the Hadamard transform matrix on the space of Pauli transfer matrix representation of an n-qubit system as  $\boldsymbol{H}_n \in \mathbb{R}^{4^n \times 4^n}$ . The corresponding inverse Hadamard transform is denoted by  $\boldsymbol{H}_n^{-1} = \frac{1}{4^n} \boldsymbol{H}$ . We omit the subscript n, whenever there is no confusion. Given these notations, according to (42), the simplified quasi-probability representation vector for a Pauli channel  $\mathcal C$  can be expressed as

$$\tilde{\boldsymbol{\mu}}_{\mathcal{C}} = \boldsymbol{H}^{-1}(1/\boldsymbol{c}). \tag{84}$$

Since the channel is CPTP, the vector 1/c satisfies  $[1/c]_1 = 1$ . Hence we define  $\zeta \in \mathbb{R}^{4^n-1}$  so that

$$1/c = 1 + [0 \zeta^{\mathrm{T}}]^{\mathrm{T}}.$$

For comparison, we consider a depolarizing channel  $\mathcal L$  having quasi-probability representation corresponding to  $\tilde{\pmb \mu}_{\mathcal L}$  given by

$$\tilde{\mu}_{\mathcal{L}} = H^{-1} (1 + [0 \ \bar{\zeta} \mathbf{1}^{\mathrm{T}}]^{\mathrm{T}}),$$
 (85)

where  $\bar{\zeta} \triangleq \frac{1}{4^n-1} \sum_{i=1}^{4^n-1} \zeta_i$ . One may verify that the channel  $\mathcal{L}$  characterized by (85) is a depolarizing channel with its GGEP satisfying

$$1 - \epsilon(\mathcal{L}) = \frac{1}{4^n} \left( 1 + \frac{4^n - 1}{1 + \overline{\epsilon}} \right). \tag{86}$$

<sup>6</sup>We say that an operator is complete positive if it maps positive semidefinite matrices to positive semidefinite matrices.



By contrast, the GGEP of the channel C satisfies

$$1 - \epsilon(\mathcal{C}) = \frac{1}{4^n} \mathbf{1}^{\mathrm{T}} \mathbf{c}$$

$$= \frac{1}{4^n} \left( 1 + \sum_{i=1}^{4^n - 1} \frac{1}{1 + \zeta_i} \right). \tag{87}$$

Since  $\zeta_i > -1$ ,  $\forall i$ , from (86) and (87) we have

$$\epsilon(\mathcal{L}) \ge \epsilon(\mathcal{C})$$
 (88)

due to the convexity of  $f(x) = (1+x)^{-1}$ , when x > -1.

Next we show that  $\|\tilde{\mu}_{\mathcal{C}}\|_1 \ge \|\tilde{\mu}_{\mathcal{L}}\|_1$ . Note that the vector  $\tilde{\mu}_{\mathcal{C}}$  can be decomposed as

$$\tilde{\boldsymbol{\mu}}_{\mathcal{C}} = \tilde{\boldsymbol{\mu}}_{\mathcal{L}} + \boldsymbol{H}^{-1} \boldsymbol{r},\tag{89}$$

where

$$r = 1/c - 1/l$$
  
=  $[0 (\boldsymbol{\zeta} - \bar{\boldsymbol{\zeta}} \mathbf{1})^{\mathrm{T}}]^{\mathrm{T}}$ .

From the definition of  $\bar{\zeta}$  we see that  $\mathbf{1}^T \mathbf{r} = 0$ , hence  $[\tilde{\boldsymbol{\mu}}_{\mathcal{C}}]_1 = [\tilde{\boldsymbol{\mu}}_{\mathcal{L}}]_1 \triangleq \mu_1$ . In addition, we have

$$\mathbf{1}^{\mathrm{T}}\tilde{\boldsymbol{\mu}}_{\mathcal{C}} = \mathbf{1}^{\mathrm{T}}\tilde{\boldsymbol{\mu}}_{\mathcal{L}} = [1/c]_{1} = 1, \tag{90}$$

since the channels are CPTP. Therefore we obtain

$$\mathbf{1}^{\mathrm{T}}[\tilde{\boldsymbol{\mu}}_{\mathcal{C}}]_{2:4^{n}} = \mathbf{1}^{\mathrm{T}}[\tilde{\boldsymbol{\mu}}_{\mathcal{L}}]_{2:4^{n}}.$$
 (91)

The 1-norm of  $\tilde{\mu}_{\mathcal{L}}$  can be calculated explicitly as

$$\|\tilde{\boldsymbol{\mu}}_{\mathcal{L}}\|_{1} = \mu_{1} - \sum_{i=2}^{4^{n}} [\tilde{\boldsymbol{\mu}}_{\mathcal{L}}]_{i}.$$
 (92)

For  $\tilde{\mu}_{\mathcal{C}}$ , we denote the sign of its *i*-th entry as  $s_i$ , thus

$$\|\tilde{\boldsymbol{\mu}}_{\mathcal{C}}\|_{1} = \mu_{1} + \sum_{i=2}^{4^{n}} s_{i} [\tilde{\boldsymbol{\mu}}_{\mathcal{C}}]_{i}$$

$$= \mu_{1} + \mathbf{1}_{+}^{T} [\tilde{\boldsymbol{\mu}}_{\mathcal{C}}]_{2:4^{n}} - \mathbf{1}_{-}^{T} [\tilde{\boldsymbol{\mu}}_{\mathcal{C}}]_{2:4^{n}}, \qquad (93)$$

where

$$[\mathbf{1}_{+}]_{i} = \begin{cases} 1, & s_{i-1} > 0; \\ 0, & s_{i-1} < 0. \end{cases} \text{ and } [\mathbf{1}_{-}]_{i} = \begin{cases} 1, & s_{i-1} < 0; \\ 0, & s_{i-1} > 0. \end{cases}$$

From (91) we have

$$\mathbf{1}_{+}^{T}[\tilde{\boldsymbol{\mu}}_{\mathcal{C}}]_{2:4^{n}} + \mathbf{1}_{-}^{T}[\tilde{\boldsymbol{\mu}}_{\mathcal{C}}]_{2:4^{n}} = \sum_{i=2}^{4^{n}} [\tilde{\boldsymbol{\mu}}_{\mathcal{L}}]_{i}.$$

Hence

$$\|\tilde{\boldsymbol{\mu}}_{\mathcal{C}}\|_{1} = \|\tilde{\boldsymbol{\mu}}_{\mathcal{L}}\|_{1} + 2\mathbf{1}_{+}^{T}[\tilde{\boldsymbol{\mu}}_{\mathcal{C}}]_{2:4^{n}}$$

$$\geq \|\tilde{\boldsymbol{\mu}}_{\mathcal{C}}\|_{1}.$$
(94)

Finally, since  $\epsilon(\mathcal{L}) \geq \epsilon(\mathcal{C})$ , we may construct a depolarizing channel  $\mathcal{L}'$  characterized by  $\epsilon(\mathcal{L}') = \epsilon(\mathcal{C})$ , while satisfying

$$\|\tilde{\boldsymbol{\mu}}_{\mathcal{L}}\|_1 \geq \|\tilde{\boldsymbol{\mu}}_{\mathcal{L}'}\|_1.$$

Hence the proof is completed.

#### **APPENDIX C**

### THE VALUES OF $\eta_{\mathcal{C}}$ FOR BASIC PAULI CHANNELS

To elaborate further on the intuition about  $\eta_{\mathcal{C}}$ , the values of  $\eta_{\mathcal{C}}$  corresponding to some single-qubit Pauli channels are listed in Table 2.

**TABLE 2.**  $\eta_{\mathcal{C}}$  values of single-qubit Pauli channels.

Channel	Parameters	$\eta_{\mathcal{C}}$
Depolarizing	Depolarizing prob. p	$[1-p, \frac{p}{3}, \frac{p}{3}, \frac{p}{3}]^{\mathrm{T}}$
Bit-flip	Bit-flip prob. $p_{\rm x}$	$[1 - p_{\rm x}, p_{\rm x}, 0, 0]^{\rm T}$
Phase-flip	Phase-flip prob. $p_{\rm z}$	$[1-p_{z}, 0, 0, p_{z}]^{\mathrm{T}}$

### **APPENDIX D**

### **PROOF OF PROPOSITION 3**

Proof: From (51), we have

$$\gamma_{\mathcal{C}} = \| \mathbf{C}^{-1} \mathbf{\alpha} \|_{1}^{2} - 1, \tag{95}$$

where

$$C = (1 - \epsilon)I + A. \tag{96}$$

Since the graph  $\mathscr{G}$  is symmetric, each column (resp. row) of C can be obtained by permuting the first column (resp. row) of C. Thus we have

$$\gamma_C = \|\boldsymbol{C}^{-1}\|_1^2 - 1. \tag{97}$$

From (96) we can obtain

$$\mathbf{C}^{-1} = ((1 - \epsilon)\mathbf{I} + \epsilon \tilde{\mathbf{A}})^{-1}$$
 (98a)

$$= \frac{1}{1 - \epsilon} \left( I + \frac{\epsilon}{1 - \epsilon} \tilde{A} \right)^{-1} \tag{98b}$$

$$= \frac{1}{1 - \epsilon} \sum_{n=0}^{\infty} (-1)^n \left( \frac{\epsilon}{1 - \epsilon} \right)^n \tilde{A}^n, \qquad (98c)$$

where  $\tilde{A} \triangleq \epsilon^{-1}A$ , and (98c) is obtained using the matrix inversion lemma. Exploiting the sub-multiplicativity of matrix *p*-norms [64, Chap. 5], we have

$$\|\tilde{\boldsymbol{A}}^n\|_1 \le \|\tilde{\boldsymbol{A}}\|_1^n = 1, \tag{99}$$

where the equality follows from the fact that  $\mathbf{1}^{T}\tilde{A} = 1$  and that all entries in  $\tilde{A}$  are non-negative. Substituting (99) into (98), we have

$$\|\boldsymbol{C}^{-1}\|_{1} \leq \frac{1}{1-\epsilon} \sum_{n=0}^{\infty} \left\| (-1)^{n} \left( \frac{\epsilon}{1-\epsilon} \right)^{n} \tilde{\boldsymbol{A}}^{n} \right\|_{1}$$
 (100a)

$$\leq \frac{1}{1-\epsilon} \sum_{n=0}^{\infty} \left(\frac{\epsilon}{1-\epsilon}\right)^n = \frac{1}{1-2\epsilon}.$$
 (100b)

Therefore, from (97) we obtain

$$\gamma_{\mathcal{C}} \le \left(\frac{1}{1 - 2\epsilon}\right)^2 - 1 = 4\epsilon \cdot \frac{1 - \epsilon}{(1 - 2\epsilon)^2}.$$
(101)

To show that channels having a single type of error achieve the equality, we note that

$$\mathcal{P}_i^{2n} = \mathcal{I}, \ \mathcal{P}_i^{2n+1} = \mathcal{P}_i \tag{102}$$



holds for any Pauli operator  $\mathcal{P}_i$ . In light of this, the inverse of their PRW matrix can be shown to satisfy

$$C^{-1} = \frac{1}{1 - \epsilon} \sum_{n=0}^{\infty} \left\{ \left( \frac{\epsilon}{1 - \epsilon} \right)^{2n} I - \left( \frac{\epsilon}{1 - \epsilon} \right)^{2n+1} \tilde{A} \right\}.$$

Therefore we have

$$\|C^{-1}\|_{1} = \frac{1}{1 - \epsilon} \sum_{n=0}^{\infty} \left\{ \left( \frac{\epsilon}{1 - \epsilon} \right)^{2n} + \left( \frac{\epsilon}{1 - \epsilon} \right)^{2n+1} \|\tilde{A}\|_{1} \right\}$$
$$= \frac{1}{1 - \epsilon} \sum_{n=0}^{\infty} \left( \frac{\epsilon}{1 - \epsilon} \right)^{n},$$

which is identical to (100b). Hence the proof is completed.

### APPENDIX E PROOF OF PROPOSITION 4

*Proof:* Let  $\eta$  be the probability vector of a Pauli channel. We first show that the function

$$f(\boldsymbol{\eta}) = \left\| \boldsymbol{H}_{1}^{-1} \left( 1/(\boldsymbol{H}_{1}[1 - \epsilon \ \boldsymbol{\eta}^{\mathrm{T}}]^{\mathrm{T}}) \right) \right\|_{1}$$
 (103)

is Schur-convex with respect to  $\eta$ . We proceed by first decomposing  $f(\eta)$  as

$$f(\eta) = g\{h_1[h_2(\eta)]\},\tag{104}$$

where

$$g(\mathbf{x}) = \|\mathbf{H}_{1}^{-1}[1\,\mathbf{x}^{\mathrm{T}}]^{\mathrm{T}}\|_{1} \quad (\mathbf{x} \succeq \mathbf{1}),$$
 (105a)

$$h_1(x) = 1/x \ (0 \le x \le 1),$$
 (105b)

$$h_2(\boldsymbol{\eta}) = \boldsymbol{H}_1[1 - \epsilon \ \boldsymbol{\eta}^{\mathrm{T}}]^{\mathrm{T}} \ \left( \mathbf{0} \le \boldsymbol{\eta} \le \frac{\epsilon}{3} \mathbf{1} \right).$$
 (105c)

Since  $h_2(\eta)$  is an affine function of  $\eta$ , we see that

$$h(\boldsymbol{\eta}) = h_1[h_2(\boldsymbol{\eta})]$$
  
= 1/\left(\mathbf{H}\_1[1 - \epsilon \beta^T]^T\right) (106)

is element-wise convex with respect to  $\eta$ . Therefore, to show that  $f(\eta)$  is Schur-convex, it suffices to show that g(x) is Schur-convex and increasing for x satisfying  $x \ge 1$ .

Next we show the Schur-convexity of g(x). Note that

$$g(\mathbf{x}) = \left\| \mathbf{H}_{1}^{-1} [1 \ \mathbf{x}^{\mathrm{T}}]^{\mathrm{T}} \right\|_{\mathbf{x}} \tag{107a}$$

$$= 1 + \mathbf{1}^{\mathrm{T}}(x - 1) + \frac{1}{4} \left\| \tilde{\boldsymbol{H}}_{1}(x - 1) \right\|_{1}, \quad (107b)$$

where

$$\tilde{\boldsymbol{H}}_1 = \begin{bmatrix} -1 & 1 & -1 \\ 1 & -1 & -1 \\ -1 & -1 & 1 \end{bmatrix} \tag{108}$$

is obtained by removing the first row and the first column from  $H_1$ . Since doubly stochastic transformations do not affect the term  $\mathbf{1}^{\mathrm{T}}(x-1)$ , the problem is reduced to showing the Schur-convexity of  $\|\tilde{H}_1x\|_1$  for  $x \geq 0$ . To facilitate the

analysis, we utilize  $\epsilon = \mathbf{1}^{T} x$  and define  $x = [x_1 \ x_2 \ x_3]^{T}$ . Now we see that

$$\tilde{\boldsymbol{H}}_{1}\boldsymbol{x} = [2x_2 - \epsilon \ 2x_1 - \epsilon \ 2x_3 - \epsilon]^{\mathrm{T}},\tag{109}$$

hence

$$\|\tilde{\boldsymbol{H}}_1 \boldsymbol{x}\|_1 = \|2\boldsymbol{x} - \epsilon\|_1. \tag{110}$$

For fixed  $\epsilon$ ,  $||2x - \epsilon||_1$  is convex with respect to x. In addition, it is also a symmetric function of x, meaning that its value is unchanged upon permutation of x. Therefore, g(x) is Schur-convex.

To show that g(x) is increasing, we calculate the gradient of g(x) as

$$\nabla_{\mathbf{x}}g(\mathbf{x}) = \frac{\partial}{\partial \mathbf{x}} \left( \mathbf{1}^{\mathrm{T}}(\mathbf{x} - 1) + \|\tilde{\mathbf{H}}_{1}(\mathbf{x} - 1)\|_{1} \right)$$
$$= \mathbf{1} + \tilde{\mathbf{H}}_{1}\operatorname{sgn}(\tilde{\mathbf{H}}_{1}(\mathbf{x} - 1)) \tag{111}$$

where  $sgn(\cdot)$  is the sign function satisfying

$$[\operatorname{sgn}(\mathbf{x})]_i = \begin{cases} -1, & x_i < 0; \\ 1, & x_i > 0; \\ 0, & x_i = 0. \end{cases}$$

After some manipulation, one can verify that  $\tilde{\boldsymbol{H}}_1 \operatorname{sgn}(\tilde{\boldsymbol{H}}_1(\boldsymbol{x}-1)) \succeq -1$  according to (108), hence  $g(\boldsymbol{x})$  is increasing.

Given that the SOF of single-qubit Pauli channels is Schurconvex, we may generalize the result to *n*-qubit memoryless Pauli channels. To elaborate, the PRW matrix of an *n*-qubit memoryless Pauli channel can be expressed as

$$C(\eta_{\mathcal{C}}) = \bigotimes_{i=1}^{n} C_i(\eta_{\mathcal{C}_i}), \tag{112}$$

where  $C_i(\eta_{C_i})$  corresponds to the partial channel of the *i*-th qubit. Thus

$$\left\| \mathbf{C}^{-1}(\boldsymbol{\eta}_{\mathcal{C}}) \right\|_{1} = \prod_{i=1}^{n} \| \mathbf{C}_{i}^{-1}(\boldsymbol{\eta}_{\mathcal{C}_{i}}) \|_{1}. \tag{113}$$

Since  $\|C_i^{-1}(\eta_{C_i})\|_1$  is Schur-convex with respect to the corresponding probability vector  $\eta_{C_i}$ , we have

$$\|\boldsymbol{C}_{i}^{-1}(\boldsymbol{\eta}_{C_{i}})\|_{1} \ge \|\boldsymbol{C}_{i}^{-1}(\boldsymbol{Q}_{i}\boldsymbol{\eta}_{C_{i}})\|_{1}$$
 (114)

for any doubly stochastic matrix  $Q_i$ . Therefore

$$\left\| \boldsymbol{C}^{-1}(\boldsymbol{\eta}_{\mathcal{C}}) \right\|_{1} \ge \prod_{i=1}^{n} \left\| \boldsymbol{C}_{i}^{-1}(\boldsymbol{Q}_{i}\boldsymbol{\eta}_{\mathcal{C}_{i}}) \right\|_{1}. \tag{115}$$

Hence the proof is completed.

#### REFERENCES

- [1] C. E. Leiserson, R. L. Rivest, T. H. Cormen, and C. Stein, *Introduction to Algorithms*, vol. 6. Cambridge, MA, USA: MIT Press, 2001.
- [2] F. Arute et al., "Quantum supremacy using a programmable superconducting processor," Nature, vol. 574, no. 7779, pp. 505–510, Oct. 2019.
- [3] J. Preskill, "Quantum computing in the NISQ era and beyond," Quantum, vol. 2, pp. 1–21, Aug. 2018.
- [4] D. Aharonov and M. Ben-Or, "Fault-tolerant quantum computation with constant error rate," SIAM J. Comput., vol. 38, no. 4, pp. 1207–1282, Jan. 2008.



- [5] A. R. Calderbank, E. M. Rains, P. M. Shor, and N. J. A. Sloane, "Quantum error correction via codes over GF(4)," *IEEE Trans. Inf. Theory*, vol. 44, pp. 1369–1387, Jul. 1998.
- [6] C. H. Bennett and P. W. Shor, "Quantum information theory," *IEEE Trans. Inf. Theory*, vol. 44, no. 6, pp. 2724–2742, Oct. 1998.
- [7] Z. Babar, D. Chandra, H. V. Nguyen, P. Botsinis, D. Alanis, S. X. Ng, and L. Hanzo, "Duality of quantum and classical error correction codes: Design principles and examples," *IEEE Commun. Surveys Tuts.*, vol. 21, no. 1, pp. 970–1010, 1st Quart., 2019.
- [8] D. Chandra, Z. Babar, H. V. Nguyen, D. Alanis, P. Botsinis, S. X. Ng, and L. Hanzo, "Quantum topological error correction codes: The classical-to-quantum isomorphism perspective," *IEEE Access*, vol. 6, pp. 13729–13757, 2018.
- [9] E. Knill and R. Laflamme, "Concatenated quantum codes," 1996, arXiv:quant-ph/9608012. [Online]. Available: https://arxiv.org/abs/quant-ph/9608012
- [10] D. Poulin, J.-P. Tillich, and H. Ollivier, "Quantum serial turbo codes," IEEE Trans. Inf. Theory, vol. 55, no. 6, pp. 2776–2798, Jun. 2009.
- [11] Z. Babar, S. X. Ng, and L. Hanzo, "EXIT-chart-aided near-capacity quantum turbo code design," *IEEE Trans. Veh. Technol.*, vol. 64, no. 3, pp. 866–875, Mar. 2015.
- [12] P. W. Shor, "Algorithms for quantum computation: Discrete logarithms and factoring," in *Proc. 35th Annu. Symp. Found. Comput. Sci.*, Santa Fe, NM, USA, Nov. 1994, pp. 124–134.
- [13] L. K. Grover, "A fast quantum mechanical algorithm for database search," in *Proc. 28th Annu. ACM Symp. Theory Comput. (STOC)*, Philadelphia, PA, USA, May 1996, pp. 212–219.
- [14] E. Farhi, J. Goldstone, and S. Gutmann, "A quantum approximate optimization algorithm," 2014, arXiv:1411.4028. [Online]. Available: http://arxiv.org/abs/1411.4028
- [15] S. Hadfield, Z. Wang, B. O'Gorman, E. Rieffel, D. Venturelli, and R. Biswas, "From the quantum approximate optimization algorithm to a quantum alternating operator ansatz," *Algorithms*, vol. 12, pp. 1–45, Feb. 2019.
- [16] A. Peruzzo, J. McClean, P. Shadbolt, M.-H. Yung, X.-Q. Zhou, P. J. Love, A. Aspuru-Guzik, and J. L. O'Brien, "A variational eigenvalue solver on a photonic quantum processor," *Nature Commun.*, vol. 5, no. 1, pp. 1–7, Jul. 2014.
- [17] S. Jin, S. Wu, G. Zhou, Y. Li, L. Li, B. Li, and X. Wang, "A query-based quantum eigensolver," *Quantum Eng.*, vol. 2, no. 3, p. e49, Sep. 2020.
- [18] S. Wei, H. Li, and G. Long, "A full quantum eigensolver for quantum chemistry simulations," *Research*, vol. 2020, Mar. 2020, Art. no. 1486935.
- [19] Y. Zhang and Q. Ni, "Recent advances in quantum machine learning," Quantum Eng., vol. 2, no. 1, p. e34, Mar. 2020.
- [20] F. Hu, B. Wang, N. Wang, and C. Wang, "Quantum machine learning with D-wave quantum computer," *Quantum Eng.*, vol. 1, no. 2, p. e12, Jun. 2019.
- [21] T. Matsumine, T. Koike-Akino, and Y. Wang, "Channel decoding with quantum approximate optimization algorithm," in *Proc. IEEE Int. Symp. Inf. Theory (ISIT)*, Paris, France, Jul. 2019, pp. 2574–2578.
- [22] J. I. Colless, V. V. Ramasesh, D. Dahlen, M. S. Blok, M. E. Kimchi-Schwartz, J. R. McClean, J. Carter, W. A. de Jong, and I. Siddiqi, "Computation of molecular spectra on a quantum processor with an error-resilient algorithm," *Phys. Rev. X*, vol. 8, no. 1, Feb. 2018.
- [23] X. Bonet-Monroig, R. Sagastizabal, M. Singh, and T. E. O'Brien, "Low-cost error mitigation by symmetry verification," *Phys. Rev. A, Gen. Phys.*, vol. 98, no. 6, pp. 1–10, Dec. 2018.
- [24] K. Temme, S. Bravyi, and J. M. Gambetta, "Error mitigation for short-depth quantum circuits," *Phys. Rev. Lett.*, vol. 119, no. 18, pp. 1–5, Nov. 2017.
- [25] S. Endo, S. C. Benjamin, and Y. Li, "Practical quantum error mitigation for near-future applications," *Phys. Rev. X*, vol. 8, no. 3, pp. 1–21, Jul. 2018.
- [26] C. Song, J. Cui, H. Wang, J. Hao, H. Feng, and Y. Li, "Quantum computation with universal error mitigation on a superconducting quantum processor," *Sci. Adv.*, vol. 5, no. 9, Sep. 2019, Art. no. eaaw5686.
- [27] J. M. Chow, J. M. Gambetta, A. D. Córcoles, S. T. Merkel, J. A. Smolin, C. Rigetti, S. Poletto, G. A. Keefe, M. B. Rothwell, J. R. Rozen, M. B. Ketchen, and M. Steffen, "Universal quantum gate set approaching fault-tolerant thresholds with superconducting qubits," *Phys. Rev. Lett.*, vol. 109, no. 6, pp. 1–5, Aug. 2012.
- [28] M. A. Nielsen and I. L. Chuang, Quantum Computation and Quantum Information, 2nd ed. New York, NY, USA: Cambridge Univ. Press, 2011.

- [29] M. A. Nielsen, "A simple formula for the average gate fidelity of a quantum dynamical operation," *Phys. Lett. A*, vol. 303, no. 4, pp. 249–252, Oct. 2002.
- [30] A. W. Harrow, A. Hassidim, and S. Lloyd, "Quantum algorithm for linear systems of equations," *Phys. Rev. Lett.*, vol. 103, no. 15, Oct. 2009, Art. no. 150502.
- [31] G. H. Low and I. L. Chuang, "Optimal Hamiltonian simulation by quantum signal processing," *Phys. Rev. Lett.*, vol. 118, no. 1, Jan. 2017, Art. no. 010501.
- [32] S.-J. Wei, Z.-R. Zhou, D. Ruan, and G.-L. Long, "Realization of the algorithm for system of linear equations in duality quantum computing," in *Proc. IEEE 85th Veh. Technol. Conf. (VTC Spring)*, Sydney, NSW, Australia, Jun. 2017, pp. 1–4.
- [33] C. Shao, Y. Li, and H. Li, "Quantum algorithm design: Techniques and applications," J. Syst. Sci. Complex., vol. 32, no. 1, pp. 375–452, Feb. 2019.
- [34] J. R. McClean, J. Romero, R. Babbush, and A. Aspuru-Guzik, "The theory of variational hybrid quantum-classical algorithms," *New J. Phys.*, vol. 18, pp. 1–22, Feb. 2016.
- [35] O. Higgott, D. Wang, and S. Brierley, "Variational quantum computation of excited states," *Quantum*, vol. 3, p. 156, Jul. 2019.
- [36] T. Jones, S. Endo, S. McArdle, X. Yuan, and S. C. Benjamin, "Variational quantum algorithms for discovering Hamiltonian spectra," *Phys. Rev. A, Gen. Phys.*, vol. 99, no. 6, Jun. 2019, Art. no. 062304.
- [37] N. Moll, P. Barkoutsos, and L. S. Bishop, "Quantum optimization using variational algorithms on near-term quantum devices," *Quantum Sci. Tech*nol., vol. 3, pp. 1–17, Jun. 2018.
- [38] S. McArdle, S. Endo, A. Aspuru-Guzik, S. Benjamin, and X. Yuan, "Quantum computational chemistry," 2018, *arXiv:1808.10402*. [Online]. Available: http://arxiv.org/abs/1808.10402
- [39] J. A. Nelder and R. Mead, "A simplex method for function minimization," Comput. J., vol. 7, no. 4, pp. 308–313, Jan. 1965.
- [40] P. Botsinis, D. Alanis, Z. Babar, H. V. Nguyen, D. Chandra, S. X. Ng, and L. Hanzo, "Quantum search algorithms for wireless communications," *IEEE Commun. Surveys Tuts.*, vol. 21, no. 2, pp. 1209–1242, 2nd Quart., 2019.
- [41] M. Mohseni, A. T. Rezakhani, and D. A. Lidar, "Quantum-process tomography: Resource analysis of different strategies," *Phys. Rev. A, Gen. Phys.*, vol. 77, no. 3, pp. 1–15, Mar. 2008.
- [42] Z. Cai and S. Benjamin, "Constructing smaller pauli twirling sets for arbitrary error channels," 2018, arXiv:1807.04973. [Online]. Available: http://arxiv.org/abs/1807.04973
- [43] C. Dankert, R. Cleve, J. Emerson, and E. Livine, "Exact and approximate unitary 2-designs and their application to fidelity estimation," *Phys. Rev.* A, Gen. Phys., vol. 80, no. 1, Jul. 2009, Art. no. 012304.
- [44] E. Knill, R. Laflamme, and W. H. Zurek, "Resilient quantum computation: Error models and thresholds," *Proc. Roy. Soc. London A, Math., Phys. Eng. Sci.*, vol. 454, no. 1969, pp. 365–384, Jan. 1998.
- [45] A. S. Fletcher, P. W. Shor, and M. Z. Win, "Channel-adapted quantum error correction for the amplitude damping channel," *IEEE Trans. Inf. Theory*, vol. 54, no. 12, pp. 5705–5718, Dec. 2008.
- [46] D. Greenbaum, "Introduction to quantum gate set tomography," 2015, arXiv:1509.02921. [Online]. Available: http://arxiv.org/abs/1509.02921
- [47] A. Carignan-Dugas, M. Alexander, and J. Emerson, "A polar decomposition for quantum channels (with applications to bounding error propagation in quantum circuits)," *Quantum*, vol. 3, p. 173, Aug. 2019.
- [48] F. R. Chung and F. C. Graham, Spectral Graph Theory, no. 92. Providence, RI, USA: American Mathematical Society, 1997.
- [49] D. J. C. MacKay, G. Mitchison, and P. L. McFadden, "Sparse-graph codes for quantum error correction," *IEEE Trans. Inf. Theory*, vol. 50, no. 10, pp. 2315–2330, Oct. 2004.
- [50] M. M. Wilde, M.-H. Hsieh, and Z. Babar, "Entanglement-assisted quantum turbo codes," *IEEE Trans. Inf. Theory*, vol. 60, no. 2, pp. 1203–1222, Feb. 2014.
- [51] D. Chandra, Z. Babar, S. X. Ng, and L. Hanzo, "Near-hashing-bound multiple-rate quantum turbo short-block codes," *IEEE Access*, vol. 7, pp. 52712–52730, 2019.
- [52] D. P. Palomar and Y. Jiang, MIMO Transceiver Design Via Majorization Theory. Boston, MA, USA: Now, 2007.
- [53] T. Ando, "Majorization, doubly stochastic matrices, and comparison of eigenvalues," *Linear Algebra Appl.*, vol. 118, pp. 163–248, Jun. 1989.
- [54] D. P. DiVincenzo, D. W. Leung, and B. M. Terhal, "Quantum data hiding," IEEE Trans. Inf. Theory, vol. 48, no. 3, pp. 580–598, Mar. 2002.



- [55] T. Can, N. Rengaswamy, R. Calderbank, and H. D. Pfister, "Kerdock codes determine unitary 2-designs," in *Proc. IEEE Int. Symp. Inf. Theory (ISIT)*, Paris, France, Jul. 2019, pp. 2908–2912.
- [56] C. J. Ballance, T. P. Harty, N. M. Linke, M. A. Sepiol, and D. M. Lucas, "High-fidelity quantum logic gates using trapped-ion hyperfine qubits," *Phys. Rev. Lett.*, vol. 117, no. 6, pp. 1–6, Aug. 2016.
- [57] S. Bravyi and A. Kitaev, "Universal quantum computation with ideal clifford gates and noisy ancillas," *Phys. Rev. A, Gen. Phys.*, vol. 71, no. 2, Feb. 2005, Art. no. 022316.
- [58] K. Fujii, "Noise threshold of quantum supremacy," 2016, arXiv:1610.03632. [Online]. Available: http://arxiv.org/abs/1610.03632
- [59] D. Chandra, Z. Babar, H. V. Nguyen, D. Alanis, P. Botsinis, S. X. Ng, and L. Hanzo, "Quantum topological error correction codes are capable of improving the performance of clifford gates," *IEEE Access*, vol. 7, pp. 121501–121529, 2019.
- [60] E. Knill, "Fault-tolerant postselected quantum computation: Schemes," 2004, arXiv:quant-ph/0402171. [Online]. Available: https://arxiv.org/abs/quant-ph/0402171
- [61] E. Knill, "Fault-tolerant postselected quantum computation: Threshold analysis," 2004, arXiv:quant-ph/0404104. [Online]. Available: https://arxiv.org/abs/quant-ph/0404104
- [62] N. M. Linke, M. Gutierrez, K. A. Landsman, C. Figgatt, S. Debnath, K. R. Brown, and C. Monroe, "Fault-tolerant quantum error detection," *Sci. Adv.*, vol. 3, Oct. 2017, Art. no. e1701074.
- [63] G. H. Golub and C. F. Van Loan, *Matrix Computations*, 3rd ed. Baltimore, MD, USA: Johns Hopkins Univ. Press, 1996.
- [64] R. A. Horn and C. R. Johnson, *Matrix Analysis*, 2nd ed. Cambridge, U.K.: Cambridge Univ. Press, 2012.



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