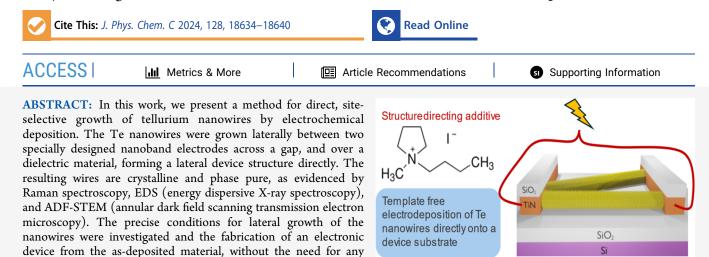


pubs.acs.org/JPCC

Article

# Toward a Procedure for the Template Free Growth of Te Nanowires Across an Insulator by Electrodeposition

Alexander W. Black, Nema M. Abdelazim,\* Yasir J. Noori, Yisong Han, Nikolay Zhelev, Shibin Thomas, Wenjian Zhang, Gillian Reid, Richard Beanland, C. H. Kees de Groot, and Philip N. Bartlett\*



# 1. INTRODUCTION

Nanowires, typically defined as high aspect ratio structures with diameters between 1 and 200 nm, are unique physical systems that have been extensively studied because they exhibit properties such as one-dimensional quantum confinement of carriers and large surface-to-volume ratios.<sup>1,2</sup> In addition, semiconductor industries have recently been exploring the possibility of using nanowires in gate-all-around transistor architectures for future electronic technologies.<sup>3,4</sup> Despite these advances in nanowires, conventional techniques for their growth *via* methods such as molecular beam epitaxy (MBE) or metal–organic chemical vapor deposition (MOCVD) still lack control over the precise growth location. Template-based techniques to grow nanowires have emerged to tackle this issue but these typically require extra fabrication steps to pattern the template.<sup>5,6</sup>

transfer process or further contact fabrication, is demonstrated.

On the other hand, elemental tellurium has attracted significant interest for applications in p-type field-effect transistors and mid-infrared photodetectors on both rigid and flexible substrates.<sup>7,8</sup> It is an unusual element, as it is semiconducting and possesses a highly anisotropic crystal structure, a combination that gives rise to several interesting electronic properties.<sup>9,10</sup> At room temperature and pressure, crystalline Te takes the form of covalently bonded helices with van der Waals forces between the chains of Te, a structure described as trigonal (t-Te).

In addition to the applications of bulk Te, the growth of tellurium nanowires is of high importance due to its potential in p-type gate-all-around architecture transistors and flexible mid-infrared photodetection.<sup>7,10–14</sup> Work on the synthesis and

properties of 2D and 1D forms of Te, consisting of a monolayer of Te and an individual helix, respectively, has also begun to emerge recently.<sup>15-17</sup>

Of the methods used to produce bulk Te and Te nanowires (Te NWs), electrochemical deposition (electrodeposition) has proven to be popular because of its simplicity, flexibility, and scalability. Electrodeposition is a "bottom up" approach, meaning that material growth starts from the electrode surface and proceeds outward, a feature that becomes particularly useful when producing materials with high aspect ratios. In previous works, we have used electrodeposition to demonstrate phase change memory, memristor and thermoelectric devices using bulk materials involving binary and ternary telluride alloys.<sup>18–21</sup>

Several reports of templated Te NW electrodeposition can be found in the literature, including deposition from aqueous electrolytes,<sup>22–24</sup> and supercritical difluoromethane  $(CH_2F_2)^{25}$ into anodic alumina and track etch membrane templates. Achieving consistent nucleation and pore filling within the template can be a problem and so an approach for the fabrication of Te NWs that does not require a hard template is an attractive target. She *et al.* have reported template-free electrodeposition of Te NWs from TeO<sub>2</sub> in 1 M KOH at 85

Received:September 1, 2024Revised:October 9, 2024Accepted:October 11, 2024Published:October 22, 2024





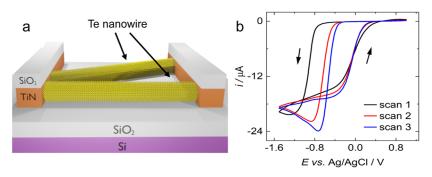


Figure 1. (a) Schematic showing the device structure for electrodeposited Te nanowires. (b) voltammograms of 0.75 mM  $[N^nBu_4]_2[TeCl_6]$  and 100 mM BMP-I at a lateral growth electrode in CH<sub>2</sub>Cl<sub>2</sub>. Scan swept from 0.5 V *vs.* Ag/AgCl at 50 mV s<sup>-1</sup> in the direction indicated by the arrows. CE: Pt mesh, RE: Ag/AgCl.

 $^{\circ}$ C,<sup>26</sup> and recently the template-free electrodeposition of Te NWs has been demonstrated from ionic liquids, including BMP-TFSI (1-butyl-1-methylpyrrolidinium bis-(trifluoromethylsulfonyl)imide) containing SiCl<sub>4</sub>,<sup>27</sup> EOPipTF-SI/EOPipBr (1-ethyl-1-octylpiperidinium bis-(trifluoromethylsulfonyl)imide/bromide mixture),<sup>28</sup> and 12CE-Cl (choline chloride/ethylene glycol deep eutectic solvent) at temperatures between 40 and 100 °C.<sup>29</sup> In all the above cases, further steps such as template dissolution and/or dispersion of the NWs in a solvent would be required for device fabrication, losing one of the inherent advantages of electrodeposition over chemical Te NW preparation.

In the present work, a method is presented for the direct growth of Te NWs across a SiO<sub>2</sub> insulator between two nanoband electrodes, without the use of a template, by electrodeposition from dichloromethane (CH<sub>2</sub>Cl<sub>2</sub>) at room temperature. The nanowires are directly integrated with electrodes by electrodeposition so that no post-growth fabrication of contacts is required. This distinguishes our work from the previous reports described above and also other solution based methods of Te nanowire preparation, 13,16,30 where typically the material is prepared separately and requires further processing such as transfer or lithography before device measurements are possible. The necessary conditions for successful NW electrodeposition are presented and the effect of the various experimental factors are discussed. The appearance and physical properties of the samples are studied using scanning electron microscopy (SEM), atomic force microscopy (AFM), annular dark field scanning transmission electron microscopy (ADF-STEM), energy dispersive X-ray spectroscopy (EDS) and Raman spectroscopy. Finally, the IV characterization of the resultant nanowire devices on the substrate is demonstrated.

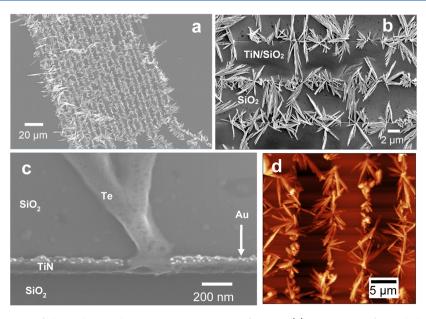
#### 2. EXPERIMENTAL

**2.1. Chemicals.** Dichloromethane,  $CH_2Cl_2$  (95%, Sigma-Aldrich) was dried and degassed by refluxing with  $CaH_2$  under a dinitrogen atmosphere followed by distillation and were stored in an inert atmosphere of N<sub>2</sub>. The water content was measured with Karl–Fischer titration (KF 899 Coulometer, Metrohm, UK). There was less than 30 ppm of water in all solvents. Tetrabutylammonium chloride, [N<sup>n</sup>Bu<sub>4</sub>]Cl (Sigma-Aldrich, >99%) and 1-butyl-1-methylpyrrolidinium iodide (BMP-I) (Sigma-Aldrich, >98%) were dried by heating at 100 °C under vacuum for several hours. Tetrabutylammonium hexachlorotellurate (IV) was prepared using a method previously described in the literature.<sup>31</sup> All solvents and reagents were stored in a dry  $N_2$  purged glovebox. Aqueous gold plating was performed using a commercial gold plating bath ("high speed gold", Spa Plating, UK) without alteration.

2.2. Electrodes. The working electrodes were formed by fabricating TiN into micropatterned electrodes. The TiN electrodes were fabricated such that they are capped with a SiO<sub>2</sub> layer to allow the material to grow laterally out from the edges of the electrodes, rather than over the top of them. To fabricate the lateral growth electrodes a photoresist was first spin-coated over a Si/SiO<sub>2</sub> substrate and the desired pattern of the electrodes was transferred onto the photoresist by photolithography. 100 and 50 nm thick layers of TiN and SiO<sub>2</sub> respectively were then sequentially sputtered and a lift-off process was used to form the final electrode structure.<sup>32</sup> Each microelectrode was connected to a large global electrode at the top of the chip through which the electrochemical current is supplied. The TiN global electrode which provided electrical contact was capped with a 190 nm evaporated Au layer to reduce the contact resistance to the electrochemical setup. The chip has been described previously, with illustration, in ref<sup>32</sup>.

2.3. Electrochemistry. All glassware was cleaned by soaking in Decon 90 (Decon Laboratories Ltd., UK) for at least 24 h, followed by rinsing with ultrapure water, 0.055  $\mu$ S  $cm^{-1}$  and then dried in an oven for a further 24 h. A Pt/Ir (90/ 10) disc was used as the counter electrode (CE) and the reference electrode (RE) was a homemade Ag/AgCl electrode stored in a 100 mM solution of [N<sup>n</sup>Bu<sub>4</sub>]Cl in CH<sub>2</sub>Cl<sub>2</sub>, separated from the electrolyte by a porous glass frit. Experiments were performed in a glovebox (Belle Technology, UK) under an inert atmosphere of  $N_2$  in the presence of <5ppm O<sub>2</sub> and H<sub>2</sub>O. A "front seal holder" arrangement was used to define the area of the working electrode, as described previously.<sup>33</sup> Measurements were performed with a PGSTAT  $\mu$ III (Metrohm Autolab, UK) potentiostat. Data were recorded with NOVA 1.11 (Metrohm Autolab, UK). The ambient temperature in the glovebox was monitored using a digital thermometer to an accuracy of  $\pm 0.05$  °C (Hama, UK). Au pretreatment of the TiN electrode walls was performed in the same "front seal holder", CE: Pt mesh, RE: saturated calomel electrode (SCE). A potential of -1.3 V vs. SCE was applied until a charge of 50  $\mu$ C was passed.

**2.4. Instrumentation.** Scanning electron microscopy (SEM) was performed with ZEISS Sigma 500 VP FESEM. An Oxford instruments Ultim Max 170 mm<sup>2</sup> was used for energy dispersive X-ray spectroscopy (EDS) and a WITec Raman RISE microscope for Raman spectroscopy. Raman spectroscopy was performed using a 532 nm laser at a power of 1 mW for 10 accumulations of 2 s. Polarized light microscopy



**Figure 2.** SEM and AFM images of electrodeposited Te NWs at various magnifications. (a) SEM image of the whole electrode array, (b) Inlens detector SEM image of a single channel within the array, (c) SEM image of an individual Te NW with pretreated Au nanoparticles visible, (d) AFM image of a single channel.

was performed using a Nikon LV100ND (Nikon Metrology, UK) in bright field mode with episcopic illumination coupled with a polarizer and an analyzer. Images were captured using NIS elements software (Nikon Instruments, UK). Cross sectional TEM specimens were made by a focused ion beam (FIB) using a Tescan Amber FIB/SEM. Annular dark field images were taken from the Te nanowires using a JEOL ARM200f transmission electron microscope, operated at 200 kV. The current–voltage characteristics were measured at room-temperature using a semiconductor device analyzer (MSA-400).

# 3. RESULTS AND DISCUSSION

Figure 1a shows an illustration of the direct lateral growth of Te nanowires over a silica insulator by electrodeposition from the nanoband electrodes. This approach permits the electronic characterization of electrodeposited NWs directly on the substrate without need for any further modification or processing. The geometry of the substrate was exploited here to encourage Te to grow as nanowires from one electrode to the other. This also means that, in the context of an electronic device, the Te grows between two terminals. A similar electrode structure has previously been employed for lateral (in-plane) growth of a 2D material across an insulator, with minimal vertical (interplane) growth.<sup>32</sup>

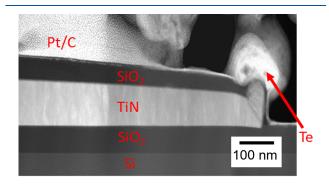
(Figure 1b) shows a typical voltammogram at the TiN nanoband electrodes in an electrolyte containing 0.75 mM  $[N^nBu_4]_2[TeCl_6]$  and 100 mM BMP-I in CH<sub>2</sub>Cl<sub>2</sub>. Scanning cathodically, a decrease in current can be observed at ca. -0.8 V vs Ag/AgCl associated with the electrodeposition of Te from  $[TeCl_6]^{2-}$ . On the reverse sweep, an apparent nucleation loop is present, but no stripping of the deposited Te appears to occur. With subsequent scans the onset of deposition takes place at higher potentials because of easier nucleation on an electrode where Te is already present. The electrochemistry of  $[N^nBu_4]_2[TeCl_6]$  in CH<sub>2</sub>Cl<sub>2</sub> at a planar TiN WE has been reported previously with a  $[N^nBu_4]Cl$  supporting electrolyte.<sup>31,34</sup> The voltammograms display similar features to those

in (Figure 1b) with BMP-I, including comparable onset potentials, suggesting that the use of BMP-I and the nanoband electrode do not significantly affect the electrochemistry of  $Te^{4+}$ .

In order to achieve adequate Te NW growth, an initial pretreatment was required. First, a small amount of gold was electrodeposited on the electrode surface from a commercial aqueous Au plating bath. The purpose of this was to provide small Au sites where Te nucleation is more favorable than from TiN itself, and therefore encouraging the formation of nanowires rather than a film. It was also found to improve the homogeneity of nanowire growth across the electrode. As can be seen in (Figure 2c), the gold particles appear randomly distributed across the walls of the electrode and are ca. 5 nm in diameter. Second, a 2 s nucleation pulse at -1.75 V vs Ag/ AgCl was used to initiate Te deposition. Due to the large cathodic overpotential of the pulse, the first step helps the Te to instantaneously nucleate on the substrate, and would also be expected to improve the uniformity and size distribution of the nanowires in the subsequent growth step, where no further nucleation takes place because of the lower applied overpotential.35

Figure 2 shows representative SEM and AFM images of Te NWs deposited onto the lateral growth substrate from an electrolyte containing 0.75 mM  $[N^nBu_4]_2$  [TeCl<sub>6</sub>] and 100 mM BMP-I for 300 s. A typical current transient for deposition under these conditions is shown in Figure S1. Te NWs can be observed to grow horizontally with a uniform distribution across the entire electrode. The wires appear to assemble into distinct units, clustering around a particular site and growing out and over the SiO<sub>2</sub> surface in multiple directions. This is likely to be the result of the gold pretreatment and nucleation pulse where, as described above, the Au nanoparticles act as preferential sites for the growth of Te. The clustering effect causes some wires to grow in an undesirable direction over the top of the electrodes, but the majority cross the insulating gap and form the electrical contact necessary for electrical measurements. Lateral growth from the wall of the TiN

substrate can also readily be observed with an ADF-STEM cross section, shown in Figure 3. The same image with EDS



**Figure 3.** Cross-sectional ADF-STEM image of one electrode showing lateral growth of Te NWs. Pt and carbon were deposited on top of the device as a protection layer during FIB sectioning.

mapping showing the elemental composition of each location is given in Figure S2. Oxygen signals can be seen to coincide with Te in the maps, this is likely due to the presence of a passivating surface oxide film, this could be studied in further detail with selected area electron diffraction (SAED) and X-ray photoelectron spectroscopy (XPS). The formation of bulk TeO<sub>x</sub> can excluded by the use of a nonaqueous solvent with negligible dissolved oxygen.

There is some variation in the length and diameter of the Te NWs, larger deposits appear needle-like in places with some branching and "feathering" (*e.g.*, Figure 2c), can also be observed. The NWs are typically in the range of  $1-7 \mu m$  in length and *ca.* 200 nm in diameter.

Attempts were made to image Te NWs that had grown directly from the Au nanoparticles to verify our hypothesis that the gold nanoparticles provided nucleation sites for Te NW growth. However, this was challenging, not only due to the small size of the gold nanoparticles, but also because the Te may grow over it and so block the Au from view, as indicated in Figure 2c. Nevertheless, it is occasionally possible to observe direct growth, such as in Figure S3, which shows SEM imaging and EDS maps of Te grown from a gold particle on the lateral growth electrode. It is possible for Te to nucleate and grow from sites other than the pretreated Au, however the SEM images in Figures 2 and S3 indicate that the majority grow from individual Au particles.

In summary, our preliminary studies show that the conditions for good growth of Te NWs across the insulating surface are as follows: a lateral growth substrate, use of BMP-I as the supporting electrolyte, pretreatment to form Au nanoparticles and application of a nucleation pulse prior to bulk deposition. Results for depositions in which one of these steps was omitted are shown in Figure S4, in each case nucleation is poor and minimal lateral growth of Te NWs is observed.

It is also important to emphasize that the effect of the Au pretreatment does not imply that the same results would be observed using an Au electrode in place of TiN. The Au pretreatment provides small islands of a more active metal relative to the TiN surrounding it and this, combined with the nucleation pulse, means that the Te will preferentially nucleate at those sites and then grow laterally. Nanowires are still formed without the pretreatment (*cf.* Figure S4), but this step asserts some control over the process and encourages growth

in a more ordered fashion, laterally across the insulator, rather than randomly.

Having established suitable conditions for Te NW growth, the wires were subjected to various characterization methods to further understand their physical properties. Figure 4 shows

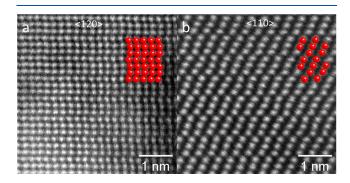


Figure 4. High resolution ADF-STEM images of electrodeposited Te NWs deposited with the optimized conditions, taken along the (a) < 120> and (b) < 110> directions, respectively. Structural models of bulk Te crystals projected along the same directions are overlaid (red), which confirm that the Te NWs adopt the structure of the bulk Te crystal.

high resolution ADF-STEM images taken from the Te NWs along either the <120> or the <110> direction. Structural models of bulk Te crystals projected along the same directions are also shown along with the images, which confirm at the atomic scale that the Te NWs have adopted the crystal structure of bulk Te. Typically X-ray diffraction can be used to assess the crystallinity of the material, however this was not suitable in the present work because of the limited sample size, hence ADF-STEM was used.

Figure 5a shows a representative EDS spectrum, confirming the presence of elemental Te, along with a representative Raman spectrum in 5b. No EDS signals corresponding to Cl or I are observed. O, Si and Ti are associated with the substrate and the C signal can be attributed to adventitious carbon. Crystalline bulk Te shows characteristic Raman peaks at 92, 120 and 141 cm<sup>-1</sup> corresponding to the  $E_1$ ,  $A_1$  and  $E_2$  Raman modes respectively.<sup>36</sup> These are each associated with bond bending, chain expansion and bond stretching, respectively. Here, a peak is observed at  $126 \text{ cm}^{-1}$ , which can be attributed to the  $A_1$  mode. A shoulder is then observed at 142 cm<sup>-1</sup>, associated with the  $E_2$  mode. A weak, broad feature also occurs at 104 cm<sup>-1</sup>, which corresponds to the  $E_1$  mode. A blueshift in the Te Raman peaks (particularly in the  $A_1$  mode) relative to bulk Te has been reported previously for nanostructured Te, and it has been suggested that this occurs because of a weakening of the van der Waals interactions between Te chains, and strengthened intrachain covalent interactions.<sup>13,30</sup> The  $E_2$  mode tends to shift to higher wavenumbers by a lesser extent than the  $A_1$ , possibly explaining why it morphs into a shoulder of the  $A_1$  peak in the spectrum in Figure 5. It also indicates that there is indeed a degree of nanostructuring in the Te NWs prepared in this work. Background Raman spectra of TiN and SiO<sub>2</sub> (Figure S5) confirming that they are Raman inactive within the range measured.

The response of the Te NWs to polarized light can give an indication of the presence of chirality, as shown in Figure S6 with images of unpolarized and cross-polarized light. By setting the polarizer and analyzer perpendicular to each other, the

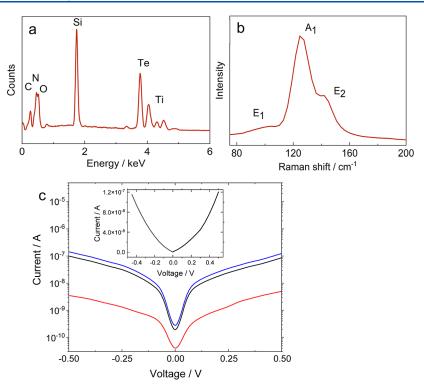


Figure 5. Physical and electronic characterization of Te NWs deposited under the optimized conditions showing (a) representative EDS spectrum, (b) Raman spectrum collected with a 532 nm laser, and (c) current–voltage sweep, where each color represents an individual device, inset is the linear scale showing one device.

light is polarized both vertically and horizontally (crosspolarized), and no light passes through. However, if an anisotropic *i.e.* chiral material is illuminated, the polarized light vector undergoes rotation within the sample, allowing a proportion of its wave vector to pass through the analyzer, resulting in observable features and a qualitative indication of the presence or absence of an anisotropic material. As can be seen, the Te NWs appear brighter with cross-polarized light, which could suggest the existence of chirality, as would be expected from the crystal structure of Te. Polarized light microscopy is typically employed to identify birefringence in crystals, and has been previously used to detect chirality in Te nanostructures,<sup>37</sup> where samples which were chiral in polarization dependent second-harmonic generation spectroscopy were also shown to be birefringent with polarized light microscopy.

The results of current-voltage IV sweep on the as-deposited Te NWs are displayed in Figure 5c for three different devices. The lateral growth electrodes, positioned at the bottom of the chips, are initially connected to a global contact where the potential for electrodeposition is applied. Afterward, the chips are cleaved into two-halves along the cleave zone before conducting electrical measurements. This step is essential to isolate the electrodes. The chip layout and methodology used in this study closely follow the designs established in our previous work.<sup>32</sup> The data shows that the Te NWs have grown across the SiO<sub>2</sub> insulating gap to form contacts between the two TiN nanoband electrodes, and therefore demonstrates the possibility of fabricating an electronic device without any further modification of the substrate. The current in different devices can vary, depending on the number and size of Te wires bridging the electrodes. The room temperature average resistivity is measured to be  $1.17-5.85 \times 10^3 \Omega$  cm. The

typical device dimensions used are as follows: channel length (L): 8  $\mu$ m, channel width (W): 200  $\mu$ m. Only a few studies have reported resistivity values for Te nanowires, ranging from around 10 to 50  $\Omega$  cm.<sup>38,39</sup> These reported values are indeed lower than those we are observing. This discrepancy can be attributed to several factors related to composition, crystal-linity, our device geometry and the specifics of the electro-deposition process. Further analysis and experimentation would be necessary to fully understand the implications of these parameters on the device functionality, but this highlights the ability of the nanoband electrodes to measure electrical responses directly from the electrodeposited Te NWs, without having to perform post deposition patterning or fabrication.

# 4. CONCLUSIONS

This work has presented a novel method for the fabrication of Te NWs from Au nanoparticles and across an insulator by electrodeposition onto TiN nanoband electrodes, without the need to use a template. The anisotropic nature of Te is exploited, and good nanowire growth can be achieved using a set of conditions designed to promote nucleation and lateral electrodeposition of Te. The protocol includes: a specifically designed lateral growth substrate, use of the supporting electrolyte BMP-I, a pretreatment of the TiN electrode surface with particles of Au by electrodeposition, and an initial nucleation pulse. The nature of the deposited Te NWs and the effect of the various experimental conditions were probed with SEM, while the presence of crystalline, elemental Te was confirmed with ADF-STEM, EDS and Raman spectroscopy. An added advantage of the lateral growth substrate is that electronic measurements could be made directly on the substrate, permitting study of the Te NWs as they were deposited, without the need for processing post hoc. The

formation of an electronic device was then demonstrated through current-voltage sweeps.

The key factor in determining the presence of lateral growth, and therefore the formation of Te NWs, seems to be the presence of BMP-I rather than  $[N^nBu_4]Cl$  in the electrolyte. It is possible that BMP-I is acting as a structure directing additive, controlling the morphology of the Te and promoting growth in a specific direction so causing the formation of nanowires. It is not presently clear why this is, but since the applied potential is likely to be more negative than the potential of zero charge (pzc), the double layer will be populated with the BMP cation. It is possible that this species adsorbs onto the electrode surface, or onto the depositing Te, and influences the direction of growth.

The development of this method provides a number of interesting opportunities for further work. In nanoparticle synthesis, a capping agent, such as polyvinylpyrrolidone (PVP), is often used to control the size and shape of the resulting particle. This may also be beneficial here, to increase the uniformity of the Te NW growth, as well as reducing the "feathering" that is sometimes observed. Furthermore, one of the most interesting features of Te NWs is their chirality, and the ability to have enantiomeric control during the electrodeposition process would be highly advantageous. This might be possible by using a chiral Te precursor or chiral additives. Finally, as described above, nanostructured Te is of interest as a transistor material and so future work will focus on fabricating transistors with the electrodeposited Te NWs.

# ASSOCIATED CONTENT

# Supporting Information

The Supporting Information is available free of charge at https://pubs.acs.org/doi/10.1021/acs.jpcc.4c05915.

Representative current transient for electrodeposition of Te nanowires including nucleation pulse and bulk deposition; STEM cross-section of electrodeposited Te nanowires with added EDS mapping; SEM image and EDS mapping indicating Te nanowires growing directly from an Au particle; SEM images showing the effect of removing each step in the deposition process which is required for good Te nanowire growth; Raman spectra of electrodeposited Te nanowires over a wider wavenumber range and background spectra of SiO<sub>2</sub> and TiN (PDF)

# AUTHOR INFORMATION

#### **Corresponding Authors**

- Nema M. Abdelazim School of Electronics and Computer Science, University of Southampton, Southampton SO17 1BJ, U.K.; Email: Nema.Abdelazim@soton.ac.uk
- Philip N. Bartlett School of Chemistry, University of Southampton, Southampton SO17 1BJ, U.K.; Email: P.N.Bartlett@soton.ac.uk

# Authors

Alexander W. Black – School of Chemistry, University of Southampton, Southampton SO17 1BJ, U.K.; Present Address: A.W.B. School of Chemistry, University of Bristol, Bristol, BS8 1TS; orcid.org/0000-0003-3001-3083

- Yasir J. Noori School of Electronics and Computer Science, University of Southampton, Southampton SO17 1BJ, U.K.; orcid.org/0000-0001-5285-8779
- **Yisong Han** Department of Physics, University of Warwick, Coventry CV4 7AL, U.K.
- Nikolay Zhelev School of Chemistry, University of Southampton, Southampton SO17 1BJ, U.K.
- Shibin Thomas School of Chemistry, University of Southampton, Southampton SO17 1BJ, U.K.
- Wenjian Zhang School of Chemistry, University of Southampton, Southampton SO17 1BJ, U.K.
- Gillian Reid School of Chemistry, University of Southampton, Southampton SO17 1BJ, U.K.; • orcid.org/ 0000-0001-5349-3468
- Richard Beanland Department of Physics, University of Warwick, Coventry CV4 7AL, U.K.; o orcid.org/0000-0003-1749-4134
- **C. H. Kees de Groot** School of Electronics and Computer Science, University of Southampton, Southampton SO17 1BJ, U.K.

Complete contact information is available at: https://pubs.acs.org/10.1021/acs.jpcc.4c05915

# **Author Contributions**

AWB: Conceptualization, Methodology, Validation, Formal analysis, Investigation, Data curation, Writing – original draft, Visualization. NA: Conceptualization, Methodology, Resources, Validation, Formal analysis, Investigation, Writing – review and editing. YH: Methodology, Formal analysis. NZ: Methodology, Resources. YJN: Conceptualization, Methodology, Resources. WZ: Resources. ST: Resources. GR: Methodology, Resources, Writing – review and editing, Supervision. RB: Methodology, Resources, Validation. KdG: Conceptualization, Methodology, Resources, Validation, Writing – review and editing. PNB: Conceptualization, Methodology, Validation, Formal analysis, Writing – review and editing, Supervision, Funding acquisition, Project administration.

#### Notes

The authors declare no competing financial interest.

# ACKNOWLEDGMENTS

This work was supported by the EPSRC through the Advanced Devices by Electroplating program grant (ADEPT; EP/ N035437/1), and EPSRC grant EP/V062689/1. Scanning Electron Microscopy was supported by the EPSRC grant EP/ V007629/1. YJN acknowledges funding from the Zepler Institute under the stimulus fund scheme. All data supporting this study are openly available from the University of Southampton repository at 10.5258/SOTON/D2877.

# REFERENCES

(1) Lu, W.; Lieber, C. M. Semiconductor Nanowires. J. Phys. D Appl. Phys. 2006, 39, R387–R406.

<sup>(2)</sup> Jia, C.; Lin, Z.; Huang, Y.; Duan, X. Nanowire Electronics: From Nanoscale to Macroscale. *Chem. Rev.* **2019**, *119*, 9074–9135.

<sup>(3)</sup> Vasen, T.; Ramvall, P.; Afzalian, A.; Doornbos, G.; Holland, M.; Thelander, C.; Dick, K. A.; Wernersson, L. E.; Passlack, M. Vertical Gate-All-Around Nanowire GaSb-InAs Core-Shell n-Type Tunnel FETs. *Sci. Rep.* **2019**, *9*, 202–209.

<sup>(4)</sup> Doornbos, G.; Holland, M.; Vellianitis, G.; Van Dal, M. J. H.; Duriez, B.; Oxland, R.; Afzalian, A.; Chen, T. K.; Hsieh, G.; Passlack, M.; et al. High-Performance InAs Gate-All-around Nanowire

MOSFETs on 300 Mm Si Substrates. IEEE J. Electron Devices Soc. 2016, 4, 253–259.

(5) Alhodaib, A.; Noori, Y. J.; Carrington, P. J.; Sanchez, A. M.; Thompson, M. D.; Young, R. J.; Krier, A.; Marshall, A. R. J. Room-Temperature Mid-Infrared Emission from Faceted InAsSb Multi Quantum Wells Embedded in InAs Nanowires. *Nano Lett.* **2018**, *18*, 235–240.

(6) Zhou, H.; Zhang, C.; Gao, A.; Shi, E.; Guo, Y. Patterned Growth of Two-Dimensional Atomic Layer Semiconductors. *Chem. Commun.* **2024**, *60*, 943–955.

(7) Zhao, C.; Tan, C.; Lien, D. H.; Song, X.; Amani, M.; Hettick, M.; Nyein, H. Y. Y.; Yuan, Z.; Li, L.; Scott, M. C.; et al. Evaporated Tellurium Thin Films for P-Type Field-Effect Transistors and Circuits. *Nat. Nanotechnol.* **2020**, *15*, 53–58.

(8) Zhu, T.; Zhang, Y.; Wei, X.; Jiang, M.; Xu, H. The Rise of Two-Dimensional Tellurium for next-Generation Electronics and Optoelectronics. *Front. Phys.* **2023**, *18*, 33601.

(9) Keller, R.; Holzapfel, W. B.; Schulz, H. Effect of Pressure on the Atom Positions in Se and Te. *Phys. Rev. B* **1977**, *16*, 4404–4412.

(10) Kramer, A.; Van de Put, M. L.; Hinkle, C. L.; Vandenberghe, W. G. Tellurium as a Successor of Silicon for Extremely Scaled Nanowires: A First-Principles Study. *npj 2D Mater. Appl.* **2020**, *4*, 10–18.

(11) Dasika, P.; Samantaray, D.; Murali, K.; Abraham, N.; Watanbe, K.; Taniguchi, T.; Ravishankar, N.; Majumdar, K. Contact-Barrier Free, High Mobility, Dual-Gated Junctionless Transistor Using Tellurium Nanowire. *Adv. Funct. Mater.* **2021**, *31*, 2006278.

(12) Calavalle, F.; Suárez-Rodríguez, M.; Martín-García, B.; Johansson, A.; Vaz, D. C.; Yang, H.; Maznichenko, I. V.; Ostanin, S.; Mateo-Alonso, A.; Chuvilin, A.; et al. Gate-Tuneable and Chirality-Dependent Charge-to-Spin Conversion in Tellurium Nanowires. *Nat. Mater.* **2022**, *21*, 526–532.

(13) Wang, Y.; Qiu, G.; Wang, R.; Huang, S.; Wang, Q.; Liu, Y.; Du, Y.; Goddard, W. A.; Kim, M. J.; Xu, X.; et al. Field-Effect Transistors Made from Solution-Grown Two-Dimensional Tellurene. *Nat. Electron.* **2018**, *1*, 228–236.

(14) Zhou, G.; Addou, R.; Wang, Q.; Honari, S.; Cormier, C. R.; Cheng, L.; Yue, R.; Smyth, C. M.; Laturia, A.; Kim, J.; et al. High-Mobility Helical Tellurium Field-Effect Transistors Enabled by Transfer-Free, Low-Temperature Direct Growth. *Adv. Mater.* **2018**, 30 (36), 1–7.

(15) Qiu, G.; Charnas, A.; Niu, C.; Wang, Y.; Wu, W.; Ye, P. D. The Resurrection of Tellurium as an Elemental Two-Dimensional Semiconductor. *npj 2D Mater. Appl.* **2022**, *6*, 17.

(16) Du, Y.; Qiu, G.; Wang, Y.; Si, M.; Xu, X.; Wu, W.; Ye, P. D. One-Dimensional van Der Waals Material Tellurium: Raman Spectroscopy under Strain and Magneto-Transport. *Nano Lett.* **2017**, *17*, 3965–3973.

(17) He, Z.; Yang, Y.; Liu, J. W.; Yu, S. H. Emerging Tellurium Nanostructures: Controllable Synthesis and Their Applications. *Chem. Soc. Rev.* **201**7, *46*, 2732–2753.

(18) Jaafar, A. H.; Meng, L.; Noori, Y. J.; Zhang, W.; Han, Y.; Beanland, R.; Smith, D. C.; Reid, G.; de Groot, K.; Huang, R.; et al. Electrodeposition of GeSbTe-Based Resistive Switching Memory in Crossbar Arrays. J. Phys. Chem. C 2021, 125, 26247–26255.

(19) Huang, R.; Kissling, G. P.; Kashtiban, R.; Noori, Y. J.; Cicvarić, K.; Zhang, W.; Hector, A. L.; Beanland, R.; Smith, D. C.; Reid, G.; et al. Towards a 3D GeSbTe Phase Change Memory with Integrated Selector by Non-Aqueous Electrodeposition. *Faraday Discuss.* **2019**, 213, 339–355.

(20) Cicvarić, K.; Meng, L.; Newbrook, D. W.; Huang, R.; Ye, S.; Zhang, W.; Hector, A. L.; Reid, G.; Bartlett, P. N.; De Groot, C. H. Thermoelectric Properties of Bismuth Telluride Thin Films Electrodeposited from a Nonaqueous Solution. *ACS Omega* **2020**, *5*, 14679–14688.

(21) Noori, Y. J.; Meng, L.; Jaafar, A. H.; Zhang, W.; Kissling, G. P.; Han, Y.; Abdelazim, N.; Alibouri, M.; Leblanc, K.; Zhelev, N.; et al. Phase-Change Memory by GeSbTe Electrodeposition in Crossbar Arrays. *ACS Appl. Electron. Mater.* **2021**, *3*, 3610–3618. (22) Zhao, A. W.; Ye, C. H.; Meng, G. W.; Zhang, L. D.; Ajayan, P. M. Tellurium Nanowire Arrays Synthesized by Electrochemical and Electrophoretic Deposition. *J. Mater. Res.* **2003**, *18*, 2318–2322.

(23) Zhao, A.; Zhang, L.; Pang, Y.; Ye, C. Ordered Tellurium Nanowire Arrays and Their Optical Properties. *Appl. Phys. A Mater. Sci. Process.* **2005**, *80*, 1725–1728.

(24) Kumar, N.; Kumar, R.; Kumar, S.; Chakarvarti, S. K. Optical and Electrical Studies of Vertically Oriented Tellurium Nanowire Arrays Produced by Template Electrodeposition. *J. Electron. Mater.* **2015**, *44*, 2939–2945.

(25) Bartlett, P. N.; Cook, D. A.; Hasan, M. M.; Hector, A. L.; Marks, S.; Naik, J.; Reid, G.; Sloan, J.; Smith, D. C.; Spencer, J.; et al. Supercritical Fluid Electrodeposition, Structural and Electrical Characterisation of Tellurium Nanowires. *RSC Adv.* **2017**, *7*, 40720–40726.

(26) She, G.; Shi, W.; Zhang, X.; Wong, T.; Cai, Y.; Wang, N. Template-Free Electrodeposition of One-Dimensional Nanostructures of Tellurium. *Cryst. Growth Des.* **2009**, *9*, 663–666.

(27) Al-Salman, R.; Sommer, H.; Brezesinski, T.; Janek, J. Template-Free Electrochemical Synthesis of High Aspect Ratio Sn Nanowires in Ionic Liquids: A General Route to Large-Area Metal and Semimetal Nanowire Arrays? *Chem. Mater.* **2015**, *27*, 3830–3837.

(28) Thiebaud, L.; Legeai, S.; Ghanbaja, J.; Stein, N. Electrodeposition of High Aspect Ratio Single Crystalline Tellurium Nanowires from Piperidinium-Based Ionic Liquid. *Electrochim. Acta* **2016**, *222*, 528–534.

(29) Perry, S. C.; White, J.; Nandhakumar, I. Template-Free Electrochemical Deposition of Tellurium Nanowires with Eutectic Solvents. *Electrochim. Acta* **2023**, *439*, 141674.

(30) Naqi, M.; Choi, K. H.; Yoo, H.; Chae, S.; Kim, B. J.; Oh, S.; Jeon, J.; Wang, C.; Liu, N.; Kim, S.; et al. Nanonet: Low-Temperature-Processed Tellurium Nanowire Network for Scalable p-Type Field-Effect Transistors and a Highly Sensitive Photo-transistor Array. NPG Asia Mater. **2021**, *13*, 46.

(31) Bartlett, P. N.; Cook, D.; De Groot, C. H.; Hector, A. L.; Huang, R.; Jolleys, A.; Kissling, G. P.; Levason, W.; Pearce, S. J.; Reid, G. Non-Aqueous Electrodeposition of p-Block Metals and Metalloids from Halometallate Salts. *RSC Adv.* **2013**, *3*, 15645–15654.

(32) Abdelazim, N. M.; Noori, Y. J.; Thomas, S.; Greenacre, V. K.; Han, Y.; Smith, D. E.; Piana, G.; Zhelev, N.; Hector, A. L.; Beanland, R.; Reid, G.; Bartlett, P. N.; De Groot, C. H. Lateral Growth of MoS2 2D Material Semiconductors Over an Insulator Via Electrodeposition. *Adv. Electron. Mater.* **2021**, *7*, 2100419.

(33) Kissling, G. P.; Aziz, M.; Lodge, A. W.; Zhang, W.; Alibouri, M.; Huang, R.; Hector, A. L.; Reid, G.; de Groot, C.; Beanland, R.; Bartlett, P. N.; Smith, D. C. Electrodeposition of Crystalline HgTe from a Non-Aqueous Plating Bath. *J. Electrochem. Soc.* **2018**, *165*, D802–D807.

(34) Meng, L.; Cicvarić, K.; Hector, A. L.; De Groot, C. H.; Bartlett, P. N. Electrodeposition of Bismuth Telluride from a Weakly Coordinating, Non-Aqueous Solution. *J. Electroanal. Chem.* **2019**, 839, 134–140.

(35) Ueda, M.; Dietz, H.; Anders, A.; Kneppe, H.; Meixner, A.; Plieth, W. Double-Pulse Technique as an Electrochemical Tool for Controlling the Preparation of Metallic Nanoparticles. *Electrochim. Acta* **2002**, *48*, 377–386.

(36) Pine, A. S.; Dresselhaus, G. Raman Spectra and Lattice Dynamics of Tellurium. *Phys. Rev. B* 1971, *4*, 356–371.

(37) Zhu, H.; Fan, L.; Wang, Y.; Wang, K.; Liu, H.; Zhang, J.; Yan, S. Birefringence after Tellurium Nanosheets and Copper Intercalation. *Symmetry* **2023**, *15*, 1885.

(38) Liu, H.; Zhu, H.; Chen, L.; Shen, Y.; Wang, K.; Jin, Z.; Tan, C. L.; Shi, Y.; Yan, S. Enhanced Intrinsic Electrical Properties in Twisted Tellurium Nanowires. *ACS Appl. Electron. Mater.* **2024**, *6*, 5879–5884.

(39) Kumar, N.; Kumar, R.; Kumar, S.; Chakarvarti, S. K. Influence of Ion Beam Irradiation Induced Defects on the Structural, Optical and Electrical Properties of Tellurium Nanowires. *Mater. Chem. Phys.* **2016**, *183*, 165–172.